

Syed Asad Alam

List of Publications by Year in descending order

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#	ARTICLE	IF	CITATIONS
1	On the RTL Implementation of FINN Matrix Vector Unit. Transactions on Embedded Computing Systems, 2023, 22, 1-27.	2.9	3
2	Low-precision Logarithmic Number Systems. Transactions on Architecture and Code Optimization, 2021, 18, 1-25.	2.0	8
3	Improved Particle Filter Resampling Architectures. Journal of Signal Processing Systems, 2020, 92, 555-568.	2.1	10
4	Multimodal fringe detection for a self-mixing interferometry-based vibration sensor. Applied Optics, 2020, 59, 11342.	1.8	1
5	Beyond Base-2 Logarithmic Number Systems (WiP Paper). , 2020, , .		0
6	Classification of laser modality for a self-mixing interferometric sensor. Applied Optics, 2020, 59, 11351.	1.8	1
7	On the Implementation of Time-Multiplexed Frequency-Response Masking Filters. IEEE Transactions on Signal Processing, 2016, 64, 3933-3944.	5.3	6
8	Generalized division-free architecture and compact memory structure for resampling in particle filters. , 2015, , .		0
9	Design of Finite Word Length Linear-Phase FIR Filters in the Logarithmic Number System Domain. VLSI Design, 2014, 2014, 1-14.	0.5	9
10	Implementation of time-multiplexed sparse periodic FIR filters for FRM on FPGAs. , 2011, , .		3
11	Implementation of narrow-band frequency-response masking for efficient narrow transition band FIR filters on FPGAs. , 2011, , .		4
12	4k-point FFT algorithms based on optimized twiddle factor multiplication for FPGAs. , 2010, , .		8