

Vishnu Unnikrishnan

List of Publications by Year in descending order

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#	ARTICLE	IF	CITATIONS
1	Fully Digital On-Chip Wideband Background Calibration for Channel Mismatches in Time-Interleaved Time-Based ADCs. IEEE Solid-State Circuits Letters, 2022, 5, 9-12.	2.0	1
2	Energy-Efficient Cyclic-Coupled Ring Oscillator With Delay-Based Injection Locking. IEEE Transactions on Circuits and Systems II: Express Briefs, 2022, 69, 3709-3713.	3.0	1
3	Data Conversion With Subgate-Delay Time Resolution Using Cyclic-Coupled Ring Oscillators. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2021, 29, 203-214.	3.1	4
4	A 100â€“750 MS/s 11-Bit Time-to-Digital Converter With Cyclic-Coupled Ring Oscillator. IEEE Access, 2021, 9, 48147-48156.	4.2	10
5	Resilient flow control for wireless data streaming in inductively coupled medical implants. Microprocessors and Microsystems, 2020, 72, 102905.	2.8	10
6	Time-Based Sensor Interface for Dopamine Detection. IEEE Transactions on Circuits and Systems I: Regular Papers, 2020, 67, 3284-3296.	5.4	3
7	A 0.6â€“4.0 GHz RF-Resampling Beamforming Receiver With Frequency-Scaling True-Time-Delays up to Three Carrier Cycles. IEEE Solid-State Circuits Letters, 2020, 3, 234-237.	2.0	11
8	Injection Locking of Ring Oscillators with Digitally Controlled Delay Modulation. , 2020, , .		1
9	A 2â€“5.5 GHz Beamsteering Receiver IC With 4-Element Vivaldi Antenna Array. IEEE Transactions on Microwave Theory and Techniques, 2020, 68, 3852-3860.	4.6	3
10	True-Time-Delay Beamforming Receiver With RF Re-Sampling. IEEE Transactions on Circuits and Systems I: Regular Papers, 2020, 67, 4457-4469.	5.4	11
11	A Delay-Based LO Phase-Shifting Generator for a 2-5GHz Beamsteering Receiver in 28nm CMOS. , 2019, , .		2
12	A Sensor Interface for Neurochemical Signal Acquisition. , 2019, , .		1
13	A 3-43ps time-delay cell for LO phase-shifting in 1.5-6.5GHz beamsteering receiver. , 2018, , .		1
14	A VCO-based ADC with Relaxation Oscillator for Biomedical Applications. , 2018, , .		3
15	Ring Counters as Phase Accumulator in VCO-Based ADCs. , 2018, , .		4
16	A Low-Power Hardware Stack for Continuous Data Streaming from Telemetry Implants. , 2018, , .		0
17	A Configurable Hysteresis Comparator for Asynchronous Sigma-Delta Modulators. , 2018, , .		0
18	Mitigation of Sampling Errors in VCO-Based ADCs. IEEE Transactions on Circuits and Systems I: Regular Papers, 2017, 64, 1730-1739.	5.4	14

#	ARTICLE	IF	CITATIONS
19	Linearization of VCO-based ADCs using asynchronous sigma-delta modulation. , 2016, , .		1
20	Design of a VCO-based ADC in 28 nm CMOS. , 2016, , .		2
21	VCO-based ADCs for IoT applications. , 2016, , .		4
22	Mixed-Signal Design Using Digital CAD. , 2016, , .		4
23	A NAND gate based standard cell VCO for use in synthesizable ADCs. , 2015, , .		3
24	A fully synthesized all-digital VCO-based analog-to-digital converter. , 2015, , .		4
25	Linearization of synthesizable VCO-based ADCs using delta modulation. , 2015, , .		2
26	Time-Mode Analog-to-Digital Conversion Using Standard Cells. IEEE Transactions on Circuits and Systems I: Regular Papers, 2014, 61, 3348-3357.	5.4	42