

# Sreenivasulu Polineni

## List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/3055539/publications.pdf>

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8  
papers

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2258059

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docs citations

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citing authors

#	ARTICLE	IF	CITATIONS
1	A fully differential switchedâ€capacitor integrator based programmable resolution hybrid ADC architecture for biomedical applications. IET Circuits, Devices and Systems, 2021, 15, 141-155.	1.4	4
2	A Switched Capacitor-Based SAR ADC Employing a Passive Reference Charge Sharing and Charge Accumulation Technique. Circuits, Systems, and Signal Processing, 2020, 39, 5352-5370.	2.0	7
3	A 10-Bit Differential Ultra-Low-Power SAR ADC with an Enhanced MSB Capacitor-Split Switching Technique. Arabian Journal for Science and Engineering, 2019, 44, 2345-2353.	3.0	8
4	A 0.3â€V, 56â€dB DR, 100â€Hz fourth order low-pass filter for ECG acquisition system. Microelectronics Journal, 2019, 94, 104652.	2.0	16
5	Design of High Resolution Delta Sigma Modulator in 180 nm CMOS technology. , 2019, , .		0
6	A Three-Stage Operational Transconductance Amplifier for Delta Sigma Modulator. , 2018, , .		2
7	91dB Dynamic Range 9.5nW Low Pass Filter for Bio-Medical Applications. , 2018, , .		2
8	8-bit Nano watt level crossing ADC for bio-medical application. , 2015, , .		3