Zhiyu Chen

List of Publications by Year
in descending order

5 papers	63 citations	2682572 2 h-index	2917675 2 g-index
5 all docs	5 docs citations	5 times ranked	38 citing authors

#	Article	IF	Citations
1	DCT-RAM: A Driver-Free Process-In-Memory 8T SRAM Macro with Multi-Bit Charge-Domain Computation and Time-Domain Quantization. , 2022, , .		5
2	CAP-RAM: A Charge-Domain In-Memory Computing 6T-SRAM for Accurate and Precision-Programmable CNN Inference. IEEE Journal of Solid-State Circuits, 2021, 56, 1924-1935.	5.4	46
3	MC^2-RAM: An In-8T-SRAM Computing Macro Featuring Multi-Bit Charge-Domain Computing and ADC-Reduction Weight Encoding. , 2021, , .		6
4	A High-Speed Successive-Cancellation Decoder for Polar Codes Using Approximate Computing. IEEE Transactions on Circuits and Systems II: Express Briefs, 2019, 66, 227-231.	3.0	6
5	Reduced complexity list polar decoder with an improved path pruning scheme. , 2017, , .		0