

Vivekanandan Subburaj

List of Publications by Year in descending order

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14
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84
citing authors

#	ARTICLE	IF	CITATIONS
1	Analysis of DFIG-STATCOM P2P control action using simulated annealing techniques. Heliyon, 2022, 8, e09008.	3.2	4
2	Revisited SCC Equivalent Resistance High-Frequency Limit Accounting for Stray Inductance Effect. IEEE Journal of Emerging and Selected Topics in Power Electronics, 2021, 9, 638-646.	5.4	2
3	Performance Enhancement of PI-Controller Using SVM for DFIG-Grid Interconnected System. , 2021, , .		4
4	Novel Gaussian flower pollination algorithm with IoT for unit price prediction in peer-to-peer energy trading market. Energy Reports, 2021, 7, 8265-8276.	5.1	9
5	High efficiency two-phase switched-capacitor converter with seven distinct negative voltage ratios for power saving applications. International Journal of Electronics Letters, 2020, 8, 298-303.	1.2	0
6	Low power digital on-chip implementation of Fibonacci non-magnetic switching converter for IoT applications. International Journal of Power Electronics, 2020, 12, 332.	0.2	0
7	High energy reduced graphene oxide/vanadium Pentoxide/polyaniline hybrid supercapacitor for power backup and switched capacitor converters. Journal of Colloid and Interface Science, 2019, 545, 82-93.	9.4	38
8	Investigation of a family of dualâ€œoutput coupled/decoupled switched capacitor converter for lowâ€œpower applications. IET Circuits, Devices and Systems, 2019, 13, 352-360.	1.4	1
9	Two Phase (Reconfigurable) Inverting Switched Capacitor Converter for Micro Power Applications and its Accurate Equivalent Resistance Calculation. IEEE Transactions on Circuits and Systems II: Express Briefs, 2019, 66, 1446-1450.	3.0	3
10	Reconfigurable highly efficient CMOSâ€œbased dual input variable output switched capacitor converter for low power applications. Electronics Letters, 2018, 54, 89-91.	1.0	7
11	An Algorithm Steps to Solve Coupled Case for Dual Input Dual Output SCC. , 2018, , .		1
12	Design, modeling and analysis of a new dual input-output switched capacitor converter. , 2017, , .		9
13	Sub-period interleaved Fibonacci switched capacitor converter. , 2016, , .		2
14	Design of series, $F(s) = \frac{1}{s^2 + 2\zeta\omega_n s + \omega_n^2}$ for the denominators (1, 2, \dots) of switched capacitor converter. , 2016, , .		3