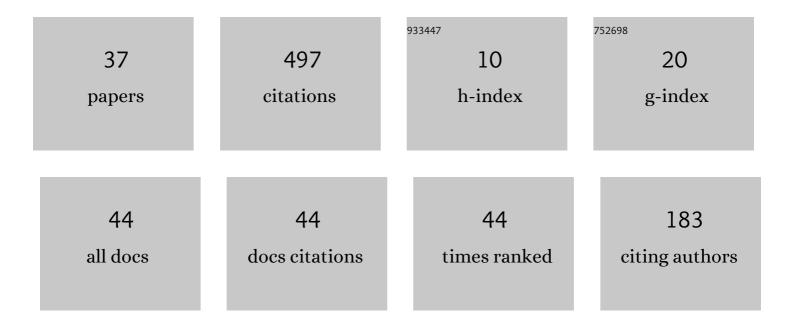
Pedro Lopez-Garcia

List of Publications by Year in descending order

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| # | Article | IF | CITATIONS |
|----|--|-------------------|------------|
| 1 | Integrated program debugging, verification, and optimization using abstract interpretation (and the) Tj ETQq1 | 1 0.784314 1.9 | rgat /Over |
| 2 | An overview of Ciao and its design philosophy. Theory and Practice of Logic Programming, 2012, 12, 219-252. | 1.5 | 85 |
| 3 | User-Definable Resource Bounds Analysis for Logic Programs. , 2007, , 348-363. | | 30 |
| 4 | Energy Consumption Analysis of Programs Based on XMOS ISA-Level Models. Lecture Notes in Computer Science, 2014, , 72-90. | 1.3 | 27 |
| 5 | Integrating Software Testing and Run-Time Checking in an Assertion Verification Framework. Lecture Notes in Computer Science, 2009, , 281-295. | 1.3 | 27 |
| 6 | Resource Usage Analysis of Logic Programs via Abstract Interpretation Using Sized Types. Theory and Practice of Logic Programming, 2014, 14, 739-754. | 1.5 | 21 |
| 7 | Program Development Using Abstract Interpretation (And the Ciao System Preprocessor). Lecture Notes in Computer Science, 2003, , 127-152. | 1.3 | 18 |
| 8 | Interval-based resource usage verification by translation into Horn clauses and an application to energy consumption. Theory and Practice of Logic Programming, 2018, 18, 167-223. | 1.5 | 16 |
| 9 | Inferring Parametric Energy Consumption Functions at Different Software Levels: ISA vs. LLVM IR. Lecture Notes in Computer Science, 2016, , 81-100. | 1.3 | 16 |
| 10 | ENTRA: Whole-systems energy transparency. Microprocessors and Microsystems, 2016, 47, 278-286. | 2.8 | 13 |
| 11 | Stochastic vs. deterministic evolutionary algorithm-based allocation and scheduling for XMOS chips. Neurocomputing, 2015, 150, 82-89. | 5.9 | 10 |
| 12 | Multivariant Non-failure Analysis via Standard Abstract Interpretation. Lecture Notes in Computer Science, 2004, , 100-116. | 1.3 | 10 |
| 13 | Determinacy Analysis for Logic Programs Using Mode and Type Information. Lecture Notes in Computer Science, 2005, , 19-35. | 1.3 | 9 |
| 14 | Abstraction carrying code and resource-awareness. , 2005, , . | | 9 |
| 15 | Automatic Inference of Determinacy and Mutual Exclusion for Logic Programs Using Mode and Type Analyses. New Generation Computing, 2010, 28, 177-206. | 3.3 | 9 |
| 16 | Cost Analysis of Smart Contracts Via Parametric Resource Analysis. Lecture Notes in Computer Science, 2020, , 7-31. | 1.3 | 9 |
| 17 | A general framework for static profiling of parametric resource usage. Theory and Practice of Logic Programming, 2016, 16, 849-865. | 1.5 | 8 |
| 18 | Combining Static Analysis and Profiling for Estimating Execution Times. Lecture Notes in Computer Science, 2006, , 140-154. | 1.3 | 7 |

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| # | Article | IF | CITATIONS |
|----|--|-----|-----------|
| 19 | Static Performance Guarantees for Programs with Runtime Checks. , 2018, , . | | 6 |
| 20 | Computing Abstract Distances in Logic Programs. Lecture Notes in Computer Science, 2020, , 57-72. | 1.3 | 5 |
| 21 | A Transformational Approach to Parametric Accumulated-Cost Static Profiling. Lecture Notes in Computer Science, 2016, , 163-180. | 1.3 | 5 |
| 22 | Interval-Based Resource Usage Verification: Formalization and Prototype. Lecture Notes in Computer Science, 2012, , 54-71. | 1.3 | 5 |
| 23 | Abstract Verification and Debugging of Constraint Logic Programs. Lecture Notes in Computer Science, 2003, , 1-14. | 1.3 | 4 |
| 24 | An Integrated Approach to Assertion-Based Random Testing in Prolog. Lecture Notes in Computer Science, 2020, , 159-176. | 1.3 | 4 |
| 25 | Using Combined Static Analysis and Profiling for Logic Program Execution Time Estimation. Lecture Notes in Computer Science, 2006, , 431-432. | 1.3 | 3 |
| 26 | A General Framework for Static Cost Analysis of Parallel Logic Programs. Lecture Notes in Computer Science, 2020, , 19-35. | 1.3 | 3 |
| 27 | CLP projection for constraint handling rules. , 2011, , . | | 2 |
| 28 | Energy Efficient Allocation and Scheduling for DVFS-enabled Multicore Environments using a Multiobjective Evolutionary Algorithm. , 2015, , . | | 2 |
| 29 | An evolutionary scheduling approach for trading-off accuracy vs. verifiable energy in multicore processors. Logic Journal of the IGPL, 2017, 25, 1006-1019. | 1.5 | 2 |
| 30 | A Practical Approach for Energy Efficient Scheduling in Multicore Environments by Combining Evolutionary and YDS Algorithms with Faster Energy Estimation. IFIP Advances in Information and Communication Technology, 2015, , 478-493. | 0.7 | 2 |
| 31 | Inferring Energy Bounds via Static Program Analysis and Evolutionary Modeling of Basic Blocks. Lecture Notes in Computer Science, 2018, , 54-72. | 1.3 | 2 |
| 32 | VeriFly: <i>On-the-fly Assertion Checking via Incrementality</i> . Theory and Practice of Logic Programming, 2021, 21, 768-784. | 1.5 | 2 |
| 33 | Testing Your (Static Analysis) Truths. Lecture Notes in Computer Science, 2021, , 271-292. | 1.3 | 1 |
| 34 | Regular Path Clauses and Their Application in Solving Loops. Electronic Proceedings in Theoretical Computer Science, EPTCS, 0, 344, 22-35. | 0.8 | 0 |
| 35 | Genetic Algorithm-Based Allocation and Scheduling for Voltage and Frequency Scalable XMOS Chips. Lecture Notes in Computer Science, 2013, , 401-410. | 1.3 | 0 |
| 36 | Energy Consumption Analysis and Verification by Transformation into Horn Clauses and Abstract Interpretation. Electronic Proceedings in Theoretical Computer Science, EPTCS, 0, 253, 4-6. | 0.8 | 0 |

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| 37 | A general framework for static profiling of parametric resource usage – CORRIGENDUM. Theory and Practice of Logic Programming, 2021, 21, 291-291. | 1.5 | 0 |