

Naveed Khan Baloch

List of Publications by Year in descending order

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16
papers

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citations

1305906

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docs citations

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234
citing authors

#	ARTICLE	IF	CITATIONS
1	An efficient and cost effective application mapping for network-on-chip using Andean condor algorithm. Journal of Network and Computer Applications, 2022, 200, 103319.	5.8	7
2	An energy efficient and low overhead fault mitigation technique for internet of thing edge devices reliable onâ€œchip communication. Software - Practice and Experience, 2021, 51, 2393-2410.	2.5	9
3	An Optimized Nature-Inspired Metaheuristic Algorithm for Application Mapping in 2D-NoC. Sensors, 2021, 21, 5102.	2.1	9
4	Supervised Machine Learning based Fast Hand Gesture Recognition and Classification Using Electromyography (EMG) Signals. , 2021, , .		7
5	Application Mapping Using Cuckoo Search Optimization With LÃ©vy Flight for NoC-Based System. IEEE Access, 2021, 9, 141778-141789.	2.6	16
6	Brain Tumor Detection based on Magnetic Resonance Imaging Analysis Using Segmentation, Thresholding and Morphological Operations. , 2021, , .		2
7	Fault-Tolerant Network-On-Chip Router Architecture Design for Heterogeneous Computing Systems in the Context of Internet of Things. Sensors, 2020, 20, 5355.	2.1	7
8	Impact of Feature Selection Algorithm on Speech Emotion Recognition Using Deep Convolutional Neural Network. Sensors, 2020, 20, 6008.	2.1	64
9	Savior: A Reliable Fault Resilient Router Architecture for Network-on-Chip. Electronics (Switzerland), 2020, 9, 1783.	1.8	5
10	Improving Mispronunciation Detection of Arabic Words for Non-Native Learners Using Deep Convolutional Neural Network Features. Electronics (Switzerland), 2020, 9, 963.	1.8	25
11	NoCGuard: A Reliable Network-on-Chip Router Architecture. Electronics (Switzerland), 2020, 9, 342.	1.8	11
12	Performance Evaluation of Application Mapping Approaches for Network-on-Chip Designs. IEEE Access, 2020, 8, 63607-63631.	2.6	38
13	Incorporating Noise Robustness in Speech Command Recognition by Noise Augmentation of Training Data. Sensors, 2020, 20, 2326.	2.1	31
14	Protector: A Permanent Fault Resilient Router Architecture for Network on Chip. Mehran University Research Journal of Engineering and Technology, 2020, 39, 686-704.	0.3	0
15	Defender: A Low Overhead and Efficient Fault-Tolerant Mechanism for Reliable on-Chip Router. IEEE Access, 2019, 7, 142843-142854.	2.6	9
16	Fingerprint verification over the network and its application in attendance management. , 2010, , .		6