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List of Publications by Year in descending order

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Version: 2024-02-01

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papers

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citations

1478505

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1872680

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all docs

13
docs citations

13
times ranked

112
citing authors

#	ARTICLE	IF	CITATIONS
1	You Cannot Improve What You Do not Measure. ACM Transactions on Reconfigurable Technology and Systems, 2018, 11, 1-23.	2.5	42
2	Embracing Diversity: Enhanced DSP Blocks for Low-Precision Deep Learning on FPGAs. , 2018, , .		39
3	COFFE 2. ACM Transactions on Reconfigurable Technology and Systems, 2019, 12, 1-27.	2.5	32
4	Interconnect Solutions for Virtualized Field-Programmable Gate Arrays. IEEE Access, 2018, 6, 10497-10507.	4.2	20
5	FPGA Logic Block Architectures for Efficient Deep Learning Inference. ACM Transactions on Reconfigurable Technology and Systems, 2020, 13, 1-34.	2.5	19
6	The Costs of Confidentiality in Virtualized FPGAs. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2019, 27, 2272-2283.	3.1	18
7	Don't Forget the Memory. , 2017, , .		16
8	Quantifying and mitigating the costs of FPGA virtualization. , 2017, , .		14
9	Automatic circuit design and modelling for heterogeneous FPGAs. , 2017, , .		13
10	Fine-Grained Architecture in Dark Silicon Era for SRAM-Based Reconfigurable Devices. IEEE Transactions on Circuits and Systems II: Express Briefs, 2014, 61, 798-802.	3.0	9
11	Math Doesn't Have to be Hard. , 2019, , .		9
12	High density, low energy, magnetic tunnel junction based block RAMs for memory-rich FPGAs. , 2016, , .		8
13	Improving Confidentiality in Virtualized FPGAs. , 2018, , .		5