

Mary Hickson

List of Publications by Citations

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The third column is the impact factor (IF) of the journal, and the fourth column is the number of citations of the article.

33
papers

97
citations

5
h-index

8
g-index

49
ext. papers

135
ext. citations

1.3
avg, IF

2.7
L-index

#	Paper	IF	Citations
33	Enhancement of test data compression with multistage encoding. <i>The Integration VLSI Journal</i> , 2014 , 47, 499-509	1.4	11
32	OBJECT TRACKING ALGORITHM IMPLEMENTATION FOR SECURITY APPLICATIONS. <i>Far East Journal of Electronics and Communications</i> , 2016 , 16, 1-13	0	11
31	Standby and dynamic power minimization using enhanced hybrid power gating structure for deep-submicron CMOS VLSI. <i>Microelectronics Journal</i> , 2017 , 62, 137-145	1.8	10
30	Two-stage low power test data compression for digital VLSI circuits. <i>Computers and Electrical Engineering</i> , 2018 , 71, 309-320	4.3	5
29	Low-power selective pattern compression for scan-based test applications. <i>Computers and Electrical Engineering</i> , 2014 , 40, 1053-1063	4.3	5
28	A novel approach for simultaneous reduction of shift and capture power for scan based testing 2011 ,		4
27	High-Throughput Deblocking Filter Architecture Using Quad Parallel Edge Filter for H.264 Video Coding Systems. <i>IEEE Access</i> , 2019 , 7, 99642-99650	3.5	3
26	Study of approximate compressors for multiplication using FPGA 2015 ,		3
25	Design and implementation of 16x16 modified booth multiplier 2015 ,		3
24	Energy conservation using automatic lighting system using FPGA 2015 ,		3
23	Reduction of Test Power and Test Data Volume by Power Aware Compression Scheme 2012 ,		3
22	Low power reconfigurable multiplier with reordering of partial products 2011 ,		3
21	FPGA implementation of FFT blocks for OFDM 2015 ,		2
20	Face detection system using FPGA 2015 ,		2
19	Home automation through FPGA controller 2015 ,		2
18	FPGA implementation of edge detection using Canny algorithm 2015 ,		2
17	CSP-Filling: A New X-Filling Technique to Reduce Capture and Shift Power in Test Applications 2012 ,		2

16	Reduction of testing power with pulsed scan flip-flop for scan based testing 2011 ,		2
15	The Mediating Role of Overall Equipment Effectiveness on the Relationship between Fit Manufacturing and Business Performance. <i>International Journal of Engineering and Technology(UAE)</i> , 2018 , 7, 1089	0.8	2
14	Monte-Carlo Black-Scholes Implementation using OpenCL Standard. <i>Indian Journal of Science and Technology</i> , 2016 , 9, 1-5	1	2
13	Efficient half-precision floating point multiplier targeting color space conversion. <i>Multimedia Tools and Applications</i> , 2020 , 79, 89-117	2.5	2
12	Implementation of radix-4 butterfly structure to prevent arithmetic overflow 2015 ,		1
11	Adaptive Low Power RTPG for BIST based test applications 2013 ,		1
10	An area-efficient 32-bit floating point multiplier using hybrid GPPs addition 2017 ,		1
9	A SAD architecture for variable block size motion estimation in H.264 video coding 2017 ,		1
8	VLSI implementation of fast convolution 2015 ,		1
7	Sine and cosine generator using CORDIC algorithm implemented in ASIC 2015 ,		1
6	Implementation of Blake 256 hash function for password encryption and parallel CRC 2015 ,		1
5	An Efficient Single Precision Floating Point Multiplier Architecture based on Classical Recoding Algorithm. <i>Indian Journal of Science and Technology</i> , 2016 , 9,	1	1
4	An Efficient Algorithm for Tracing Minimum Leakage Current Vector in Deep-Sub Micron Circuits. <i>Advances in Intelligent Systems and Computing</i> , 2016 , 59-69	0.4	1
3	Dynamic Reconfigurable Architectures A Boon for Desires of Real Time Systems. <i>Advances in Intelligent Systems and Computing</i> , 2015 , 235-244	0.4	
2	Implementation of Special Function Unit for Vertex Shader Processor Using Hybrid Number System. <i>Journal of Computer Networks and Communications</i> , 2014 , 2014, 1-7	2.5	
1	Voice Controlled Energy Management System. <i>Asian Journal of Applied Sciences</i> , 2016 , 10, 25-31	0.4	