

# Filipe Moutinho

## List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/2840717/publications.pdf>

Version: 2024-02-01

50  
papers

458  
citations

1478280

6  
h-index

1281743

11  
g-index

54  
all docs

54  
docs citations

54  
times ranked

122  
citing authors

#	ARTICLE	IF	CITATIONS
1	IOPT-Tools “ From Executable Models to Automatic Code Generation for Embedded Controllers Development. Lecture Notes in Computer Science, 2022, , 127-138.	1.0	6
2	How to Build a 2D and 3D Aerial Multispectral Map? “All Steps Deeply Explained. Remote Sensing, 2021, 13, 3227.	1.8	10
3	Fabric Defect Detection With Deep Learning and False Negative Reduction. IEEE Access, 2021, 9, 81936-81945.	2.6	11
4	JSON Schemas with Semantic Annotations Supporting Data Translation. Applied Sciences (Switzerland), 2021, 11, 11978.	1.3	1
5	NOVA Mobility Assistive System: Developed and Remotely Controlled with IOPT-Tools. Electronics (Switzerland), 2020, 9, 1328.	1.8	1
6	Reachability Graph of IOPT Petri Net Models Using CUDA C++ “Parallel Application. IFIP Advances in Information and Communication Technology, 2020, , 93-100.	0.5	0
7	A Survey of IOPT-Flow for GALS Systems Development. , 2019, , .		1
8	Static and Dynamic Algorithms for Terrain Classification in UAV Aerial Imagery. Remote Sensing, 2019, 11, 2501.	1.8	12
9	GPGPU applied to support the construction of the state-space graphs of IOPT Petri net models. , 2019, , .		3
10	Asynchronous Interfaces for IOPT-Flow to Support GALS Systems. , 2019, , .		1
11	Annotation Rules for XML Schemas with Grouped Semantic Annotations. , 2019, , .		4
12	Web-based Editor for Signal Interpretation Models. , 2019, , .		1
13	Extended Semantic Annotations for Generating Translators in the Arrowhead Framework. IEEE Transactions on Industrial Informatics, 2018, 14, 2760-2769.	7.2	12
14	Reconfigurable devices based experimentation supporting teaching introductory digital systems. , 2017, , .		0
15	Semantic annotation of data in schemas to support data translations. , 2016, , .		5
16	Application Example. Springer Briefs in Electrical and Computer Engineering, 2016, , 43-67.	0.3	0
17	Development of Distributed Embedded Controllers. Springer Briefs in Electrical and Computer Engineering, 2016, , 19-41.	0.3	0
18	Distributed controllers modeling through Petri nets with multi-asynchronous-channels. , 2015, , .		0

#	ARTICLE	IF	CITATIONS
19	Attracting students to engineering through autonomous sailing yacht development. , 2015, , .		6
20	Extending input-output place-transition Petri nets for distributed controller systems development. , 2014, , .		21
21	IOPT-tools &#x2014; Towards cloud design automation of digital controllers with Petri nets. , 2014, , .		34
22	Communication support for Petri nets based distributed controllers. , 2014, , .		7
23	Asynchronous-Channels Within Petri Net-Based GALS Distributed Embedded Systems Modeling. IEEE Transactions on Industrial Informatics, 2014, 10, 2024-2033.	7.2	37
24	Towards medical device behavioural validation using Petri nets. , 2013, , .		10
25	IOPT-tools &#x2014; A Web based tool framework for embedded systems controller development using Petri nets. , 2013, , .		39
26	Towards distributed execution of Petri net conflicts through model transformation. , 2013, , .		3
27	Configuring communication nodes for networked embedded systems specified by Petri nets. , 2013, , .		9
28	Distributed embedded systems design using Petri nets. , 2013, , .		6
29	Augmenting High-Level Petri Nets to Support GALS Distributed Embedded Systems Specification. IFIP Advances in Information and Communication Technology, 2013, , 221-228.	0.5	1
30	MDA-Based Methodology for Verifying Distributed Execution of Embedded Systems Models. Advances in Civil and Industrial Engineering Book Series, 2013, , 112-135.	0.2	3
31	Extending a net splitting operation for decomposition of high-level Petri nets. , 2012, , .		1
32	Asynchronous wrappers configuration within GALS systems specified by Petri nets. , 2012, , .		7
33	Asynchronous-Channels and Time-Domains Extending Petri Nets for GALS Systems. International Federation for Information Processing, 2012, , 143-150.	0.4	16
34	Web based IOPT Petri net Editor with an extensible plugin architecture to support generic net operations. , 2012, , .		18
35	Model-checking framework for embedded systems controllers development using IOPT Petri nets. , 2012, , .		23
36	State space generation for Petri nets-based GALS systems. , 2012, , .		9

#	ARTICLE	IF	CITATIONS
37	A State-Space Based Model-Checking Framework for Embedded System Controllers Specified Using IOPT Petri Nets. International Federation for Information Processing, 2012, , 123-132.	0.4	4
38	From IOPT Petri nets to C: An automatic code generator tool. , 2011, , .		27
39	An IOPT-net state-space generator tool. , 2011, , .		9
40	State space generation algorithm for gals systems modeled by IOPT Petri nets. , 2011, , .		13
41	Automatic generation of graphical user interfaces for VHDL based controllers. , 2011, , .		5
42	IOPT Petri net state space generation algorithm with maximal-step execution semantics. , 2011, , .		12
43	An Ecore based Petri net type definition for PNML IOPT models. , 2011, , .		10
44	Petri Net Based Specification and Verification of Globally-Asynchronous-Locally-Synchronous System. International Federation for Information Processing, 2011, , 237-245.	0.4	6
45	SysVeritas: A Framework for Verifying IOPT Nets and Execution Semantics within Embedded Systems Design. International Federation for Information Processing, 2011, , 256-265.	0.4	7
46	Automatic Generation of Run-Time Monitoring Capabilities to Petri Nets Based Controllers with Graphical User Interfaces. International Federation for Information Processing, 2011, , 246-255.	0.4	2
47	Ecore representation for extending PNML for Input-Output Place-Transition nets. , 2010, , .		14
48	From models to controllers integrating graphical animation in FPGA through automatic code generation. , 2009, , .		9
49	Towards Usage of Formal methods within Embedded Systems Co-design. , 0, , .		19
50	Merging and Splitting Petri Net Models within Distributed Embedded Controller Design. , 0, , 160-183.		1