

# Serdar Ozoguz

## List of Publications by Year in descending order

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123  
papers

2,399  
citations

236612

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h-index

233125

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g-index

123  
all docs

123  
docs citations

123  
times ranked

900  
citing authors

#	ARTICLE	IF	CITATIONS
1	A GaN Microwave Power Amplifier Design Based on the Source/Load Pull Impedance Modeling via Virtual Gain Optimization. IEEE Access, 2022, 10, 50677-50691.	2.6	5
2	Multi-Tone Harmonic Balance Optimization for High-Power Amplifiers through Coarse and Fine Models Based on X-Parameters. Sensors, 2022, 22, 4305.	2.1	1
3	Broadband performance assessment of a microwave power transistor employing the real frequency technique. International Journal of Circuit Theory and Applications, 2022, 50, 3725-3748.	1.3	4
4	Overview of evolutionary algorithms and neural networks for modern mobile communication. Transactions on Emerging Telecommunications Technologies, 2022, 33, .	2.6	6
5	Optimization techniques for analog and RF circuit designs: an overview. Analog Integrated Circuits and Signal Processing, 2021, 106, 511-524.	0.9	3
6	Automated top-down pruning optimization approach in RF power amplifier designs. Analog Integrated Circuits and Signal Processing, 2021, 106, 525-534.	0.9	5
7	Guest editorial: introduction to the special issue on selected papers from the ELECO™2019 conference. Analog Integrated Circuits and Signal Processing, 2021, 106, 473-474.	0.9	0
8	MOS-only implementation of memristor emulator circuit. AEU - International Journal of Electronics and Communications, 2021, 141, 153975.	1.7	9
9	Multi-objective Efficiency and Phase Distortion Optimizations for Automated Design of Power Amplifiers Through Deep Neural Networks. , 2021, , .		5
10	Deep Neural Network Based Digital Predistorter of Power Amplifiers. , 2021, , .		0
11	Design Consideration of Meminductor Emulator Circuit. , 2021, , .		0
12	Automated Deep Neural Learning-Based Optimization for High Performance High Power Amplifier Designs. IEEE Transactions on Circuits and Systems I: Regular Papers, 2020, 67, 4420-4433.	3.5	32
13	Automated Two-Step Power Amplifier Design with Pre-constructed Artificial Neural Network. , 2020, , .		6
14	Inspecting Distortion in the Power Amplifiers with the aid of Neural Networks. , 2020, , .		3
15	CMOS Current-Mode Exponential/Logarithmic Function Generator Based on Translinear Loop Configuration. , 2019, , .		3
16	Algorithms for Speeding-Up the Deep Neural Networks For Detecting Plant Disease. , 2019, , .		4
17	Sensing schemes for STT-MRAMs structured with high TMR in low RA MTJs. Microelectronics Journal, 2019, 89, 30-36.	1.1	4
18	Electronically Controllable Audio Equalizers Based on Bilinear Immittances Utilizing CMOS Voltage Differencing Current Conveyor. , 2019, , .		1

#	ARTICLE	IF	CITATIONS
19	Automated Matching Network Modeling and Optimization for Power Amplifier Designs. , 2019, , .		10
20	Automated RF Power Amplifier Optimization and Design: From Lumped Elements to Distributed Elements. , 2019, , .		9
21	A Review on Optimization Methods for Designing RF Power Amplifiers. , 2019, , .		12
22	A High Linearity Low Power 130 nm CMOS LNA for Medical Ultrasound Imaging. , 2019, , .		0
23	A class of MOSFET-C multifunction filters. Analog Integrated Circuits and Signal Processing, 2018, 97, 5-13.	0.9	8
24	Introduction to the special issue on selected papers from the ELECOâ€™2017 conference. Analog Integrated Circuits and Signal Processing, 2018, 97, 1-4.	0.9	1
25	Wide Range High Precision CMOS Exponential Circuit Based on Linear Least Squares Approach. Radioengineering, 2018, 27, 1092-1099.	0.3	0
26	Minimal twoâ€™transistor multifunction filter design. International Journal of Circuit Theory and Applications, 2017, 45, 1449-1466.	1.3	12
27	The common-base differential amplifier and applications revisited. Microelectronics Journal, 2017, 63, 8-19.	1.1	6
28	Spin-torque memristor based offset cancellation technique for sense amplifiers. , 2017, , .		2
29	MOS-only complex filter design for dual-band GNSS receivers. AEU - International Journal of Electronics and Communications, 2017, 82, 83-92.	1.7	12
30	On the design of MOS-only complex filter. , 2017, , .		1
31	Design of a power output unit for HF transmitters. , 2017, , .		4
32	Design of a high-linear, high-precision analog multiplier, free from body effect. Turkish Journal of Electrical Engineering and Computer Sciences, 2016, 24, 820-832.	0.9	13
33	Wideband hybrid power combiner using double-sided-parallel-strip lines. , 2016, , .		0
34	A new active only integrator for low frequency operations. , 2016, , .		0
35	Introduction to the special issue on selected papers from the ELECOâ€™2015 conference. Analog Integrated Circuits and Signal Processing, 2016, 89, 641-643.	0.9	0
36	Low frequency active only filters with small chip area. Analog Integrated Circuits and Signal Processing, 2016, 89, 739-747.	0.9	5

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37	A new neuron and synapse model suitable for low power VLSI implementation. Analog Integrated Circuits and Signal Processing, 2016, 89, 749-770.	0.9	6
38	Distributed wideband power amplifier using reactive coupled line feedback structure. , 2015, , .		2
39	MOS-only biquads with extended operating frequency range. , 2015, , .		1
40	Design considerations of MOS-only allpass filters. , 2015, , .		2
41	Impedance matching networks for current output integrated circuits. , 2015, , .		0
42	On the realization of Gaussian membership function circuit operating in saturation region. , 2015, , .		4
43	A new neuron model suitable for low power VLSI implementation. , 2015, , .		0
44	CMOS implementation of scalable Morlet wavelet for application in signal processing. , 2015, , .		2
45	Applications of a CMOS current squaring circuit in analog signal processing. , 2015, , .		6
46	Programmable Implementation of Diamond-Shaped Type-2 Membership Function in CMOS Technology. Circuits, Systems, and Signal Processing, 2015, 34, 321-340.	1.2	4
47	Introduction to the special issue on selected papers from the ELECO™2013 conference. Analog Integrated Circuits and Signal Processing, 2014, 81, 1-3.	0.9	2
48	MOS-only allpass filters with extended operating frequency range. Analog Integrated Circuits and Signal Processing, 2014, 81, 17-22.	0.9	18
49	A Chaotic Time-Delay Sampled-Data System and Its Implementation. International Journal of Bifurcation and Chaos in Applied Sciences and Engineering, 2014, 24, 1450039.	0.7	5
50	CMOS high-precision lossless all circuit. IEEE Transactions on Electrical and Electronic Engineering, 2014, 9, 695-696.	0.8	8
51	On the Realization of MOS-Only Allpass Filters. Circuits, Systems, and Signal Processing, 2013, 32, 1455-1465.	1.2	35
52	MOS-only allpass filters modified for low operating frequency range. , 2013, , .		1
53	Guest editorial: Introduction to the special issue on selected papers from the ELECO™2011 conference. Analog Integrated Circuits and Signal Processing, 2013, 74, 59-61.	0.9	0
54	On the design and implementation of real active-only filters. , 2012, , .		1

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55	CMOS realization of a cellular neural network generating autowaves. , 2011, , .		0
56	A new CMOS exponential circuit with extended linear output range. , 2011, , .		7
57	A low power VLSI implementation of the Izhikevich neuron model. , 2011, , .		15
58	Truly random number generators based on non- $\epsilon$ -autonomous continuous-time chaos. International Journal of Circuit Theory and Applications, 2010, 38, 1-24.	1.3	48
59	Sinusoidal oscillators with lower gain requirements at higher frequencies based on an explicit $\tanh(x)$ nonlinearity. International Journal of Circuit Theory and Applications, 2010, 38, 747-760.	1.3	6
60	AN ADC BASED RANDOM BIT GENERATOR BASED ON A DOUBLE SCROLL CHAOTIC CIRCUIT. Journal of Circuits, Systems and Computers, 2010, 19, 1621-1639.	1.0	8
61	MULTISCROLL CHAOTIC ATTRACTORS FROM A HYSTERESIS BASED TIME-DELAY DIFFERENTIAL EQUATION. International Journal of Bifurcation and Chaos in Applied Sciences and Engineering, 2010, 20, 3275-3281.	0.7	18
62	Experimental verification of rank 1 chaos in switch-controlled Chua circuit. Chaos, 2009, 19, 013116.	1.0	1
63	Integrated cross-coupled chaos oscillator applied to random number generation. IET Circuits, Devices and Systems, 2009, 3, 1-11.	0.9	18
64	Linearly weighted classifier circuit. , 2009, , .		4
65	A low frequency oscillator structure. , 2009, , .		11
66	Random number generator design using continuous-time chaos. , 2008, , .		4
67	Synchronization of first-order time-delay systems generating n-scroll chaotic attractors. , 2008, , .		0
68	A SYSTEM AND CIRCUIT FOR GENERATING "MULTI-BUTTERFLIES". International Journal of Bifurcation and Chaos in Applied Sciences and Engineering, 2008, 18, 841-844.	0.7	13
69	Comments on "First-order allpass filter and sinusoidal oscillators using DDCCs". International Journal of Electronics, 2008, 95, 867-867.	0.9	2
70	A CMOS realization of double-scroll chaotic circuit and its application to random number generation. , 2008, , .		9
71	A cellular neural network made of relaxation oscillators for autowave generation in CMOS. , 2008, , .		0
72	High frequency chaos oscillators with applications. , 2007, , .		6

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73	A Chaos-Modulated Dual Oscillator-Based Truly Random Number Generator. , 2007, , .		20
74	Truly random number generators based on a non-autonomous chaotic oscillator. AEU - International Journal of Electronics and Communications, 2007, 61, 235-242.	1.7	71
75	n -scroll chaotic attractors from a first-order time-delay differential equation. Chaos, 2007, 17, 033112.	1.0	36
76	Explaining Hysteresis in Electronic Circuits: Robust Simulation and Design Examples. , 2006, , .		5
77	On the generation of higher order chaotic oscillators via passive coupling of two identical or nonidentical sinusoidal oscillators. IEEE Transactions on Circuits and Systems Part 1: Regular Papers, 2006, 53, 1521-1532.	0.1	8
78	Novel Approximate Square-Root Domain All-Pass Filter with Application to Multiphase Oscillators. Analog Integrated Circuits and Signal Processing, 2006, 46, 297-301.	0.9	19
79	Cross-coupled chaotic oscillators and application to random bit generation. IET Circuits, Devices and Systems, 2006, 153, 506.	0.6	37
80	Truly Random Number Generators Based On a Double-Scroll Attractor. , 2006, , .		16
81	PULSE-EXCITED RC NONAUTONOMOUS CHAOTIC OSCILLATOR STRUCTURES. International Journal of Bifurcation and Chaos in Applied Sciences and Engineering, 2005, 15, 2257-2261.	0.7	1
82	Novel All-Pass Filter Section Using Differential Difference Amplifier. AEU - International Journal of Electronics and Communications, 2004, 58, 153-155.	1.7	27
83	On the Realization of Circuit-Independent Nonautonomous Pulse-Excited Chaotic Oscillator Circuits. IEEE Transactions on Circuits and Systems Part 2: Express Briefs, 2004, 51, 552-556.	2.3	20
84	On the realization of NPN-only log-domain chaotic oscillators. IEEE Transactions on Circuits and Systems Part 1: Regular Papers, 2003, 50, 291-294.	0.1	9
85	New Realizations of Current Mode Tunable All-pass Filters. Frequenz, 2003, 57, .	0.6	3
86	A FOUR-WING BUTTERFLY ATTRACTOR FROM A FULLY AUTONOMOUS SYSTEM. International Journal of Bifurcation and Chaos in Applied Sciences and Engineering, 2003, 13, 3093-3098.	0.7	46
87	Tunable allpass filter for low voltage operation. Electronics Letters, 2003, 39, 175.	0.5	46
88	Chaos in pulse-excited resonator with self feedback. Electronics Letters, 2003, 39, 831.	0.5	28
89	Switched-capacitor realisation of Lorenz-type chaotic systems. Electronics Letters, 2003, 39, 496.	0.5	2
90	EXPERIMENTAL VERIFICATION OF THE BUTTERFLY ATTRACTOR IN A MODIFIED LORENZ SYSTEM. International Journal of Bifurcation and Chaos in Applied Sciences and Engineering, 2002, 12, 1627-1632.	0.7	56

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91	FAMILIES OF SCROLL GRID ATTRACTORS. International Journal of Bifurcation and Chaos in Applied Sciences and Engineering, 2002, 12, 23-41.	0.7	269
92	n-scroll chaos generator using nonlinear transconductor. Electronics Letters, 2002, 38, 685.	0.5	72
93	A modified Third Generation Current Conveyor, its Characterization and Applications. Frequenz, 2002, 56, 47-54.	0.6	17
94	Creation of a complex butterfly attractor using a novel Lorenz-Type system. IEEE Transactions on Circuits and Systems Part 1: Regular Papers, 2002, 49, 527-530.	0.1	108
95	Tunable Ladder-Type Realization of Current-Mode Elliptic Filters. AEU - International Journal of Electronics and Communications, 2002, 56, 193-199.	1.7	8
96	Linearly Tunable Transconductor Using Modified CDBA. Analog Integrated Circuits and Signal Processing, 2001, 26, 179-183.	0.9	5
97	Derivation of low-sensitivity current-mode CCII-based filters. IET Circuits, Devices and Systems, 2001, 148, 115.	0.6	9
98	n-scroll chaos generators: a simple circuit model. Electronics Letters, 2001, 37, 147.	0.5	87
99	Simple log-domain chaotic oscillator. Electronics Letters, 2001, 37, 1378.	0.5	12
100	n <sup>th</sup> -order current transfer function synthesis using current differencing buffered amplifier: signal-flow graph approach. Microelectronics Journal, 2000, 31, 49-53.	1.1	33
101	N <sup>th</sup> -order Voltage Transfer Function Synthesis Using a Commercially Available Active Component, CFA: Signal-Flow Graph Approach. Frequenz, 2000, 54, .	0.6	5
102	Current-mode all-pass filters using current differencing buffered amplifier and a new high-Q bandpass filter configuration. IEEE Transactions on Circuits and Systems Part 2: Express Briefs, 2000, 47, 949-954.	2.3	155
103	Insensitive current-mode universal filter using dual output current conveyors. International Journal of Electronics, 2000, 87, 667-674.	0.9	42
104	First-order allpass sections-based current-mode universal filter using ICCIIs. Electronics Letters, 2000, 36, 1443.	0.5	22
105	Insensitive current-mode universal filter with minimum components using dual-output current conveyors. Electronics Letters, 1999, 35, 524.	0.5	35
106	High Output Impedance Current-Mode Multifunction Filters with Minimum Number of Active and Passive Elements Using Dual-Output Current Conveyors. Frequenz, 1999, 53, 206-209.	0.6	3
107	Current-mode KHN-equivalent biquad using CDBAs. Electronics Letters, 1999, 35, 1682.	0.5	42
108	A new versatile building block: current differencing buffered amplifier suitable for analog signal-processing filters. Microelectronics Journal, 1999, 30, 157-160.	1.1	246

#	ARTICLE	IF	CITATIONS
109	New current-mode universal filters using only four (CCII+)s. Microelectronics Journal, 1999, 30, 255-258.	1.1	23
110	Current-mode continuous-time fully-integrated universal filter using CDBAs. Electronics Letters, 1999, 35, 97.	0.5	73
111	High output impedance current-mode multifunction filter with minimum number of active and reduced number of passive elements. Electronics Letters, 1998, 34, 1807.	0.5	35
112	Single-input and three-output current-mode universal filter using a reduced number of active elements. Electronics Letters, 1998, 34, 605.	0.5	7
113	On the realization of floating imittance function simulators using current conveyors. International Journal of Electronics, 1998, 85, 463-475.	0.9	12
114	Universal current-mode filter with reduced number of active and passive components. Electronics Letters, 1997, 33, 948.	0.5	70
115	Universal filter with three inputs using CCII+. Electronics Letters, 1996, 32, 2134.	0.5	47
116	High-order voltage transfer function synthesis using CCII+ based unity gain current amplifiers. Electronics Letters, 1996, 32, 2030.	0.5	34
117	On the current-mode current conveyor-based high-order filter realisations. , 0, , .		1
118	CCII-based balanced fully integrated continuous-time current-mode filter synthesis: signal-flow graph approach. , 0, , .		0
119	Modified log-domain oscillator for chaos. , 0, , .		0
120	First-order allpass filter realisation using a modified third generation current conveyor. , 0, , .		3
121	Pulse-excited RC nonautonomous chaotic oscillator structures. , 0, , .		1
122	A non-autonomous IC chaotic oscillator and its application for random bit generation. , 0, , .		9
123	An Integrated Circuit Chaotic Oscillator and Its Application for High Speed Random Bit Generation. , 0, , .		11