Iwona Grobelna

List of Publications by Year in Descending Order

25 papers	**140** citations
8 h-index	**11** g-index
33 ext. papers	**207** ext. citations
2.6 avg, IF	**3.48** L-index

#	Paper	IF	Citations
25	Interpreted Petri Nets Applied to Autonomous Components within Electric Power Systems. *Applied Sciences (Switzerland)*, **2022**, 12, 4772	2.6	1
24	Overview of Control Algorithm Verification Methods in Power Electronics Systems. *Energies*, **2021**, 14, 4360	3.1	3
23	Challenges in Application of Petri Nets in Manufacturing Systems. *Electronics (Switzerland)*, **2021**, 10, 2305	2.6	2
22	Determinism in Cyber-Physical Systems Specified by Interpreted Petri Nets. *Sensors*, **2020**, 20,	3.8	8
21	Formal Verification of Control Modules in Cyber-Physical Systems. *Sensors*, **2020**, 20,	3.8	3
20	Design and Verification of Cyber-Physical Systems Specified by Petri Nets—A Case Study of a Direct Matrix Converter. *Mathematics*, **2019**, 7, 812	2.3	10
19	Specification of Cyber-Physical Systems with the Application of Interpreted Nets **2019**,		4
18	Design of Multi-Context Reconfigurable Logic Controllers Implemented in FPGA Devices Oriented for Further Partial Reconfiguration. *Journal of Circuits, Systems and Computers*, **2018**, 27, 1850086	0.9	8
17	Model checking of reconfigurable FPGA modules specified by Petri nets. *Journal of Systems Architecture*, **2018**, 89, 1-9	5.5	7
16	Design and Verification of Real-Life Processes With Application of Petri Nets. *IEEE Transactions on Systems, Man, and Cybernetics: Systems*, **2017**, 47, 2856-2869	7.3	33
15	IoT security with one-time pad secure algorithm based on the double memory technique **2017**,		2
14	Model Checking of UML Activity Diagrams Using a Rule-Based Logical Model. *Studies in Systems, Decision and Control*, **2016**, 153-163	0.8	
13	Various Interpretations of Actions of UML Activity Diagrams in Logic Controller Design. *Studies in Systems, Decision and Control*, **2016**, 143-151	0.8	
12	A rule-based approach to model checking of UML state machines **2016**,		2
11	Partial reconfiguration of concurrent logic controllers implemented in FPGA devices **2016**,		1
10	Logic controller design system supporting UML activity diagrams **2015**,		2
9	UML activity diagram swimlanes in logic controller design **2015**,		2

LIST OF PUBLICATIONS

8   Design and verification of distributed logic controllers with application of Petri nets **2015**,   1

7   UML activity diagrams in requirements specification of logic controllers **2015**,   2

6   Deadlock detection in Petri nets: One trace for one deadlock? **2014**,   9

5   Decomposition, validation and documentation of control process specification in form of a Petri net **2014**,   13

4   Model Checking of UML Activity Diagrams in Logic Controllers Design. *Advances in Intelligent Systems and Computing*, **2014**, 233-242   0.4   13

3   Hardware Behavioural Modelling, Verification and Synthesis with UML 2.x Activity Diagrams. *IFAC Postprint Volumes IPPV / International Federation of Automatic Control*, **2012**, 45, 134-139   8

2   Embedded WWW Server in Wireless Sensor Networks. *IFAC Postprint Volumes IPPV / International Federation of Automatic Control*, **2009**, 42, 220-225

1   Visualization of control process by means of Petri nets and database **2006**,   3