Sheng-Yu

List of Publications by Year
in descending order

7 papers	**29** citations	3311381 **1** h-index	2917675 **2** g-index
7 all docs	**7** docs citations	**7** times ranked	**22** citing authors

#	Article	IF	Citations
1	Processor-Tracing Guided Region Formation in Dynamic Binary Translation. Transactions on Architecture and Code Optimization, 2019, 15, 1-25.	2.0	1
2	Efficient and retargetable SIMD translation in a dynamic binary translator. Software - Practice and Experience, 2018, 48, 1312-1330.	3.6	5
3	Automatically Migrating Sequential Applications to Heterogeneous System Architecture. , 2018, , .		0
4	Exploiting Asymmetric SIMD Register Configurations in ARM-to-x86 Dynamic Binary Translation. , 2017, , .		6
5	Exploiting Longer SIMD Lanes in Dynamic Binary Translation. , 2016, , .		7
6	SIMD Code Translation in an Enhanced HQEMU. , 2015, , .		5
7	Improving SIMD Code Generation in QEMU. , 2015, , .		5