

Sheng-Yu

List of Publications by Year in descending order

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#	ARTICLE	IF	CITATIONS
1	Exploiting Longer SIMD Lanes in Dynamic Binary Translation. , 2016, , .		7
2	Exploiting Asymmetric SIMD Register Configurations in ARM-to-x86 Dynamic Binary Translation. , 2017, , .		6
3	SIMD Code Translation in an Enhanced HQEMU. , 2015, , .		5
4	Efficient and retargetable SIMD translation in a dynamic binary translator. Software - Practice and Experience, 2018, 48, 1312-1330.	3.6	5
5	Improving SIMD Code Generation in QEMU. , 2015, , .		5
6	Processor-Tracing Guided Region Formation in Dynamic Binary Translation. Transactions on Architecture and Code Optimization, 2019, 15, 1-25.	2.0	1
7	Automatically Migrating Sequential Applications to Heterogeneous System Architecture. , 2018, , .		0