Ingrid M Verbauwhede

List of Publications by Year
in descending order

412 papers	11,440 citations	53794 45 h-index	71685 76 g-index
423 all docs	423 docs citations	423 times ranked	4489 citing authors

#	Article	IF	Citations
1	TROT: A Three-Edge Ring Oscillator Based True Random Number Generator With Time-to-Digital Conversion. IEEE Transactions on Circuits and Systems I: Regular Papers, 2022, 69, 2435-2448.	5.4	15
2	Hardware Security: Physical Design versus Side-Channel and Fault Attacks. , 2022, , .		0
3	DATE 2022: Aiming for an Online/ Onsite Format and Finally Moving to Online Only. IEEE Design and Test, 2022, 39, 90-93.	1.2	0
4	Trust in FPGA-accelerated Cloud Computing. ACM Computing Surveys, 2021, 53, 1-28.	23.0	21
5	Exploring Micro-architectural Side-Channel Leakages through Statistical Testing. , 2021, , .		0
6	A Side-Channel-Resistant Implementation of SABER. ACM Journal on Emerging Technologies in Computing Systems, 2021, 17, 1-26.	2.3	30
7	Design and Analysis of Configurable Ring Oscillators for True Random Number Generation Based on Coherent Sampling. ACM Transactions on Reconfigurable Technology and Systems, 2021, 14, 1-20.	2.5	3
8	Prime+Scope. , 2021, , .		21
9	Lattice-Based Public-Key Cryptography in Hardware. Computer Architecture and Design Methodologies, 2020, , .	0.8	1
10	Design and Evaluation of a Spark Gap Based EM-fault Injection Setup. , 2020, , .		2
11	Sweeping for Leakage in Masked Circuit Layouts. , 2020, , .		1
12	Compact domain-specific co-processor for accelerating module lattice-based KEM. , 2020, , .		6
13	Attacking Hardware Random Number Generators in a Multi-Tenant Scenario. , 2020, , .		3
14	HEAWS: An Accelerator for Homomorphic Encryption on the Amazon AWS FPGA. IEEE Transactions on Computers, 2020, , 1-1.	3.4	39
15	Towards efficient and automated side-channel evaluations at design time. Journal of Cryptographic Engineering, 2020, 10, 305-319.	1.8	11
16	Coprocessor for Koblitz Curves. Computer Architecture and Design Methodologies, 2020, , 25-42.	0.8	0
17	Discrete Gaussian Sampling. Computer Architecture and Design Methodologies, 2020, , 43-63.	0.8	0
18	Design Considerations for EM Pulse FaultÂInjection. Lecture Notes in Computer Science, 2020, , 176-192.	1.3	3

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19	Atlas: Application Confidentiality in Compromised Embedded Systems. IEEE Transactions on Dependable and Secure Computing, 2019, 16, 415-423.	5.4	3
20	EM Information Security Threats Against RO-Based TRNGs: The Frequency Injection Attack Based on IEMI and EM Information Leakage. IEEE Transactions on Electromagnetic Compatibility, 2019, 61, 1122-1128.	2.2	15
21	A Physically Unclonable Function Using Soft Oxide Breakdown Featuring 0% Native BER and 51.8 fJ/bit in 40-nm CMOS. IEEE Journal of Solid-State Circuits, 2019, 54, 2765-2776.	5.4	45
22	A Self-Calibrating True Random Number Generator. , 2019, , .		1
23	A Highly-Portable True Random Number Generator Based on Coherent Sampling. , 2019, , .		18
24	A Lightweight 1.16 pJ/bit Processor for the Authenticated Encryption Scheme KetjeSR. , 2019, , .		0
25	Pushing the speed limit of constant-time discrete Gaussian sampling. A case study on the Falcon signature scheme. , 2019, , .		17
26	Design Principles for True Random Number Generators for Security Applications. , 2019, , .		3
27	Compact and Flexible FPGA Implementation of Ed25519 and X25519. Transactions on Embedded Computing Systems, 2019, 18, 1-21.	2.9	23
28	Decryption Failure Attacks on IND-CCA Secure Lattice-Based Schemes. Lecture Notes in Computer Science, 2019, , 565-598.	1.3	26
29	FPGA-Based High-Performance Parallel Architecture for Homomorphic Computing on Encrypted Data. , 2019, , .		49
30	Security and reliability â€“ friend or foe. , 2019, , .		1
31	Hardware-Efficient Post-Processing Architectures for True Random Number Generators. IEEE Transactions on Circuits and Systems II: Express Briefs, 2019, 66, 1242-1246.	3.0	6
32	Single-Round Pattern Matching Key Generation Using Physically Unclonable Function. Security and Communication Networks, 2019, 2019, 1-13.	1.5	4
33	The Impact of Error Dependencies on Ring/Mod-LWE/LWR Based Schemes. Lecture Notes in Computer Science, 2019, , 103-115.	1.3	20
34	An In-Depth and Black-Box Characterization of the Effects of Laser Pulses on ATmega328P. Lecture Notes in Computer Science, 2019, , 156-170.	1.3	10
35	Propagating trusted execution through mutual attestation. , 2019, , .		2
36	Characterization of EM faults on ATmega328p. , 2019, , .		6

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37	The Need for Hardware Roots of Trust. , 2019, , .		1
38	Arithmetic of $$au $$ ï„ -adic expansions for lightweight Koblitz curve cryptography. Journal of Cryptographic Engineering, 2018, 8, 285-300.	1.8	2
39	Private Mobile Pay-TV From Priced Oblivious Transfer. IEEE Transactions on Information Forensics and Security, 2018, 13, 280-291.	6.9	8
40	EE2: Workshop on circuits for social good. , 2018, , .		0
41	Towards inter-vendor compatibility of true random number generators for FPGAs. , 2018, , .		0
42	F1: Intelligent energy-efficient systems at the edge of IoT. , 2018, , .		1
43	X-Ray and Proton Radiation Effects on 40 nm CMOS Physically Unclonable Function Devices. IEEE Transactions on Nuclear Science, 2018, 65, 1519-1524.	2.0	9
44	HEPCloud: An FPGA-based Multicore Processor for FV Somewhat Homomorphic Function Evaluation. IEEE Transactions on Computers, 2018, , 1-1.	3.4	31
45	Constant-Time Discrete Gaussian Sampling. IEEE Transactions on Computers, 2018, 67, 1561-1571.	3.4	39
46	Hardware-Based Trusted Computing Architectures for Isolation and Attestation. IEEE Transactions on Computers, 2018, 67, 361-374.	3.4	91
47	A Physically Unclonable Function with 0% BER Using Soft Oxide Breakdown in 40nm CMOS. , 2018, , .		5
48	Teaching HW/SW codesign with a Zynq ARM/FPGA SoC. , 2018, , .		3
49	The Impact of Pulsed Electromagnetic Fault Injection on True Random Number Generators. , 2018, , .		6
50	A multi-bit/cell PUF using analog breakdown positions in CMOS. , 2018, , .		10
51	Design and testing methodologies for true random number generators towards industry certification. , 2018, , .		10
52	Introduction to EM information security for IoT devices. , 2018, , .		1
53	Detection of IEMI fault injection using voltage monitor constructed with fully digital circuit. , 2018, , .		5
54	Comparison of two setups for contactless power measurements for side-channel analysis. , 2018, , .		1

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55	Fundamental study on non-invasive frequency injection attack against RO-based TRNG. , 2018, , .		1
56	A Closer Look at the Delay-Chain based TRNG. , 2018, , .		5
57	SOFIA: Software and control flow integrity architecture. Computers and Security, 2017, 68, 16-35.	6.0	30
58	A 5.1Î¼J per pointâ€multiplication elliptic curve cryptographic processor. International Journal of Circuit Theory and Applications, 2017, 45, 170-187.	2.0	6
59	Dude, is my code constant time?. , 2017, , .		39
60	Hardware Assisted Fully Homomorphic Function Evaluation and Encrypted Search. IEEE Transactions on Computers, 2017, 66, 1562-1572.	3.4	21
61	Lightweight Prediction-Based Tests for On-Line Min-Entropy Estimation. IEEE Embedded Systems Letters, 2017, 9, 45-48.	1.9	2
62	High-Performance Ideal Lattice-Based Cryptography on 8-Bit AVR Microcontrollers. Transactions on Embedded Computing Systems, 2017, 16, 1-24.	2.9	14
63	Sancus 2.0. ACM Transactions on Privacy and Security, 2017, 20, 1-33.	3.0	61
64	STBC: Side Channel Attack Tolerant Balanced Circuit with Reduced Propagation Delay. , 2017, , .		4
65	LiBrA-CAN. Transactions on Embedded Computing Systems, 2017, 16, 1-28.	2.9	31
66	SCM. , 2017, , .		2
67	Elliptic Curve Cryptography with Efficiently Computable Endomorphisms and Its Hardware Implementations for the Internet of Things. IEEE Transactions on Computers, 2017, 66, 773-785.	3.4	49
68	Security Adds an Extra Dimension to IC Design: Future IC Design Must Focus on Security in Addition to Low Power and Energy. IEEE Solid-State Circuits Magazine, 2017, 9, 41-45.	0.4	9
69	The Monte Carlo PUF. , 2017, , .		1
70	Physically unclonable function using CMOS breakdown position. , 2017, , .		15
71	SSCS AdCom Member-at-Large Ingrid Verbauwhede Receives IEEE Computer Society 2017 Technical Achievement Award [IEEE News]. IEEE Solid-State Circuits Magazine, 2017, 9, 94-94.	0.4	0
72	On-chip jitter measurement for true random number generators. , 2017, , .		11

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73	Fast Leakage Assessment. Lecture Notes in Computer Science, 2017, , 387-399.	1.3	15
74	Hold Your Breath, PRIMATEs Are Lightweight. Lecture Notes in Computer Science, 2017, , 197-216.	1.3	0
75	Providing security on demand using invasive computing. IT - Information Technology, 2016, 58, 281-295.	0.9	4
76	Iterating Von Neumann's post-processing under hardware constraints. , 2016, , .		8
77	Exploring active manipulation attacks on the TERO random number generator. , 2016, , .		12
78	On the Feasibility of Cryptography for a Wireless Insulin Pump System. , 2016, , .		23
79	Ring-LWE: Applications to Cryptography and Their Efficient Realization. Lecture Notes in Computer Science, 2016, , 323-331.	1.3	2
80	Binary decision diagram to design balanced secure logic styles. , 2016, , .		1
81	Upper bounds on the min-entropy of RO Sum, Arbiter, Feed-Forward Arbiter, and S-ArbRO PUFs. , 2016, , .		6
82	VLSI Design Methods for Low Power Embedded Encryption. , 2016, , .		4
83	Masking ring-LWE. Journal of Cryptographic Engineering, 2016, 6, 139-153.	1.8	24
84	A Lockdown Technique to Prevent Machine Learning on PUFs for Lightweight Authentication. IEEE Transactions on Multi-Scale Computing Systems, 2016, 2, 146-159.	2.4	142
85	A Fast and Compact FPGA Implementation of Elliptic Curve Cryptography Using Lambda Coordinates. Lecture Notes in Computer Science, 2016, , 63-83.	1.3	2
86	Hardware acceleration of a software-based VPN. , 2016, , .		4
87	IoT: Source of test challenges. , 2016, , .		21
88	A Tiny Coprocessor for Elliptic Curve Cryptography over the 256-bit NIST Prime Field. , 2016, , .		6
89	Embedded Security. , 2016, , .		1
90	Additively Homomorphic Ring-LWE Masking. Lecture Notes in Computer Science, 2016, , 233-244.	1.3	28

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91	Single-Cycle Implementations of Block Ciphers. Lecture Notes in Computer Science, 2016, , 131-147.	1.3	19
92	Design and Implementation of a Waveform-Matching Based Triggering System. Lecture Notes in Computer Science, 2016, , 184-198.	1.3	11
93	Efficient Finite Field Multiplication for Isogeny Based Post Quantum Cryptography. Lecture Notes in Computer Science, 2016, , 193-207.	1.3	17
94	Efficient Fuzzy Extraction of PUF-Induced Secrets: Theory and Applications. Lecture Notes in Computer Science, 2016, , 412-431.	1.3	48
95	TOTAL: TRNG On-the-fly Testing for Attack Detection using Lightweight Hardware. , 2016, , .		20
96	Software Security: Vulnerabilities and Countermeasures for Two Attacker Models. , 2016, , .		6
97	On-the-fly tests for non-ideal true random number generators. , 2015, , .		9
98	Efficient Software Implementation of Ring-LWE Encryption. , 2015, , .		44
99	Embedded HW/SW Platform for On-the-Fly Testing of True Random Number Generators. , 2015, , .		9
100	Helper Data Algorithms for PUF-Based Key Generation: Overview and Analysis. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2015, 34, 889-902.	2.7	189
101	RECTANGLE: a bit-slice lightweight block cipher suitable for multiple platforms. Science China Information Sciences, 2015, 58, 1-15.	4.3	115
102	A Survey on Lightweight Entity Authentication with Strong PUFs. ACM Computing Surveys, 2015, 48, 1-42.	23.0	133
103	Soteria. , 2015, , .		14
104	Practical feasibility evaluation and improvement of a pay-per-use licensing scheme for hardware IP cores in Xilinx FPGAs. Journal of Cryptographic Engineering, 2015, 5, 113-122.	1.8	8
105	High-Speed Polynomial Multiplication Architecture for Ring-LWE and SHE Cryptosystems. IEEE Transactions on Circuits and Systems I: Regular Papers, 2015, 62, 157-166.	5.4	94
106	Secure, Remote, Dynamic Reconfiguration of FPGAs. ACM Transactions on Reconfigurable Technology and Systems, 2015, 7, 1-19.	2.5	11
107	24.1 Circuit challenges from cryptography. , 2015, , .		15
108	Highly efficient entropy extraction for true random number generators on FPGAs. , 2015, , .		28

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109	Electromagnetic circuit fingerprints for Hardware Trojan detection. , 2015, , .		59
110	Accelerating Scalar Conversion for Koblitz Curve Cryptoprocessors on Hardware Platforms. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2015, 23, 810-818.	3.1	3
111	How to Use Koblitz Curves on Small Devices?. Lecture Notes in Computer Science, 2015, , 154-170.	1.3	3
112	Consolidating Masking Schemes. Lecture Notes in Computer Science, 2015, , 764-783.	1.3	128
113	DPA, Bitslicing and Masking at 1 GHz. Lecture Notes in Computer Science, 2015, , 599-619.	1.3	47
114	Efficient Ring-LWE Encryption on 8-Bit AVR Processors. Lecture Notes in Computer Science, 2015, , 663-682.	1.3	45
115	A Masked Ring-LWE Implementation. Lecture Notes in Computer Science, 2015, , 683-702.	1.3	38
116	Lightweight Coprocessor for Koblitz Curves: 283-Bit ECC Including Scalar Conversion with only 4300 Gates. Lecture Notes in Computer Science, 2015, , 102-122.	1.3	9
117	Modular Hardware Architecture for Somewhat Homomorphic Function Evaluation. Lecture Notes in Computer Science, 2015, , 164-184.	1.3	16
118	Anonymous Split E-Cashâ€"Toward Mobile Anonymous Payments. Transactions on Embedded Computing Systems, 2015, 14, 1-25.	2.9	9
119	Hardware/software co-design flavors of elliptic curve scalar multiplication. , 2014, , .		0
120	A noise bifurcation architecture for linear additive physical functions. , 2014, , .		56
121	Software Only, Extremely Compact, Keccak-based Secure PRNG on ARM Cortex-M. , 2014, , .		9
122	Ultra Low-Power implementation of ECC on the ARM Cortex-M0+. , 2014, , .		29
123	Secure interrupts on low-end microcontrollers. , 2014, , .		9
124	Key-recovery attacks on various RO PUF constructions via helper data manipulation. , 2014, , .		9
125	Test Versus Security: Past and Present. IEEE Transactions on Emerging Topics in Computing, 2014, 2, 50-62.	4.6	77
126	BLAKE-512-Based 128-Bit CCA2 Secure Timing Attack Resistant McEliece Cryptoprocessor. IEEE Transactions on Computers, 2014, 63, 1124-1133.	3.4	17

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127	Fault Injection Modeling Attacks on 65 nm Arbiter and RO Sum PUFs via Environmental Changes. IEEE Transactions on Circuits and Systems I: Regular Papers, 2014, 61, 1701-1713.	5.4	90
128	Novel RNS Parameter Selection for Fast Modular Multiplication. IEEE Transactions on Computers, 2014, 63, 2099-2105.	3.4	15
129	Attacking PUF-Based Pattern Matching Key Generators via Helper Data Manipulation. Lecture Notes in Computer Science, 2014, , 106-131.	1.3	34
130	Generic DPA Attacks: Curse or Blessing?. Lecture Notes in Computer Science, 2014, , 98-111.	1.3	9
131	Chaskey: An Efficient MAC Algorithm for 32-bit Microcontrollers. Lecture Notes in Computer Science, 2014, , 306-323.	1.3	113
132	High Precision Discrete Gaussian Sampling on FPGAs. Lecture Notes in Computer Science, 2014, , 383-401.	1.3	18
133	Compact Ring-LWE Cryptoprocessor. Lecture Notes in Computer Science, 2014, , 371-391.	1.3	125
134	Secure Lightweight Entity Authentication with Strong PUFs: Mission Impossible?. Lecture Notes in Computer Science, 2014, , 451-475.	1.3	44
135	Key-recovery attacks on various RO PUF constructions via helper data manipulation. , 2014, , .		7
136	A Note on the Use of Margins to Compare Distinguishers. Lecture Notes in Computer Science, 2014, , 1-8.	1.3	4
137	Secure JTAG Implementation Using Schnorr Protocol. Journal of Electronic Testing: Theory and Applications (JETTA), 2013, 29, 193-209.	1.2	32
138	Teaching HW/SW Co-Design With a Public Key Cryptography Application. IEEE Transactions on Education, 2013, 56, 478-483.	2.4	9
139	A New Model for Error-Tolerant Side-Channel Cube Attacks. Lecture Notes in Computer Science, 2013, , 453-470.	1.3	4
140	A single-chip solution for the secure remote configuration of FPGAs using bitstream compression. , 2013, , .		13
141	Core Based Architecture to Speed Up Optimal Ate Pairing on FPGA Platform. Lecture Notes in Computer Science, 2013, , 141-159.	1.3	10
142	Hardware Designer's Guide to Fault Attacks. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2013, 21, 2295-2306.	3.1	128
143	Side channel modeling attacks on 65nm arbiter PUFs exploiting CMOS device noise. , 2013, , .		97
144	SPONGENT: The Design Space of Lightweight Cryptographic Hashing. IEEE Transactions on Computers, 2013, 62, 2041-2053.	3.4	74

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145	Low-energy encryption for medical devices. , 2013, , .		7
146	DEMO: Inherent PUFs and secure PRNGs on commercial off-the-shelf microcontrollers. , 2013, , .		5
147	Secure PRNG seeding on commercial off-the-shelf microcontrollers. , 2013, , .		15
148	The exponential impact of creativity in computer engineering education. , 2013, , .		3
149	Security Analysis of Industrial Test Compression Schemes. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2013, 32, 1966-1977.	2.7	33
150	Signal Processing for Cryptography and Security Applications. , 2013, , 223-241.		3
151	Faster Pairing Coprocessor Architecture. Lecture Notes in Computer Science, 2013, , 160-176.	1.3	16
152	On the Implementation of Unified Arithmetic on Binary Huff Curves. Lecture Notes in Computer Science, 2013, , 349-364.	1.3	11
153	Dietary Recommendations for Lightweight Block Ciphers: Power, Energy and Area Analysis of Recently Developed Architectures. Lecture Notes in Computer Science, 2013, , 103-112.	1.3	34
154	Protected Software Module Architectures. , 2013, , 241-251.		14
155	PUF-based secure test wrapper design for cryptographic SoC testing. , 2012, , .		33
156	Fair and Consistent Hardware Evaluation of Fourteen Round Two SHA-3 Candidates. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2012, 20, 827-840.	3.1	35
157	Low-cost implementations of on-the-fly tests for random number generators. , 2012, , .		12
158	Experimental evaluation of Physically Unclonable Functions in 65 nm CMOS. , 2012, , .		85
159	Guest Editorial - Integrated Circuit and System Security. IEEE Transactions on Information Forensics and Security, 2012, 7, 1-2.	6.9	3
160	Scan attacks on side-channel and fault attack resistant public-key implementations. Journal of Cryptographic Engineering, 2012, 2, 207-219.	1.8	3
161	Machine learning attacks on 65nm Arbiter PUFs: Accurate modeling poses strict bounds on usability. , 2012, , .		98
162	Design solutions for securing SRAM cell against power analysis. , 2012, , .		12

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163	A Pay-per-Use Licensing Scheme for Hardware IP Cores in Recent SRAM-Based FPGAs. IEEE Transactions on Information Forensics and Security, 2012, 7, 98-108.	6.9	55
164	A systematic M safe-error detection in hardware implementations of cryptographic algorithms. , 2012, , .		3
165	A Speed Area Optimized Embedded Co-processor for McEliece Cryptosystem. , 2012, , .		18
166	Tiny application-specific programmable processor for BCH decoding. , 2012, , .		4
167	A scan-based attack on Elliptic Curve Cryptosystems in presence of industrial Design-for-Testability structures. , 2012, , .		14
168	Differential Scan Attack on AES with X-tolerant and X-masked Test Response Compactor. , 2012, , .		20
169	PUFs: Myth, Fact or Busted? A Security Evaluation of Physically Unclonable FunctionsÂ(PUFs) Cast in Silicon. Lecture Notes in Computer Science, 2012, , 283-301.	1.3	148
170	Efficient and secure hardware. Datenschutz Und Datensicherheit - DuD, 2012, 36, 872-875.	0.4	0
171	Interface Design for Mapping a Variety of RSA Exponentiation Algorithms on a HW/SW Co-design Platform. , 2012, , .		4
172	Theory and Practice of a Leakage Resilient Masking Scheme. Lecture Notes in Computer Science, 2012, , 758-775.	1.3	30
173	Extending ECC-based RFID authentication protocols to privacy-preserving multi-party grouping proofs. Personal and Ubiquitous Computing, 2012, 16, 323-335.	2.8	36
174	Efficient Hardware Implementation of Fp-Arithmetic for Pairing-Friendly Curves. IEEE Transactions on Computers, 2012, 61, 676-685.	3.4	22
175	A Practical Attack on KeeLoq. Journal of Cryptology, 2012, 25, 136-157.	2.8	14
176	Hierarchical ECC-Based RFID Authentication Protocol. Lecture Notes in Computer Science, 2012, , 183-201.	1.3	14
177	Power Analysis of Atmel CryptoMemory â€“ Recovering Keys from Secure EEPROMs. Lecture Notes in Computer Science, 2012, , 19-34.	1.3	34
178	An Updated Survey on Secure ECC Implementations: Attacks, Countermeasures and Cost. Lecture Notes in Computer Science, 2012, , 265-282.	1.3	60
179	A New Scan Attack on RSA in Presence of Industrial Countermeasures. Lecture Notes in Computer Science, 2012, , 89-104.	1.3	20
180	Reverse Fuzzy Extractors: Enabling Lightweight Mutual Authentication for PUF-Enabled RFIDs. Lecture Notes in Computer Science, 2012, , 374-389.	1.3	115

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181	Selecting Time Samples for Multivariate DPA Attacks. Lecture Notes in Computer Science, 2012, , 155-174.	1.3	31
182	PUFKY: A Fully Functional PUF-Based Cryptographic Key Generator. Lecture Notes in Computer Science, 2012, , 302-319.	1.3	147
183	Performance and Security Evaluation of AES S-Box-Based Glitch PUFs on FPGAs. Lecture Notes in Computer Science, 2012, , 45-62.	1.3	6
184	LiBrA-CAN: A Lightweight Broadcast Authentication Protocol for Controller Area Networks. Lecture Notes in Computer Science, 2012, , 185-200.	1.3	118
185	Three Phase Dynamic Current Mode Logic: A More Secure DyCML to Achieve a More Balanced Power Consumption. Lecture Notes in Computer Science, 2012, , 68-81.	1.3	10
186	Systematic security evaluation method against C safe-error attacks. , 2011, , .		1
187	The Fault Attack Jungle - A Classification Model to Guide You. , 2011, , .		39
188	An In-depth and Black-box Characterization of the Effects of Clock Glitches on 8-bit MCUs. , 2011, , .		104
189	The cost of cryptography: Is low budget possible?. , 2011, , .		5
190	spongent: A Lightweight Hash Function. Lecture Notes in Computer Science, 2011, , 312-325.	1.3	185
191	Secure remote reconfiguration of an FPGA-based embedded system. , 2011, , .		15
192	Can We Trust the Chips of the Future?. IEEE Design and Test of Computers, 2011, 28, 96-103.	1.0	1
193	Machine learning in side-channel analysis: a first study. Journal of Cryptographic Engineering, 2011, 1, 293-302.	1.8	211
194	Tripartite modular multiplication. The Integration VLSI Journal, 2011, 44, 259-269.	2.1	21
195	Design and design methods for unified multiplier and inverter and its application for HECC. The Integration VLSI Journal, 2011, 44, 280-289.	2.1	12
196	Physically unclonable functions. , 2011, , .		29
197	The communication and computation cost of wireless security. , 2011, , .		14
198	Low-cost fault detection method for ECC using Montgomery powering ladder. , 2011, , .		9

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199	FPGA Implementation of Pairings Using Residue Number System and Lazy Reduction. Lecture Notes in Computer Science, 2011, , 421-441.	1.3	48
200	Constructing Application-Specific Memory Hierarchies on FPGAs. Lecture Notes in Computer Science, 2011, , 201-216.	1.3	1
201	FO4-based models for area, delay and energy of polynomial multiplication over binary fields. , 2010, , .		0
202	Low Cost Built in Self Test for Public Key Crypto Cores. , 2010, , .		5
203	An embedded platform for privacy-friendly road charging applications. , 2010, , .		4
204	Physically Unclonable Functions: A Study on the State of the Art and Future Research Directions. Information Security and Cryptography, 2010, , 3-37.	0.3	294
205	Low-cost untraceable authentication protocols for RFID. , 2010, , .		70
206	Wideâ€“Weak Privacyâ€“Preserving RFID Authentication Protocols. Lecture Notes of the Institute for Computer Sciences, Social-Informatics and Telecommunications Engineering, 2010, , 254-267.	0.3	12
207	A compact FPGA-based architecture for elliptic curve cryptography over prime fields. , 2010, , .		43
208	Faster Interleaved Modular Multiplication Based on Barrett and Montgomery Reduction Methods. IEEE Transactions on Computers, 2010, 59, 1715-1721.	3.4	52
209	Implementation of binary edwards curves for very-constrained devices. , 2010, , .		20
210	State-of-the-art of secure ECC implementations: a survey on known side-channel attacks and countermeasures. , 2010, , .		101
211	A Hybrid Scheme for Concurrent Error Detection of Multiplication over Finite Fields. , 2010, , .		3
212	Breaking Elliptic Curve Cryptosystems Using Reconfigurable Hardware. , 2010, , .		14
213	Prototyping platform for performance evaluation of SHA-3 candidates. , 2010, , .		12
214	Compact Public-Key Implementations for RFID and Sensor Nodes. Integrated Circuits and Systems, 2010, , 179-195.	0.2	1
215	Privacy Challenges in RFID Systems. , 2010, , 397-407.		12
216	Revisiting Higher-Order DPA Attacks:. Lecture Notes in Computer Science, 2010, , 221-234.	1.3	45

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217	Speeding Up Bipartite Modular Multiplication. Lecture Notes in Computer Science, 2010, , 166-179.	1.3	11
218	Anti-counterfeiting, Untraceability and Other Security Challenges for RFID Systems: Public-Key-Based Protocols and Hardware. Information Security and Cryptography, 2010, , 237-257.	0.3	12
219	Hardware design for Hash functions. Integrated Circuits and Systems, 2010, , 79-104.	0.2	2
220	Signal Processing for Cryptography and Security Applications. , 2010, , 161-177.		0
221	Practical Mitigations for Timing-Based Side-Channel Attacks on Modern x86 Processors. , 2009, , .		133
222	Analysis and design of active IC metering schemes. , 2009, , .		29
223	Light-weight implementation options for curve-based cryptography: HECC is also ready for RFID. , 2009, , .		1
224	Empirical comparison of side channel analysis distinguishers on DES in hardware. , 2009, , .		3
225	FPGA-based testing strategy for cryptographic chips: A case study on Elliptic Curve Processor for RFID tags. , 2009, , .		2
226	Modular reduction without precomputational phase. , 2009, , .		9
227	Practical DPA attacks on MDPL. , 2009, , .		13
228	A soft decision helper data algorithm for SRAM PUFs. , 2009, , .		125
229	Efficient implementation of anonymous credentials on Java Card smart cards. , 2009, , .		31
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