List of Publications by Year in descending order

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#	Article	IF	CITATIONS
1	New analog implementation technique for fractional-order controller: A DC motor control. AEU - International Journal of Electronics and Communications, 2017, 78, 192-200.	1.7	110
2	Electronically Tunable Fully Integrated Fractional-Order Resonator. IEEE Transactions on Circuits and Systems II: Express Briefs, 2018, 65, 166-170.	2.2	74
3	A survey of single and multi-component Fractional-Order Elements (FOEs) and their applications. Microelectronics Journal, 2019, 84, 9-25.	1.1	74
4	Emulation of a constant phase element using operational transconductance amplifiers. Analog Integrated Circuits and Signal Processing, 2015, 85, 413-423.	0.9	71
5	0.5V bulk-driven analog building blocks. AEU - International Journal of Electronics and Communications, 2012, 66, 920-927.	1.7	66
6	A low frequency oscillator using a super-capacitor. AEU - International Journal of Electronics and Communications, 2016, 70, 970-973.	1.7	65
7	Comparative Study of Discrete Component Realizations of Fractional-Order Capacitor and Inductor Active Emulators. Journal of Circuits, Systems and Computers, 2018, 27, 1850170.	1.0	64
8	Design of CMOS Analog Integrated Fractional-Order Circuits. Springer Briefs in Electrical and Computer Engineering, 2017, , .	0.3	63
9	Fractionalâ€order electronically controlled generalized filters. International Journal of Circuit Theory and Applications, 2017, 45, 595-612.	1.3	62
10	Experimental verification of on hip CMOS fractionalâ€order capacitor emulators. Electronics Letters, 2016, 52, 1298-1300.	0.5	60
11	Fractional-order filters based on low-voltage DDCCs. Microelectronics Journal, 2016, 50, 50-59.	1.1	60
12	0.5â€V fractionalâ€order companding filters. International Journal of Circuit Theory and Applications, 2015, 43, 1105-1126.	1.3	57
13	Electronically controlled multiphase sinusoidal oscillators using current amplifiers. International Journal of Circuit Theory and Applications, 2009, 37, 43-52.	1.3	56
14	Approximation of the Fractional-Order Laplacian <inline-formula> <tex-math notation="LaTeX">\$s^alpha\$ </tex-math </inline-formula> As a Weighted Sum of First-Order High-Pass Filters. IEEE Transactions on Circuits and Systems II: Express Briefs, 2018, 65, 1114-1118	2.2	53
15	Ultra-low voltage fractional-order differentiator and integrator topologies: an application for handling noisy ECGs. Analog Integrated Circuits and Signal Processing, 2014, 81, 393-405.	0.9	51
16	Ultraâ€low voltage fractionalâ€order circuits using current mirrors. International Journal of Circuit Theory and Applications, 2016, 44, 109-126.	1.3	49
17	Partial fraction expansion–based realizations of fractionalâ€order differentiators and integrators using active filters. International Journal of Circuit Theory and Applications, 2019, 47, 513-531.	1.3	44
18	Multiple-input single-output universal biquad filter using single output operational transconductance amplifiers. AEU - International Journal of Electronics and Communications, 2018, 93, 360-367.	1.7	42

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19	Low-Voltage Low-Power Integrable CMOS Circuit Implementation of Integer- and Fractional–Order FitzHugh–Nagumo Neuron Model. IEEE Transactions on Neural Networks and Learning Systems, 2019, 30, 2108-2122.	7.2	42
20	Power law filters: A new class of fractional-order filters without a fractional-order Laplacian operator. AEU - International Journal of Electronics and Communications, 2021, 129, 153537.	1.7	41
21	Current amplifier based grounded and floating inductance simulators. AEU - International Journal of Electronics and Communications, 2006, 60, 168-171.	1.7	40
22	Multiple Input Single Output Universal Biquad Filter with Current Feedback Operational Amplifiers. Circuits, Systems, and Signal Processing, 2010, 29, 1167-1180.	1.2	39
23	Switched-Capacitor Fractional-Step Butterworth Filter Design. Circuits, Systems, and Signal Processing, 2016, 35, 1377-1393.	1.2	39
24	Emulation of an electrical-analogue of a fractional-order human respiratory mechanical impedance model using OTA topologies. AEU - International Journal of Electronics and Communications, 2017, 78, 201-208.	1.7	39
25	A floating generalized impedance converter with current feedback operational amplifiers. AEU - International Journal of Electronics and Communications, 2008, 62, 81-85.	1.7	38
26	Differential Difference Current Conveyor Using Bulk-Driven Technique for Ultra-Low-Voltage Applications. Circuits, Systems, and Signal Processing, 2014, 33, 159-176.	1.2	38
27	Practical Design and Evaluation of Fractional-Order Oscillator Using Differential Voltage Current Conveyors. Circuits, Systems, and Signal Processing, 2016, 35, 2003-2016.	1.2	38
28	Multiple-Input Bulk-Driven MOS Transistor for Low-Voltage Low-Frequency Applications. Circuits, Systems, and Signal Processing, 2019, 38, 2829-2845.	1.2	37
29	A systematic design procedure for square-root-domain circuits based on the signal flow graph approach. IEEE Transactions on Circuits and Systems Part 1: Regular Papers, 2002, 49, 1702-1712.	0.1	34
30	Emulation of current excited fractional-order capacitors and inductors using OTA topologies. Microelectronics Journal, 2016, 55, 70-81.	1.1	34
31	Single active element implementation of fractional-order differentiators and integrators. AEU - International Journal of Electronics and Communications, 2018, 97, 6-15.	1.7	33
32	Harmonic oscillators realized using current amplifiers and grounded capacitors. International Journal of Circuit Theory and Applications, 2007, 35, 165-173.	1.3	30
33	Practical Design of RC Approximants of Constant Phase Elements and Their Implementation in Fractional-Order PID Regulators Using CMOS Voltage Differencing Current Conveyors. Circuits, Systems, and Signal Processing, 2019, 38, 1520-1546.	1.2	28
34	Multiphase sinusoidal oscillators using second generation current conveyors. AEU - International Journal of Electronics and Communications, 2010, 64, 1178-1181.	1.7	27
35	Synthesis and design of constant phase elements based on the multiplication of electronically controllable bilinear immittances in practice. AEU - International Journal of Electronics and Communications, 2017, 78, 98-113.	1.7	27
36	Design and application examples of CMOS fractional-order differentiators and integrators. Microelectronics Journal, 2019, 83, 155-167.	1.1	27

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37	Fractional-order oscillator design using unity-gain voltage buffers and OTAs. , 2017, , .		26
38	Comprehensive comparison based on meta-heuristic algorithms for approximation of the fractional-order Laplacian s as a weighted sum of first-order high-pass filters. Microelectronics Journal, 2019, 87, 110-120.	1.1	26
39	Ultra low-power electronically tunable current-mode instrumentation amplifier for biomedical applications. AEU - International Journal of Electronics and Communications, 2020, 117, 153120.	1.7	26
40	Nonlinear charge-voltage relationship in constant phase element. AEU - International Journal of Electronics and Communications, 2020, 117, 153104.	1.7	26
41	Realization of log-domain high-order transfer functions using first-order building blocks and complementary operators. International Journal of Circuit Theory and Applications, 2007, 35, 17-32.	1.3	25
42	Guest Editorial: Fractional-Order Circuits and Systems: Theory, Design, and Applications. Circuits, Systems, and Signal Processing, 2016, 35, 1807-1813.	1.2	25
43	Experimental Verification of Fractional-Order Filters Using a Reconfigurable Fractional-Order Impedance Emulator. Journal of Circuits, Systems and Computers, 2017, 26, 1750142.	1.0	23
44	One-terminal electronically controlled fractional-order capacitor and inductor emulator. AEU - International Journal of Electronics and Communications, 2019, 103, 32-45.	1.7	23
45	Novel two-measurements-only Cole-Cole bio-impedance parameters extraction technique. Measurement: Journal of the International Measurement Confederation, 2019, 131, 394-399.	2.5	23
46	1.5-V Complex Filters Using Current Mirrors. IEEE Transactions on Circuits and Systems II: Express Briefs, 2011, 58, 575-579.	2.2	22
47	0.65V class-AB current-mode four-quadrant multiplier with reduced power dissipation. AEU - International Journal of Electronics and Communications, 2011, 65, 673-677.	1.7	22
48	1ÂV Rectifier Based on Bulk-Driven Quasi-Floating-Gate Differential Difference Amplifiers. Circuits, Systems, and Signal Processing, 2015, 34, 2077-2089.	1.2	22
49	Capacitorless digitally programmable fractional-order filters. AEU - International Journal of Electronics and Communications, 2017, 78, 228-237.	1.7	22
50	High-Frequency Capacitorless Fractional-Order CPE and FI Emulator. Circuits, Systems, and Signal Processing, 2018, 37, 2694-2713.	1.2	22
51	Low-Voltage Log-Domain Complex Filters. IEEE Transactions on Circuits and Systems I: Regular Papers, 2008, 55, 3404-3412.	3.5	21
52	Digitally programmed fractional-order Chebyshev filters realizations using current-mirrors. , 2015, , .		21
53	Experimental behavior evaluation of series and parallel connected constant phase elements. AEU - International Journal of Electronics and Communications, 2017, 74, 5-12.	1.7	21
54	Log-Domain Wave Filters. IEEE Transactions on Circuits and Systems Part 2: Express Briefs, 2004, 51, 299-306.	2.3	20

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55	Design of Sinhâ€Ðomain filters using complementary operators. International Journal of Circuit Theory and Applications, 2012, 40, 1019-1039.	1.3	20
56	Realization of companding filters with large time-constants for biomedical applications. Analog Integrated Circuits and Signal Processing, 2014, 78, 217-231.	0.9	20
57	Analysis and realization of a switched fractionalâ€orderâ€capacitor integrator. International Journal of Circuit Theory and Applications, 2016, 44, 2035-2040.	1.3	20
58	Double Exponent Fractional-Order Filters: Approximation Methods and Realization. Circuits, Systems, and Signal Processing, 2021, 40, 993-1004.	1.2	20
59	Modular Log-Domain Filters Realized Using Wave Port Terminators. IEEE Transactions on Circuits and Systems Part 1: Regular Papers, 2004, 51, 2235-2244.	0.1	19
60	Log-domain filtering by simulating the topology of passive prototypes. IEEE Transactions on Circuits and Systems Part 1: Regular Papers, 2005, 52, 2043-2054.	0.1	19
61	Multiple-Input Universal Filter and Quadrature Oscillator Using Multiple-Input Operational Transconductance Amplifiers. IEEE Access, 2021, 9, 56253-56263.	2.6	19
62	Design of square-root domain filters by substituting the passive elements of the prototype filter by their equivalents. International Journal of Circuit Theory and Applications, 2008, 36, 185-204.	1.3	18
63	First-order allpass filter using multi-input OTA. International Journal of Electronics, 2013, 100, 1373-1382.	0.9	18
64	Transient and Steady-State Response of a Fractional-Order Dynamic PV Model Under Different Loads. Journal of Circuits, Systems and Computers, 2018, 27, 1850023.	1.0	18
65	Low-voltage Bluetooth/ZigBee complex filter using current mirrors. , 2010, , .		17
66	Simple nonâ€impedanceâ€based measuring technique for supercapacitors. Electronics Letters, 2015, 51, 1699-1701.	0.5	17
67	Ultra-low-Voltage Integrable Electronic Realization of Integer- and Fractional-Order Liao's Chaotic Delayed Neuron Model. Circuits, Systems, and Signal Processing, 2017, 36, 4844-4868.	1.2	17
68	On the exact realization of log-domain elliptic filters using the signal flow graph approach. IEEE Transactions on Circuits and Systems Part 2: Express Briefs, 2002, 49, 770-774.	2.3	16
69	On the transposition ofGm–C filters to DC stabilized log-domain filters. International Journal of Circuit Theory and Applications, 2006, 34, 217-236.	1.3	16
70	A Comparative Study of the Performance of the Flipped Voltage Follower Based Low-Voltage Current Mirrors. , 2007, , .		16
71	Square-root domain wave filters. International Journal of Circuit Theory and Applications, 2007, 35, 131-148.	1.3	16
72	Low-voltage current controlled current conveyor. Analog Integrated Circuits and Signal Processing, 2010, 63, 129-135.	0.9	16

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73	A 0.5ÂV tunable complex filter for Bluetooth and Zigbee using OTAs. Analog Integrated Circuits and Signal Processing, 2014, 79, 73-81.	0.9	16
74	Sub-Volt Fully Balanced Differential Difference Amplifier. Journal of Circuits, Systems and Computers, 2015, 24, 1550005.	1.0	16
75	Current-mode capacitorless integrators and differentiators for implementing emulators of fractional-order elements. AEU - International Journal of Electronics and Communications, 2017, 80, 94-103.	1.7	16
76	Generalized Fully Adjustable Structure for Emulating Fractional-Order Capacitors and Inductors of Orders less than Two. Circuits, Systems, and Signal Processing, 2020, 39, 1797-1814.	1.2	16
77	1.2 V BiCMOS Sinh-Domain Filters. Circuits, Systems, and Signal Processing, 2012, 31, 1257-1277.	1.2	15
78	Universal biquad filter topology using lowâ€voltage current mirrors. International Journal of Circuit Theory and Applications, 2012, 40, 65-75.	1.3	15
79	Minimization of Spread of Time-Constants and Scaling Factors in Fractional-Order Differentiator and Integrator Realizations. Circuits, Systems, and Signal Processing, 2018, 37, 5647-5663.	1.2	15
80	On the mechanism of creating pinched hysteresis loops using a commercial memristor device. AEU - International Journal of Electronics and Communications, 2019, 111, 152923.	1.7	15
81	Implementation and analysis of tunable fractional-order band-pass filter of order 2α. AEU - International Journal of Electronics and Communications, 2020, 124, 153343.	1.7	15
82	Current-Mode Linear Transformation Filters Using Current Mirrors. IEEE Transactions on Circuits and Systems II: Express Briefs, 2008, 55, 541-545.	2.2	14
83	Digitally programmable low-voltage highly linear transconductor based on promising CMOS structure of differential difference current conveyor. AEU - International Journal of Electronics and Communications, 2015, 69, 1010-1017.	1.7	14
84	Two-quadrant fully integrable rms-to-dc converter for handling low-frequency signals. AEU - International Journal of Electronics and Communications, 2015, 69, 1897-1901.	1.7	14
85	Experimental verification of filters using fractional-order capacitor and inductor emulators. , 2016, ,		14
86	0.65â€V integrable electronic realisation of integer―and fractionalâ€order Hindmarsh–Rose neuron model using companding technique. IET Circuits, Devices and Systems, 2018, 12, 696-706.	0.9	14
87	Realizations of simple fractional-order capacitor emulators with electronically-tunable capacitance. The Integration VLSI Journal, 2019, 69, 225-233.	1.3	14
88	Employment of the Padé approximation for implementing fractional-order lead/lag compensators. AEU - International Journal of Electronics and Communications, 2020, 120, 153203.	1.7	14
89	Pseudo-Differential (2 + α)-Order Butterworth Frequency Filter. IEEE Access, 2021, 9, 92178-92188.	2.6	14
90	Fractional-order filter design for ultra-low frequency applications. , 2016, , .		13

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91	Realizations of fractional-order PID loop-shaping controller for mechatronic applications. The Integration VLSI Journal, 2021, 80, 5-12.	1.3	13
92	Low-Voltage Current Feedback Operational Amplifiers. Circuits, Systems, and Signal Processing, 2009, 28, 377-388.	1.2	12
93	Log-domain SIMO and MISO low-voltage universal biquads. Analog Integrated Circuits and Signal Processing, 2011, 67, 201-211.	0.9	12
94	Electronically tunable fractional-order highpass filter for phantom electroencephalographic system model implementation. AEU - International Journal of Electronics and Communications, 2019, 110, 152850.	1.7	12
95	Realization of Cole–Davidson Function-Based Impedance Models: Application on Plant Tissues. Fractal and Fractional, 2020, 4, 54.	1.6	12
96	A log-domain multiphase sinusoidal oscillator. AEU - International Journal of Electronics and Communications, 2008, 62, 622-626.	1.7	11
97	On the pinched hysteresis behavior in a state-controlled resistor. AEU - International Journal of Electronics and Communications, 2017, 74, 171-175.	1.7	11
98	Programmable analog array of fractional-order filters with CFOAs. , 2017, , .		11
99	Extraction of Cole-Cole model parameters through low-frequency measurements. AEU - International Journal of Electronics and Communications, 2018, 84, 355-359.	1.7	11
100	Correlation Between the Theory of Lissajous Figures and the Generation of Pinched Hysteresis Loops in Nonlinear Circuits. IEEE Transactions on Circuits and Systems I: Regular Papers, 2019, 66, 2606-2614.	3.5	11
101	Fractional-Order and Power-Law Shelving Filters: Analysis and Design Examples. IEEE Access, 2021, 9, 145977-145987.	2.6	11
102	Log-domain linear transformation filters revised: improved building blocks and comparison results. International Journal of Circuit Theory and Applications, 2008, 36, 119-133.	1.3	10
103	Analysis and experimental verification of a fractional-order Hartley oscillator. , 2017, , .		10
104	Simple MOSâ€based circuit designed to show pinched hysteresis behavior. International Journal of Circuit Theory and Applications, 2018, 46, 1123-1128.	1.3	10
105	Design and Implementation of an Optimized Artificial Human Eardrum Model. Circuits, Systems, and Signal Processing, 2020, 39, 3219-3233.	1.2	10
106	FPAA-Based Realization of Filters with Fractional Laplace Operators of Different Orders. Fractal and Fractional, 2021, 5, 218.	1.6	10
107	A novel all-pass current-mode filter realized using a minimum number of single output OTAs. Frequenz, 2010, 64, .	0.6	9
108	Sinh-Domain multiphase sinusoidal oscillator. Microelectronics Journal, 2013, 44, 834-839.	1.1	9

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109	Compact MOS-RC voltage-mode fractional-order oscillator design. , 2017, , .		9
110	Design of a Generalized Fractional-Order PID Controller Using Operational Amplifiers. , 2018, , .		9
111	Fractional-Order Multiphase Sinusoidal Oscillator Design Using Current-Mirrors. , 2018, , .		9
112	0.5ÂV Universal Filter Based on Multiple-Input FDDAs. Circuits, Systems, and Signal Processing, 2019, 38, 5896-5907.	1.2	9
113	A High Performance Square-Root Domain Integrator. Analog Integrated Circuits and Signal Processing, 2002, 32, 97-101.	0.9	8
114	Low-voltage CMOS VT extractor. Electronics Letters, 2007, 43, 921.	0.5	8
115	Universal biquad filters using low-voltage current mirrors. Analog Integrated Circuits and Signal Processing, 2010, 65, 77-88.	0.9	8
116	A fractional-order dynamic PV model. , 2016, , .		8
117	Comparative study of fractional-order differentiators and integrators. , 2017, , .		8
118	Reduced Active Components Count Electronically Adjustable Fractional-Order Controllers: Two Design Examples. Electronics (Switzerland), 2020, 9, 63.	1.8	8
119	Simple implementations of fractional-order driving-point impedances: Application to biological tissue models. AEU - International Journal of Electronics and Communications, 2021, 137, 153784.	1.7	8
120	Electronically Controlled Power-Law Filters Realizations. Fractal and Fractional, 2022, 6, 111.	1.6	8
121	Realization of current-mirror filters with large time-constants. AEU - International Journal of Electronics and Communications, 2014, 68, 1261-1264.	1.7	7
122	Designing constant phase elements of complement order. Analog Integrated Circuits and Signal Processing, 2018, 97, 107-114.	0.9	7
123	Fractional-Order Model of a Commercial Ear Simulator. , 2018, , .		7
124	CCII Based Realization of Fractional-Order PD Controller for a Position Servo. , 2019, , .		7
125	Implementation of a Fractional-Order Electronically Reconfigurable Lung Impedance Emulator of the Human Respiratory Tree. Journal of Low Power Electronics and Applications, 2020, 10, 18.	1.3	7
126	Power-Law Compensator Design for Plants with Uncertainties: Experimental Verification. Electronics (Switzerland), 2021, 10, 1305.	1.8	7

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127	Tinnitus Detector Realization Using Sinh-Domain Circuits. Journal of Low Power Electronics, 2013, 9, 458-470.	0.6	7
128	± 0.45ÂV CMOS Second-Generation Voltage Conveyor Based on Super Source Follower. Circuits, Systems, and Signal Processing, 2022, 41, 1819-1833.	1.2	7
129	Current Amplifier-Based Wave Filters. Circuits, Systems, and Signal Processing, 2005, 24, 303-313.	1.2	6
130	A low-voltage square-root domain n-th order multifunction FLF filter topology. Analog Integrated Circuits and Signal Processing, 2009, 61, 315-322.	0.9	6
131	Low-voltage CMOS adjustable current mirror. Electronics Letters, 2010, 46, 124.	0.5	6
132	Low-voltage reduced complexity cells for MOS translinear loops. Circuits, Systems, and Signal Processing, 2013, 32, 2445-2456.	1.2	6
133	Companding Realizations of the Nonlinear Energy Operator. ISRN Biomedical Engineering, 2013, 2013, 1-7.	0.4	6
134	Single MIMO-OTA and single-grounded-capacitor-based first-order allpass filter design. International Journal of Electronics, 2014, 101, 1716-1723.	0.9	6
135	A 50 mHz Sinh-Domain High-pass Filter for Realizing an ECG Signal Acquisition System. Circuits, Systems, and Signal Processing, 2014, 33, 3673-3696.	1.2	6
136	Simple Multi-Function Fractional-Order Filter Designs. , 2019, , .		6
137	Low-voltage and low-power fractional-order parallel tunable resonator. Microelectronics Journal, 2019, 88, 108-116.	1.1	6
138	Passive approximations of doubleâ€exponent fractionalâ€order impedance functions. International Journal of Circuit Theory and Applications, 2021, 49, 1274-1284.	1.3	6
139	Decoupling the magnitude and phase in a constant phase element. Journal of Electroanalytical Chemistry, 2021, 888, 115153.	1.9	6
140	0.5V Sinh-Domain Design of Activation Functions and Neural Networks. Journal of Low Power Electronics, 2014, 10, 201-213.	0.6	6
141	Versatile Field-Programmable Analog Array Realizations of Power-Law Filters. Electronics (Switzerland), 2022, 11, 692.	1.8	6
142	Squareâ€root domain linear transformation filters. International Journal of Circuit Theory and Applications, 2011, 39, 719-731.	1.3	5
143	Low-Voltage Complex Filters Using Current Feedback Operational Amplifiers. ISRN Electronics, 2013, 2013, 1-7.	1.1	5
144	Ultra-Low Voltage Sixth-Order Low Pass Filter for Sensing the T-Wave Signal in ECGs. Journal of Low Power Electronics and Applications, 2014, 4, 292-303.	1.3	5

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145	1.2 V Sinhâ€Domain allpass filter. International Journal of Circuit Theory and Applications, 2015, 43, 22-35.	1.3	5
146	Mihalas–Niebur model implementation using Sinh-Domain integrators. Analog Integrated Circuits and Signal Processing, 2016, 88, 161-171.	0.9	5
147	Voltage gain-controlled third-generation current conveyor and its all-pass filter verification. , 2017, ,		5
148	Single- Input Multiple-Output and Multiple-Input Single-Output Fractional-Order Filter Designs. , 2018, , .		5
149	Electronically Tunable Implementation of the Arterial Viscoelasticity Model. , 2019, , .		5
150	A compact power-efficient 0.5†V fully differential difference amplifier. AEU - International Journal of Electronics and Communications, 2019, 105, 71-77.	1.7	5
151	Tunable Fractional-Order Band-pass Filter of order 2α. , 2019, , .		5
152	Fractional-Order Shelving Filter Designs for Acoustic Applications. , 2020, , .		5
153	Design of Low-Voltage FO-[PD] Controller for Motion Systems. Journal of Low Power Electronics and Applications, 2021, 11, 26.	1.3	5
154	0.5 V RMS-to-DC Converter Topologies Suitable for Implantable Biomedical Devices. Journal of Low Power Electronics, 2014, 10, 373-382.	0.6	5
155	Log-Domain Implementation of QRS Detection System Using the Pan-Tompkins Algorithm with Fractional-Order Differentiator for Improved Noise Rejection. Journal of Low Power Electronics, 2016, 12, 352-360.	0.6	5
156	Firstâ€order inverse filters: Implementations using a single voltage conveyor and potential applications. International Journal of Circuit Theory and Applications, 2022, 50, 3704-3714.	1.3	5
157	On the exact realization of log-domain elliptic filters using the signal flow graph approach. IEEE Transactions on Circuits and Systems Part 2: Express Briefs, 2003, 50, 325-325.	2.3	4
158	A Square-Root Domain Differentiator Circuit. Analog Integrated Circuits and Signal Processing, 2004, 40, 53-59.	0.9	4
159	Square-Root Domain Operational Simulation of LC Ladder Elliptic Filters. Circuits, Systems, and Signal Processing, 2007, 26, 263-280.	1.2	4
160	All-pass filters realised using the current-controlled CCII with intrinsic negative resistance. International Journal of Electronics, 2010, 97, 491-498.	0.9	4
161	Analog cochlear implant using Sinh-Domain filters. , 2011, , .		4
162	Differential voltage current controlled current conveyor with low-voltage operation capability. International Journal of Electronics, 2014, 101, 939-949.	0.9	4

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163	Ultra-low voltage CMOS current-mode four-quadrant multiplier. International Journal of Electronics Letters, 2014, 2, 224-233.	0.7	4
164	Biomedical and biological applications of fractional-order circuits. , 2017, , .		4
165	Design of a wood tissue impedance emulator in monolithic form. , 2017, , .		4
166	Cole-Cole Bio-Impedance Parameters Extraction From a Single Time-Domain Measurement. , 2019, , .		4
167	Simple Design of Fractional-Order DC Motor Controller. , 2019, , .		4
168	Single transistor fractional-order filter using a multi-walled carbon nanotube device. Analog Integrated Circuits and Signal Processing, 2019, 100, 215-219.	0.9	4
169	Fully Electronically Tunable Inverse Fractional-Order Filter Designs. , 2019, , .		4
170	Ultra-low-voltage integrable electronic implementation of delayed inertial neural networks for complex dynamical behavior using multiple activation functions. Neural Computing and Applications, 2020, 32, 8297-8314.	3.2	4
171	Design of Fractional-Order Lead Compensator for a Car Suspension System Based on Curve-Fitting Approximation. Fractal and Fractional, 2021, 5, 46.	1.6	4
172	Novel Double-Dispersion Models Based on Power-Law Filters. Circuits, Systems, and Signal Processing, 2021, 40, 5799-5812.	1.2	4
173	On The Equivalent Impedance of Two-Impedance Self-Similar Ladder Networks. IEEE Transactions on Circuits and Systems II: Express Briefs, 2021, 68, 2685-2689.	2.2	4
174	A collection of interdisciplinary applications of fractional-order circuits. , 2022, , 35-69.		4
175	Switched capacitor circuit synthesis using voltage inversion principles. IEE Proceedings, Part C: Circuits, Devices and Systems, 1991, 138, 703.	0.2	3
176	Improved switched-current (SI) bilinear integrator circuit. Electronics Letters, 1995, 31, 26-27.	0.5	3
177	A Novel Log-Domain Differentiator. Analog Integrated Circuits and Signal Processing, 2002, 32, 285-287.	0.9	3
178	Switched-Current Wave Filters with Reduced Number of Current Inversions. Analog Integrated Circuits and Signal Processing, 2003, 36, 255-258.	0.9	3
179	OTA Based Frequency Tuning System. , 2007, , .		3
180	DESIGN OF FILTERS WITH ONLY GROUNDED PASSIVE ELEMENTS USING DIFFERENTIAL VOLTAGE CURRENT FEEDBACK OPERATIONAL AMPLIFIERS. Journal of Circuits, Systems and Computers, 2010, 19, 573-580.	1.0	3

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181	Multiple-loop feedback filters using current feedback amplifiers. International Journal of Electronics, 2011, 98, 833-846.	0.9	3
182	Square-Root-Domain Realization of Single-Cell Architecture of Complex TDCNN. Circuits, Systems, and Signal Processing, 2013, 32, 959-978.	1.2	3
183	0.5ÂV sinh-domain differentiator. International Journal of Electronics Letters, 2015, 3, 34-44.	0.7	3
184	Analysis of a rectifier circuit realized with a fractional-order capacitor. , 2016, , .		3
185	Applications of Fractional-Order Circuits. Springer Briefs in Electrical and Computer Engineering, 2017, , 87-112.	0.3	3
186	CMOS Realization of All-Positive Pinched Hysteresis Loops. Complexity, 2017, 2017, 1-15.	0.9	3
187	Fractional-Order Differentiators and Integrators with Reduced Circuit Complexity. , 2018, , .		3
188	Differentiator based fractional-order high-pass filter designs. , 2018, , .		3
189	OTA-C Implementation of Fractional-Order Lead/Lag Compensators. , 2019, , .		3
190	Design of Operational Amplifier Based Fractional-Order Controller for a MAGLEV System. , 2020, , .		3
191	Implementing Fractional PID Control for MagLev with SoftFRAC. , 2020, , .		3
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