

Alberto Prieto Espinosa

List of Publications by Year in descending order

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Version: 2024-02-01

77
papers

2,339
citations

218592

26
h-index

233338

45
g-index

82
all docs

82
docs citations

82
times ranked

1828
citing authors

#	ARTICLE	IF	CITATIONS
1	New trends in computational intelligence. Neurocomputing, 2017, 250, 1-4.	3.5	1
2	Neural networks: An overview of early research, current frameworks and new challenges. Neurocomputing, 2016, 214, 242-268.	3.5	217
3	Affinity-Based Network Interfaces for Efficient Communication on Multicore Architectures. Journal of Computer Science and Technology, 2013, 28, 508-524.	0.9	9
4	Deploying intelligent e-health services in a mobile gateway. Expert Systems With Applications, 2013, 40, 1231-1239.	4.4	21
5	AN AUDITORY BRAIN-COMPUTER INTERFACE WITH ACCURACY PREDICTION. International Journal of Neural Systems, 2012, 22, 1250009.	3.2	37
6	Daily living activity recognition based on statistical feature quality group selection. Expert Systems With Applications, 2012, 39, 8013-8021.	4.4	117
7	Customized stimulation enhances performance of independent binary SSVEP-BCIs. Clinical Neurophysiology, 2011, 122, 128-133.	0.7	36
8	Method for prediction of protein-protein interactions in yeast using genomics/proteomics information and feature selection. Neurocomputing, 2011, 74, 2683-2690.	3.5	10
9	Bio-inspired systems: Computational and ambient intelligence. Neurocomputing, 2011, 74, 2591-2593.	3.5	11
10	Use of Phase in Brain-Computer Interfaces based on Steady-State Visual Evoked Potentials. Neural Processing Letters, 2010, 32, 1-9.	2.0	39
11	Network interfaces for programmable NICs and multicore platforms. Computer Networks, 2010, 54, 357-376.	3.2	5
12	Statistical Characterization of Steady-State Visual Evoked Potentials and Their Use in Brain-Computer Interfaces. Neural Processing Letters, 2009, 29, 179-187.	2.0	21
13	Protocol offload analysis by simulation. Journal of Systems Architecture, 2009, 55, 25-42.	2.5	7
14	Visualizing the evolution of a web-based social network. Journal of Network and Computer Applications, 2008, 31, 677-698.	5.8	10
15	Asynchronous distributed genetic algorithms with Javascript and JSON. , 2008, , .		26
16	Real-Time System for High-Image Resolution Disparity Estimation. IEEE Transactions on Image Processing, 2007, 16, 280-285.	6.0	46
17	Recursive prediction for long term time series forecasting using advanced models. Neurocomputing, 2007, 70, 2870-2880.	3.5	64
18	Using fuzzy logic to improve a clustering technique for function approximation. Neurocomputing, 2007, 70, 2853-2860.	3.5	28

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19	General Logarithmic Image Processing Convolution. IEEE Transactions on Image Processing, 2006, 15, 3602-3608.	6.0	25
20	TaSe, a Taylor series-based fuzzy system model that combines interpretability and accuracy. Fuzzy Sets and Systems, 2005, 153, 403-427.	1.6	60
21	Motion-Driven Segmentation by Competitive Neural Processing. Neural Processing Letters, 2005, 22, 125-147.	2.0	8
22	Online Global Learning in Direct Fuzzy Controllers. IEEE Transactions on Fuzzy Systems, 2004, 12, 218-229.	6.5	31
23	A new clustering technique for function approximation. IEEE Transactions on Neural Networks, 2002, 13, 132-142.	4.8	105
24	Structure identification in complete rule-based fuzzy systems. IEEE Transactions on Fuzzy Systems, 2002, 10, 349-359.	6.5	47
25	Statistical analysis of the parameters of a neuro-genetic algorithm. IEEE Transactions on Neural Networks, 2002, 13, 1374-1394.	4.8	53
26	Time series analysis using normalized PG-RBF network with regression weights. Neurocomputing, 2002, 42, 267-285.	3.5	131
27	Multidimensional and multideme genetic algorithms for the construction of fuzzy systems. International Journal of Approximate Reasoning, 2001, 26, 179-210.	1.9	14
28	Optimization of web newspaper layout in real time. Computer Networks, 2001, 36, 311-321.	3.2	2
29	Improved RAN sequential prediction using orthogonal techniques. Neurocomputing, 2001, 41, 153-172.	3.5	48
30	G-Prop: Global optimization of multilayer perceptrons using GAs. Neurocomputing, 2000, 35, 149-163.	3.5	125
31	Improving the tolerance of multilayer perceptrons by minimizing the statistical sensitivity to weight deviations. Neurocomputing, 2000, 31, 87-103.	3.5	31
32	Competitive and Temporal Inhibition Structures with Spiking Neurons. Neural Processing Letters, 2000, 11, 197-208.	2.0	1
33	Analysis of the Functional Block Involved in the Design of Radial Basis Function Networks. Neural Processing Letters, 2000, 12, 1-17.	2.0	27
34	Evolving Multilayer Perceptrons. Neural Processing Letters, 2000, 12, 115-128.	2.0	58
35	Obtaining Fault Tolerant Multilayer Perceptrons Using an Explicit Regularization. Neural Processing Letters, 2000, 12, 107-113.	2.0	35
36	Self-Organization by Temporal Inhibition (SOTI). Neural Processing Letters, 2000, 12, 199-213.	2.0	0

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37	A Quantitative Study of Fault Tolerance, Noise Immunity, and Generalization Ability of MLPs. Neural Computation, 2000, 12, 2941-2964.	1.3	51
38	Neural network for demixing super-Gaussian signals. Electronics Letters, 2000, 36, 1474.	0.5	0
39	A systematic approach to a self-generating fuzzy rule-table for function approximation. IEEE Transactions on Systems, Man, and Cybernetics, 2000, 30, 431-447.	5.5	91
40	Self-organized fuzzy system generation from training examples. IEEE Transactions on Fuzzy Systems, 2000, 8, 23-36.	6.5	125
41	Statistical analysis of the main parameters in the fuzzy inference process. Fuzzy Sets and Systems, 1999, 102, 157-173.	1.6	23
42	New methodology for the development of adaptive and self-learning fuzzy controllers in real time. International Journal of Approximate Reasoning, 1999, 21, 109-136.	1.9	19
43	An Accurate Measure for Multilayer Perceptron Tolerance to Weight Deviations. Neural Processing Letters, 1999, 10, 121-130.	2.0	17
44	Implementation of adaptable and hierarchical fuzzy T-norm. Electronics Letters, 1999, 35, 2150.	0.5	1
45	Title is missing!. Neural Processing Letters, 1998, 8, 55-65.	2.0	12
46	Parallel Coarse Grain Computing of Boltzmann Machines. Neural Processing Letters, 1998, 7, 169-184.	2.0	4
47	Focal-Plane and Multiple Chip VLSI Approaches to CNNs. Analog Integrated Circuits and Signal Processing, 1998, 15, 263-275.	0.9	8
48	Neural net approach for blind separation of sources based on geometric properties. Neurocomputing, 1998, 18, 141-164.	3.5	40
49	Analysis of the operators involved in the definition of the implication functions and in the fuzzy inference process. International Journal of Approximate Reasoning, 1998, 19, 367-389.	1.9	27
50	A neural learning algorithm for blind separation of sources based on geometric properties. Signal Processing, 1998, 64, 315-331.	2.1	27
51	Area efficient implementations of fixed-template CNN's. IEEE Transactions on Circuits and Systems Part 1: Regular Papers, 1998, 45, 968-973.	0.1	5
52	Compact CMOS fuzzy controllers using the normalised product of adaptive membership functions. Electronics Letters, 1997, 33, 221.	0.5	2
53	Geometric approach for blind separation of signals. Electronics Letters, 1997, 33, 835.	0.5	4
54	A low-power CMOS implementation of programmable CNN's with embedded photosensors. IEEE Transactions on Circuits and Systems Part 1: Regular Papers, 1997, 44, 149-153.	0.1	7

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55	VLSI Implementation of a Neural Model Using Spikes. Analog Integrated Circuits and Signal Processing, 1997, 13, 111-121.	0.9	16
56	An adaptive geometrical procedure for blind separation of sources. Neural Processing Letters, 1995, 2, 23-27.	2.0	25
57	Separation of sources: A geometry-based procedure for reconstruction of n-valued signals. Signal Processing, 1995, 46, 267-284.	2.1	36
58	Proteinotopic feature maps. Neurocomputing, 1994, 6, 443-454.	3.5	128
59	Generalized Hopfield neural network for concurrent testing. IEEE Transactions on Computers, 1993, 42, 898-912.	2.4	9
60	Test-pattern generation based on Reed-Muller coefficients. IEEE Transactions on Computers, 1993, 42, 968-980.	2.4	12
61	Current-mode analogue defuzzifier. Electronics Letters, 1993, 29, 743.	0.5	17
62	Analog CMOS implementation of a discrete time CNN with programmable cloning templates. IEEE Transactions on Circuits and Systems Part 2: Express Briefs, 1993, 40, 215-218.	2.3	25
63	Using Reed-Muller coefficients to synthesise optimal prediction modules for concurrent testing. Electronics Letters, 1991, 27, 1243.	0.5	2
64	CMOS current-mode multivalued PLAs. IEEE Transactions on Circuits and Systems, 1991, 38, 434-441.	0.9	25
65	Universal built-in self-test procedure for CMOS PLA's. IEEE Transactions on Circuits and Systems, 1991, 38, 941-945.	0.9	1
66	Implementation and applications of multivalued decoders. International Journal of Electronics, 1991, 70, 785-794.	0.9	1
67	Characterization and design of hybrid-mode CMOS circuits. International Journal of Electronics, 1991, 71, 591-607.	0.9	0
68	Some improvements in the implementation of multithreshold and multivalued I2.L circuits. International Journal of Electronics, 1989, 66, 19-34.	0.9	3
69	Multithreshold logic circuits implemented with operational amplifiers. International Journal of Electronics, 1985, 58, 395-406.	0.9	1
70	Distribution of natural frequencies in electrical ladder networks. Proceedings of the IEEE, 1983, 71, 773-775.	16.4	0
71	Calculation of potential distribution in homogeneous anisotropic nonlinear dielectrics. Journal of Applied Physics, 1983, 54, 6610-6614.	1.1	1
72	Design of active circuits with non-linear transfer characteristics. International Journal of Electronics, 1983, 54, 813-824.	0.9	3

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73	Implementation of the unary operators in ternary logic : A universal CMOS circuit. International Journal of Electronics, 1982, 52, 307-311.	0.9	4
74	Numerical calculation of some potential distributions in non-linear dielectrics. Journal of Electrostatics, 1982, 13, 139-150.	1.0	1
75	C.M.O.S. circuit for implementation of unary operators in ternary logic. Electronics Letters, 1980, 16, 161.	0.5	6
76	VLSI implementations of CNNs for image processing and vision tasks: single and multiple chip approaches. , 0, , .		6
77	Design and evaluation of a reconfigurable digital architecture for self-organizing maps. , 0, , .		4