Rajagopalan Devanathan

List of Publications by Year in descending order

Source: https://exaly.com/author-pdf/2745874/publications.pdf

Version: 2024-02-01

1307366 1125617 50 231 13 7 citations g-index h-index papers 50 50 50 111 docs citations times ranked citing authors all docs

#	Article	IF	Citations
1	Pansharpening using data-centric optimization approach. International Journal of Remote Sensing, 2019, 40, 7784-7804.	1.3	1
2	Study of Performance of Bio- Inspired Strategies Applied to Pursuit Evasion Game Under Feedback Laws. Advances in Science, Technology and Engineering Systems, 2019, 4, 207-219.	0.4	O
3	Algorithms for Multiplierless Multiple Constant Multiplication in Online Arithmetic. Circuits, Systems, and Signal Processing, 2018, 37, 5127-5142.	1.2	4
4	Pansharpening Using Data Driven Model Based on Linear Regression. , 2018, , .		1
5	An Analysis of Quadratic Linearization of Three Phase Horizontal Gravity Separator. , 2018, , .		0
6	Performance Improvement of Bio-Inspired Strategies Through Feedback Laws. Advances in Intelligent Systems and Computing, 2018, , 143-156.	0.5	2
7	Necessary and sufficient condition for modified Nevanlinna-Pick interpolation for closed-loop pole placement. Control Theory and Technology, 2017, 15, 58-68.	1.0	O
8	Quadratic linearization of three phase horizontal gravity separator. , 2017, , .		0
9	Modeling and robust diagnosis of power system protection failures using observations in discrete event system. , 2016, , .		1
10	Diagnosis of power system failures using observer based discrete event system. , 2016, , .		4
11	Internal model control of PMSM using discrete event system approach. , 2015, , .		O
12	Modeling a system using observations in discrete event system for failure diagnosis., 2015,,.		2
13	Modified N–P Interpolation Theory for Closed Loop Pole Placement. Asian Journal of Control, 2015, 17, 1880-1888.	1.9	1
14	Robust diagnosis of power system failures using discrete event system approach. , 2015, , .		3
15	Adoption of Park's Transformation for Inverter Fed Drive. International Journal of Power Electronics and Drive Systems, 2015, 5, 366.	0.5	6
16	Radix-2 ^h online floating point multipliers. , 2014, , .		1
17	Fault Diagnosis of PMSM Using Artificial Neural Network. International Review on Modelling and Simulations, 2014, 7, 760.	0.2	2
18	Hybrid Re-Clustering Algorithm for Enhancement of Network Lifetime in Wireless Sensor Networks. International Review on Computers and Software, 2014, 9, 1548.	0.1	0

#	Article	IF	Citations
19	Analysis and Application of Quadratic Linearization to the Control of Permanent Magnet Synchronous Motor. International Journal on Electrical Engineering and Informatics, 2014, 6, 644-664.	0.3	2
20	Explicit Necessary and Sufficient Conditions for Quadratic Linearization. Asian Journal of Control, 2013, 15, 225-236.	1.9	4
21	Indirect torque control of PMSM using hybrid system approach. , 2012, , .		O
22	Modified nevanlinna - pick interpolation theory for control system design. , 2012, , .		1
23	Comparative analysis and simulation of PWM and SVPWM inverter fed permanent magnet synchronous motor. , 2012, , .		10
24	Application of quadratic linearization to control of Permanent Magnet synchronous motor., 2011,,.		1
25	A New Linearisation Technique for Permanent Magnet Synchronous Motor Model. , 2008, , .		O
26	Robust Control of Permanent Magnet Synchronous Motor: Nevanlinna-Pick Approach., 2008,,.		2
27	Complete Quadratic Linearisation of Machine Models. Control Applications (CCA), Proceedings of the IEEE International Conference on, 2007, , .	0.0	1
28	Linearisation of permanent magnet synchronous motor model. , 2006, , .		0
29	Linearisation of Permanent Magnet Synchronous Motor Model. , 2006, , .		3
30	Necessary and sufficient conditions for quadratic linearization of a linearly controllable system. International Journal of Control, 2004, 77, 613-621.	1.2	8
31	Vision based neurofuzzy logic control of weld pool geometry. Science and Technology of Welding and Joining, 2002, 7, 321-325.	1.5	19
32	Linearization condition through state feedback. IEEE Transactions on Automatic Control, 2001, 46, 1257-1260.	3.6	23
33	Neural network approach for linearizing control of nonlinear process plants. IEEE Transactions on Industrial Electronics, 2000, 47, 470-477.	5.2	20
34	Pattern-based identification for process control applications. IEEE Transactions on Control Systems Technology, 1996, 4, 641-648.	3.2	6
35	A temporal logic approach to discrete event control for the safety canonical class. Systems and Control Letters, 1996, 28, 205-217.	1.3	17
36	Feedback linearisation of a heat exchanger. Systems and Control Letters, 1995, 26, 203-209.	1.3	6

#	Article	IF	Citations
37	A lower bound for limiting time delay for closed-loop stability of an arbitrary SISO plant. IEEE Transactions on Automatic Control, 1995, 40, 717-721.	3 . 6	14
38	A temporal framework for assembly sequence representation and analysis. IEEE Transactions on Automation Science and Engineering, 1994, 10, 220-229.	2.4	32
39	Temporal logic programming for assembly sequence planning. Advanced Engineering Informatics, 1993, 8, 253-263.	0.5	7
40	An expert autotuner for multiloop SISO controllers. Control Engineering Practice, 1993, 1, 999-1008.	3.2	5
41	Robust stabilization of a SISO system with uncertainty in time delay. IEEE Transactions on Automatic Control, 1992, 37, 1820-1823.	3 . 6	4
42	EXPERT AUTO-TUNER FOR MULTIVARIABLE CONTROL APPLICATIONS. , 1992, , 267-272.		3
43	A Case for Sequential Control Systems and Programmable Logic Controllers in a University Curriculum. International Journal of Electrical Engineering and Education, 1990, 27, 218-225.	0.4	1
44	Computer Aided Design Of Relay Ladder Logic Via State Transition Diagram. Proceedings of SPIE, 1987, , .	0.8	5
45	Continuously equivalent networks in state space. Electronics Letters, 1973, 9, 372.	0.5	2
46	Process Identification through Stable Closed Loop Response. , 0, , .		0
47	Relay Ladder Logic Design Using The Language Of Regular Expressions. , 0, , .		2
48	Some optimum properties of PID regulators useful for expert system applications. , 0, , .		2
49	Assembly Sequence Planning for Odd Form Component Insertion on Printed Circuit Boards Using and/or Graphs., 0,,.		3
50	A Temporal Logic Framework For Assembly Sequence Planning. , 0, , .		0