Ganapathi Hegde

List of Publications by Year
in descending order

Source: https://exaly.com/author-pdf/2740510/publications.pdf

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16	43	2258059 3	2053705 5
papers	citations	h-index	g-index

16	16	16	23
all docs	docs citations	times ranked	citing authors

#	Article	IF	Citations
1	High Throughput Pipelined S-Boxes for Encryption and Watermarking Applications. , 2020, , .		0
2	FPGA Implementation of 8-bit SSA Multiplier for designing OFDM Transceiver. , 2019, , .		2
3	Improving the Reliability of Embedded Memories using ECC and Built-In Self-Repair Techniques. , 2018, , .		3
4	High performance VLSI architecture for 3-D DWT (discrete Wavelet Transform). , 2018, , .		5
5	An approach for area and power optimization of flipping 3-D discrete wavelet transform architecture. , 2017, , .		1
6	An efficient hardware realization of diamond search algorithm for motion estimation task in video compression applications. , 2017, , .		1
7	An efficient hybrid integer coefficient-DCT architecture using quantization module for HEVC standard. , 2017, , .		1
8	A Novel Low Voltage Hybrid Phase Locked Loop. , 2017, , .		0
9	VLSI implementation of the video encoder using an efficient 3-D DCT algorithm. International Journal of Electronics Letters, 2016, 4, 38-49.	1.2	3
10	High performance VLSI architecture for 2-D DWT using lifting scheme. , 2015, , .		2
11	Conservative Approximationâ€“Based Full-Search Block Matching Algorithm Architecture for QCIF Digital Video Employing Systolic Array Architecture. ETRI Journal, 2015, 37, 772-779.	2.0	0
12	A parallel 3-D discrete wavelet transform architecture using pipelined lifting scheme approach for video coding. International Journal of Electronics, 2013, 100, 1429-1440.	1.4	3
13	Systolic array based motion estimation architecture of 3D DWT sub band component for video processing. International Journal of Signal and Imaging Systems Engineering, 2012, 5, 158.	0.6	3
14	An efficient hardware model for RSA Encryption system using Vedic mathematics. Procedia Engineering, 2012, 30, 124-128.	1.2	16
15	An efficient 3-dimensional discrete wavelet transform architecture for video processing application. Journal of Electronics, 2012, 29, 534-540.	0.2	1
16	An Efficient Distributive Arithmetic Based 3-Dimensional Discrete Wavelet Transform for Video Processing. , 2011, , .		2