

# Shen Xu

## List of Publications by Year in descending order

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58  
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docs citations

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times ranked

322  
citing authors

#	ARTICLE	IF	CITATIONS
1	Modelling of Quasi-parallel Sigma DC-DC Converter for High Efficiency Single-stage Voltage Regulator. , 2022, , .		0
2	A Digital Adaptive Control Method for Optimizing Valley Current of Active Clamp Flyback Converter. IEEE Journal of Emerging and Selected Topics in Power Electronics, 2022, 10, 5749-5761.	3.7	2
3	An Autotuning Method Based on System Identification for Digitally Controlled Synchronous Buck Converter. IEEE Journal of Emerging and Selected Topics in Power Electronics, 2021, 9, 3307-3321.	3.7	7
4	A Novel Digital Control Method of Primary-Side Regulated Flyback With Active Clamping Technique. IEEE Transactions on Circuits and Systems I: Regular Papers, 2021, 68, 950-962.	3.5	9
5	A High-Power-Density Four-switch Buck-boost Converter using 3D Multi-PCB Structure. , 2021, , .		2
6	Numerical analysis on the effect of microstructures on the thermal and mechanical properties of carbon fiber / Al <sub>2</sub> O <sub>3</sub> thermal pad. , 2021, , .		0
7	Fast Transient Response Based on Single-cycle Charge Regulator (SCCR) Control to Realize Fast Recovery Process. , 2021, , .		1
8	Startup control strategy of single-stage 48V/1.8V Sigma Converter. , 2021, , .		1
9	A Digital Control Scheme for PSR Flyback Converter in CCM and DCM. IEEE Journal of Emerging and Selected Topics in Power Electronics, 2020, 8, 2837-2849.	3.7	16
10	Small-signal modelling for time-length compensation algorithm in current controlled converters. International Journal of Circuit Theory and Applications, 2020, 48, 148-155.	1.3	1
11	Optimal Phase Shift Control Strategy of Buck-Boost Integrated LLC Converter Achieving Wide Input Voltage Range, MHz-frequency and High Efficiency. , 2020, , .		8
12	A New Modulation Strategy for Four-switch Buck-boost Converter with Reduced Freewheeling Current. , 2020, , .		11
13	New Digital Control Method for Improving Dynamic Response of Synchronous Rectified PSR Flyback Converter With CCM and DCM Modes. IEEE Transactions on Power Electronics, 2020, 35, 12347-12358.	5.4	5
14	Sample-Data Modeling for Active Clamp Flyback Converter in Critical Conduction Mode with PCM and ZVS control at Variable Switching frequency. , 2020, , .		1
15	Improved Control of GaN-based Active-clamped Flyback Converter with Shorter Reverse Conduction Time. , 2020, , .		0
16	An Improved Peak Current Control Method for GaN-based Active-clamped Flyback Converter. , 2020, , .		2
17	A single-switched high-switching-frequency quasi-resonant flyback converter with zero-current-switching and valley-switching. , 2019, , .		8
18	New digital control method for improving dynamic performance of a quasi-resonant flyback converter. , 2019, , .		1

#	ARTICLE	IF	CITATIONS
19	An efficiency optimization method for a high frequency quasi-ZVS controlled resonant flyback converter. , 2019, , .		2
20	An Evolutionary Method to Achieve the Maximum Efficiency Tracking with Multi-Objective Optimization Based on the Genetic Algorithm. , 2019, , .		1
21	A Single-Switched High-Switching-Frequency Quasi-Resonant Flyback Converter. IEEE Transactions on Power Electronics, 2019, 34, 8775-8786.	5.4	25
22	A Low-Cost Constant Current Control Method for DCM and CCM in Digitally Controlled Primary-Side Regulation Flyback Converter. IEEE Journal of Emerging and Selected Topics in Power Electronics, 2018, 6, 1483-1494.	3.7	31
23	A simple average current control with time-length equality for primary-side regulation flyback converter with constant output current control. International Journal of Circuit Theory and Applications, 2018, 46, 2477-2494.	1.3	7
24	New digital control method for improving dynamic response of synchronous rectified flyback converter with CCM and DCM mode. , 2018, , .		8
25	Novel Digital Control Method for Improving Dynamic Responses of Multimode Primary-Side Regulation Flyback Converter. IEEE Transactions on Power Electronics, 2017, 32, 1457-1468.	5.4	22
26	Radiated EMI study on super-junction VDMOS in flyback power converters. International Journal of Circuit Theory and Applications, 2017, 45, 1313-1325.	1.3	1
27	A time-length compensation algorithm for sub-harmonic oscillation elimination in digital controlled primary-side regulation flyback converter. , 2017, , .		1
28	A novel digital multi-mode control strategy with PSM for primary-side flyback converter. International Journal of Electronics, 2017, 104, 840-854.	0.9	4
29	Analysis of a Time-Length Compensation Algorithm for Elimination of Subharmonic Oscillation and Application in a Digitally Controlled Primary-Side Regulation Flyback Converter. IEEE Journal of Emerging and Selected Topics in Power Electronics, 2017, 5, 1710-1719.	3.7	9
30	Novel Hybrid Analytical/Numerical Conducted EMI Model of a Flyback Converter. IEEE Transactions on Electromagnetic Compatibility, 2017, 59, 488-497.	1.4	7
31	An accurate design method of RCD circuit for flyback converter considering diode reverse recovery. , 2016, , .		7
32	Hybrid modulation scheme for dual active bridge converter that employs the triangular modulation and the single phase shift modulation. International Journal of Circuit Theory and Applications, 2016, 44, 1982-2002.	1.3	6
33	Digital regulation scheme for multimode primary-side controlled flyback converter. IET Power Electronics, 2016, 9, 782-788.	1.5	14
34	Accurate model of switched reluctance motor based on indirect measurement method and least square support vector machine. IET Electric Power Applications, 2016, 10, 916-922.	1.1	29
35	High precision constant voltage digital control scheme for primary-side controlled flyback converter. IET Power Electronics, 2016, 9, 2522-2533.	1.5	19
36	A novel lumped-parameter model of crosstalk between vias in high-speed PCBs. Microwave and Optical Technology Letters, 2016, 58, 2088-2090.	0.9	0

#	ARTICLE	IF	CITATIONS
37	A new digital predictive control strategy for boost PFC converter. IEICE Electronics Express, 2015, 12, 20150726-20150726.	0.3	3
38	Zeroâ€steadyâ€stateâ€error compensation method in application of peak current mode buck converter with fast transient response. IET Power Electronics, 2015, 8, 647-655.	1.5	5
39	An electrolytic capacitorâ€less LED driver with interleaving flyback topology. International Journal of Circuit Theory and Applications, 2015, 43, 2025-2038.	1.3	6
40	Analysis of commonâ€mode electromagnetic interference noise in a flyback converter using a selfâ€supply power control integrated circuit. IET Power Electronics, 2015, 8, 1749-1757.	1.5	8
41	Analysis and accurate modeling of a flyback converter on conducted EMI. , 2015, , .		7
42	Modeling and analysis of primary side regulated flyback converter with pulse frequency modulation. , 2015, , .		4
43	A High-Frequency Model for a PCM Buck Converter. IEEE Transactions on Power Electronics, 2015, 30, 2304-2312.	5.4	9
44	A Ripple Control Dual-Mode Single-Inductor Dual-Output Buck Converter With Fast Transient Response. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2015, 23, 107-117.	2.1	21
45	Transient response optimisation for peak current mode buck converter in the application of dynamic voltage scaling. IET Power Electronics, 2014, 7, 705-712.	1.5	8
46	A digital control algorithm for single-phase boost PFC converter with fast dynamic response. IEICE Electronics Express, 2014, 11, 20140493-20140493.	0.3	0
47	Modelling of $L di/dt$ effect with frequency spectrum analysis and parameter design in float ground driver system. IET Circuits, Devices and Systems, 2014, 8, 442-449.	0.9	0
48	Digital Controller for Single-Phase DCM Boost PFC Converter with High Power Factor over Wide Input Voltage and Load Range. IEICE Transactions on Electronics, 2014, E97.C, 377-385.	0.3	2
49	An improved model of switched reluctance motors based on least square support vector machine. , 2013, , .		4
50	Analytical model for energy recovery circuit of plasma display panel data driver integrated circuit. IET Circuits, Devices and Systems, 2013, 7, 74-80.	0.9	1
51	A novel digital controller for boost PFC converter with high power factor and fast dynamic response. , 2013, , .		3
52	Power loss analysis of active clamp forward converter in continuous conduction mode and discontinuous conduction mode operating modes. IET Power Electronics, 2013, 6, 1142-1150.	1.5	14
53	VCCS Models of DPLED MOS for PDP Data Driver IC. IEICE Transactions on Electronics, 2013, E96.C, 1061-1067.	0.3	0
54	Analytic $L di/dt$ Effect Model Based on Float Ground in Plasma Display Panel Driver System. IEICE Transactions on Electronics, 2013, E96.C, 1428-1435.	0.3	0

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55	A dual-mode single-inductor dual-output dc-dc converter with fast transient response. IEICE Electronics Express, 2012, 9, 1780-1785.	0.3	4
56	A 65nm 10MHz single-inductor dual-output switching buck converter with time-multiplexing control. , 2011, , .		4
57	Analysis of switching noise for an output driver circuit in an application. , 2011, , .		0
58	Low power design for SoC with power management unit. , 2011, , .		3