

Alex F Kirichenko

List of Publications by Year in descending order

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254
citing authors

#	ARTICLE	IF	CITATIONS
1	Interfacing Superconducting Qubits With Cryogenic Logic: Readout. IEEE Transactions on Applied Superconductivity, 2019, 29, 1-5.	1.7	21
2	ERSFQ 8-bit Parallel Binary Shifter for Energy-Efficient Superconducting CPU. IEEE Transactions on Applied Superconductivity, 2019, 29, 1-4.	1.7	5
3	Diagnosis of Factors Impacting Yield in Multilayer Devices for Superconducting Electronics. IEEE Transactions on Applied Superconductivity, 2019, 29, 1-4.	1.7	1
4	ERSFQ 8-Bit Parallel Arithmetic Logic Unit. IEEE Transactions on Applied Superconductivity, 2019, 29, 1-7.	1.7	31
5	Effects of Adaptive DC Biasing on Operational Margins in ERSFQ Circuits. IEEE Transactions on Applied Superconductivity, 2017, 27, 1-6.	1.7	17
6	Energy-Efficient and Compact ERSFQ Decoder for Cryogenic RAM. IEEE Transactions on Applied Superconductivity, 2017, 27, 1-5.	1.7	17
7	ERSFQ 8-Bit Parallel Adders as a Process Benchmark. IEEE Transactions on Applied Superconductivity, 2015, 25, 1-5.	1.7	31
8	ERSFQ 4-to-16 Decoder for Energy-Efficient RAM. IEEE Transactions on Applied Superconductivity, 2015, 25, 1-4.	1.7	13
9	20GHz Operation of an Asynchronous Wave-Pipelined RSFQ Arithmetic-Logic Unit. Physics Procedia, 2012, 36, 59-65.	1.2	94
10	Zero Static Power Dissipation Biasing of RSFQ Circuits. IEEE Transactions on Applied Superconductivity, 2011, 21, 776-779.	1.7	250