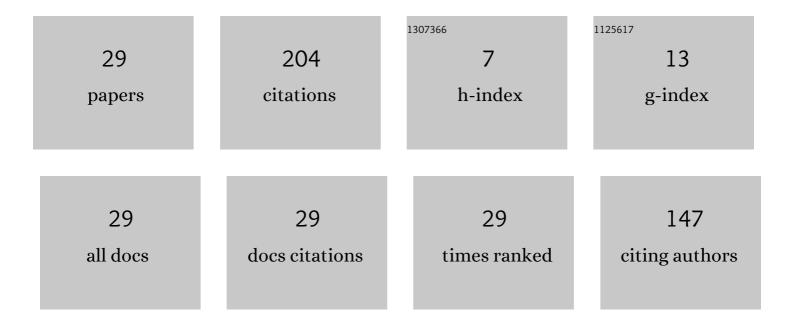
Yuen-Haw Chang

List of Publications by Year in descending order

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#	Article	IF	CITATIONS
1	Analysis, design and implementation of serial-parallel-switched-capacitor coupled-inductor boost DC-AC inverter. International Journal of Electronics, 2021, 108, 795-818.	0.9	2
2	Controller Chip Design of Coupled-Inductor Cockcroft-Walton-Switched-Capacitor Boost DC-AC Inverter. , 2021, , .		0
3	Design and analysis of coupled-inductor switched-capacitor boost DC-AC inverter. International Journal of Electronics, 2020, 107, 288-309.	0.9	3
4	Controller Chip Design of Serial-Parallel-Switched-Capacitor Coupled-Inductor Boost DC-AC Inverter. , 2020, , .		0
5	Modeling and Implementation of Active-Harmonic-Elimination Switched-Capacitor Boost DC-AC Inverter. , 2019, , .		1
6	Design and analysis of switched-coupled-inductor switched-capacitor step-up DC-DC converter. International Journal of Electronics, 2019, 106, 1726-1745.	0.9	15
7	Design and Analysis of High-Gain Switched-Capacitor-Inductor-Based Inverter for Step-Up DC-AC Conversion. Journal of Circuits, Systems and Computers, 2018, 27, 1850030.	1.0	12
8	Modeling and implementation of high-gain coupled-inductor switched-capacitor step-up DC-DC converter. , 2018, , .		3
9	Design and Analysis of AMSCC for Piezoelectric Energy Harvesting. Journal of Circuits, Systems and Computers, 2017, 26, 1750083.	1.0	2
10	A switchâ€utilizationâ€improved switchedâ€inductor switchedâ€capacitor converter with adapting stage number. International Journal of Circuit Theory and Applications, 2016, 44, 709-728.	1.3	23
11	Modeling and implementation of high-gain switched-inductor switched-capacitor converter. , 2014, , .		7
12	A gain/efficiencyâ€enhanced bidirectional switchedâ€capacitor DCâ€DC converter. International Journal of Circuit Theory and Applications, 2014, 42, 468-493.	1.3	18
13	Switchedâ€capacitorâ€voltageâ€multiplier boost DC–AC inverter with adaptive stages. International Journal of Circuit Theory and Applications, 2013, 41, 128-149.	1.3	1
14	A Gain/Efficiency-Improved Serial-Parallel Switched-Capacitor Step-Up DC–DC Converter. IEEE Transactions on Circuits and Systems I: Regular Papers, 2013, 60, 2799-2809.	3.5	28
15	A simple two-phase cell-gain-multiplication switched-capacitor bidirectional converter. International Journal of Electronics, 2013, 100, 1207-1227.	0.9	2
16	HIGH-GAIN/EFFICIENCY MULTISTAGE SWITCHED-CAPACITOR-VOLTAGE-MULTIPLIER DC–DC CONVERTER. Journal of Circuits, Systems and Computers, 2012, 21, 1250023.	1.0	0
17	Generalisedmc × nc-stage switched-capacitor-voltage-multiplier-based boost DC–AC inverter. International Journal of Electronics, 2012, 99, 29-53.	0.9	0
18	Multistage multiphase switchedâ€capacitor DC–DC converter with variableâ€phase and PWM control. International Journal of Circuit Theory and Applications, 2012, 40, 835-857.	1.3	6

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#	ARTICLE	IF	CITATIONS
19	Threeâ€stage multiphaseâ€switchedâ€capacitor boost DCâ€AC inverter. International Journal of Circuit Theory and Applications, 2012, 40, 877-902.	1.3	0
20	Variable-Conversion-Ratio Switched-Capacitor-Voltage-Multiplier/Divider DC-DC Converter. IEEE Transactions on Circuits and Systems I: Regular Papers, 2011, 58, 1944-1957.	3.5	31
21	Modelling and analysis of multistage switched-capacitor-voltage-multiplier boost DC-AC inverter. , 2011, , .		6
22	HIGH-EFFICIENCY 2-STAGE MULTIPHASE SWITCHED-CAPACITOR CONVERTER VIA VARIABLE-PHASE AND PWM CONTROL. Journal of Circuits, Systems and Computers, 2010, 19, 1753-1780.	1.0	0
23	Design and analysis of a generalisedn-stage current-mode multiphase switched-capacitor converter. International Journal of Electronics, 2010, 97, 737-757.	0.9	4
24	Implementation and Design of CPLD-Based Switched-Capacitor Step-Down DC-DC Converter with Multiple Output Choice. , 2007, , .		1
25	Design and analysis of power-CMOS-gate-based switched-capacitor DC-DC converter with step-down and step-up modes. International Journal of Circuit Theory and Applications, 2003, 31, 483-511.	1.3	18
26	Frequency-domain grouping robust fault diagnosis for analog circuits with uncertainties. International Journal of Circuit Theory and Applications, 2002, 30, 65-86.	1.3	17
27	The relaxation pseudocircuit method for analogue fault diagnosis. International Journal of Circuit Theory and Applications, 1996, 24, 201-221.	1.3	3
28	Design and analysis of multiphase switched-capacitor coupled-inductor converter for high-gain DC-DC conversion. International Journal of Electronics, 0, , 1-24.	0.9	0
29	Design and Analysis of Coupled-Inductor Cockcroft-Walton-Switched-Capacitor Boost DC-AC Inverter. International Journal of Electronics, 0, , .	0.9	1