List of Publications by Year
in descending order

13 papers	**707** citations	1478505 **6** h-index	1474206 **9** g-index
13 all docs	**13** docs citations	**13** times ranked	**657** citing authors

#	Article	IF	Citations
1	INCAME: Interruptible CNN Accelerator for Multirobot Exploration. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2022, 41, 964-978.	2.7	2
2	A Unified FPGA Virtualization Framework for General-Purpose Deep Neural Networks in the Cloud. ACM Transactions on Reconfigurable Technology and Systems, 2022, 15, 1-31.	2.5	2
3	Reliability evaluation of FPGA based pruned neural networks. Microelectronics Reliability, 2022, 130, 114498.	1.7	4
4	A Survey of FPGA-Based Robotic Computing. IEEE Circuits and Systems Magazine, 2021, 21, 48-74.	2.3	38
5	Robotic Computing on FPGAs. Synthesis Lectures on Computer Architecture, 2021, 16, 1-218.	1.3	7
6	Ensemble of Pruned Networks for Reliable Classifiers. , 2021, , .		2
7	[DL] A Survey of FPGA-based Neural Network Inference Accelerators. ACM Transactions on Reconfigurable Technology and Systems, 2019, 12, 1-26.	2.5	150
8	Angel-Eye: A Complete Design Flow for Mapping CNN Onto Embedded FPGA. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2018, 37, 35-47.	2.7	382
9	Hardware Trojan Detection in Third-Party Digital Intellectual Property Cores by Multilevel Feature Analysis. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2018, 37, 1370-1383.	2.7	42
10	Real-Time Object Detection and Semantic Segmentation Hardware System with Deep Learning Networks. , 2018, , .		13
11	Design of fault-tolerant neuromorphic computing systems. , 2018, , .		14
12	A General Framework for Hardware Trojan Detection in Digital Circuits by Statistical Learning Algorithms. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2017, 36, 1633-1646.	2.7	23
13	FASTrust: Feature analysis for third-party IP trust verification. , 2015, , .		28