

Shambhu Upadhyaya

List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/2680074/publications.pdf>

Version: 2024-02-01

30
papers

263
citations

1478280

6
h-index

1372474

10
g-index

30
all docs

30
docs citations

30
times ranked

250
citing authors

#	ARTICLE	IF	CITATIONS
1	Game Theoretic Analysis of Ransomware: A Preliminary Study. , 2022, , .		3
2	Inside the Insider. IEEE Engineering Management Review, 2019, 47, 84-91.	1.0	8
3	Cybersecurity Interventions for Teens: Two Time-Based Approaches. IEEE Transactions on Education, 2019, 62, 134-140.	2.0	6
4	Synthetic Forgery Attack against Continuous Keystroke Authentication Systems. , 2018, , .		2
5	Similarity Metrics for SQL Query Clustering. IEEE Transactions on Knowledge and Data Engineering, 2018, 30, 2408-2420.	4.0	44
6	Sensitivity analysis in keystroke dynamics using convolutional neural networks. , 2017, , .		12
7	Adaptive techniques for intra-user variability in keystroke dynamics. , 2016, , .		5
8	Effects of text filtering on authentication performance of keystroke biometrics. , 2016, , .		9
9	Twitter structure as a composition of two distinct networks. , 2016, , .		0
10	Enhanced recognition of keystroke dynamics using Gaussian mixture models. , 2015, , .		21
11	Advances in secure knowledge management in the big data era. Information Systems Frontiers, 2015, 17, 983-986.	4.1	29
12	Secure and privacy preserving data processing support for active authentication. Information Systems Frontiers, 2015, 17, 1007-1015.	4.1	13
13	Minimum Cost Blocking Problem in Multi-Path Wireless Routing Protocols. IEEE Transactions on Computers, 2014, 63, 1765-1777.	2.4	5
14	Analysis of Malware Propagation in Twitter. , 2013, , .		14
15	SESAME: Smartphone enabled secure access to multiple entities. , 2013, , .		5
16	Data De-duplication and Event Processing for Security Applications on an Embedded Processor. , 2012, , .		0
17	Accelerated Processing of Secure Email by Exploiting Built-in Security Features on the Intel EP80579 Integrated Processor with Intel QuickAssist Technology. , 2011, , .		0
18	Situation Awareness of multistage cyber attacks by semantic event fusion. , 2010, , .		9

#	ARTICLE	IF	CITATIONS
19	Attack scenario recognition through heterogeneous event stream analysis. , 2009, , .		2
20	An exploration of unintended online private information disclosure in educational institutions across four countries. , 2009, , .		2
21	Design Considerations for High Performance RF Cores Based on Process Variation Study. Journal of Electronic Testing: Theory and Applications (JETTA), 2008, 24, 143-155.	0.9	1
22	Defect Analysis and Defect Tolerant Design of Multi-port SRAMs. Journal of Electronic Testing: Theory and Applications (JETTA), 2008, 24, 165-179.	0.9	5
23	ASFALT: A Simple Fault-Tolerant Signature-based Localization Technique for Emergency Sensor Networks. , 2007, , .		3
24	Design principles for critical incident response systems. Information Systems and E-Business Management, 2007, 5, 201-227.	2.2	38
25	ASFALT: A Simple Fault-Tolerant Signature-based Localization Technique for Emergency Sensor Networks. Proceedings of the IEEE Symposium on Reliable Distributed Systems, 2007, , .	0.0	0
26	A 3-port Register File Design for Improved Fault Tolerance on Resistive Defects in Core-Cells. , 2006, , .		0
27	On the Hardness of Approximating the Min-Hack Problem. Journal of Combinatorial Optimization, 2005, 9, 295-311.	0.8	2
28	Intrusion countermeasures security model based on prioritization scheme for intranet access security (emerging concepts category). , 0, , .		0
29	Security Policies to Mitigate Insider Threat in the Document Control Domain. , 0, , .		21
30	AVARE: Aggregated Vulnerability Assessment and Response against Zero-day Exploits. , 0, , .		4