

Rogério Paludo

List of Publications by Year in descending order

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5
papers

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citations

2682572

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2917675

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all docs

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docs citations

5
times ranked

16
citing authors

#	ARTICLE	IF	CITATIONS
1	NTT Architecture for a Linux-Ready RISC-V Fully-Homomorphic Encryption Accelerator. IEEE Transactions on Circuits and Systems I: Regular Papers, 2022, 69, 2669-2682.	5.4	14
2	Number Theoretic Transform Architecture suitable to Lattice-based Fully-Homomorphic Encryption. , 2021, , .		3
3	Efficient RNS Reverse Converters for Moduli Sets with Dynamic Ranges Up to $(10n+1)$ -bit. Circuits, Systems, and Signal Processing, 2018, 37, 5178-5196.	2.0	2
4	Efficient implementation of modular multiplication by constants applied to RNS reverse converters. , 2017, , .		9
5	A methodology for early functional verification of embedded software combining virtual platforms and bounded model checking. , 2016, , .		0