

Rogério Paludo

List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/265308/publications.pdf>

Version: 2024-02-01

5
papers

28
citations

2682572

2
h-index

2917675

2
g-index

5
all docs

5
docs citations

5
times ranked

16
citing authors

| # | ARTICLE | IF | CITATIONS |
|---|---|-----|-----------|
| 1 | NTT Architecture for a Linux-Ready RISC-V Fully-Homomorphic Encryption Accelerator. IEEE Transactions on Circuits and Systems I: Regular Papers, 2022, 69, 2669-2682. | 5.4 | 14 |
| 2 | Efficient implementation of modular multiplication by constants applied to RNS reverse converters. , 2017, , . | | 9 |
| 3 | Number Theoretic Transform Architecture suitable to Lattice-based Fully-Homomorphic Encryption. , 2021, , . | | 3 |
| 4 | Efficient RNS Reverse Converters for Moduli Sets with Dynamic Ranges Up to $(10n+1)$ -bit. Circuits, Systems, and Signal Processing, 2018, 37, 5178-5196. | 2.0 | 2 |
| 5 | A methodology for early functional verification of embedded software combining virtual platforms and bounded model checking. , 2016, , . | | 0 |