Patricio Bulic

List of Publications by Year
in descending order

Source: https://exaly.com/author-pdf/2651348/publications.pdf

Version: 2024-02-01

18	256	1162889 8	1058333 14
papers	citations	h-index	g-index

19	19	19	260
all docs	docs citations	times ranked	citing authors

#	Article	IF	Citations
1	On the Design of an Energy Efficient Digital IIR A-Weighting Filter Using Approximate Multiplication. Sensors, 2021, 21, 732.	2.1	5
2	A Hybrid Radix-4 and Approximate Logarithmic Multiplier for Energy Efficient Image Processing. Electronics (Switzerland), 2021, 10, 1175.	1.8	11
3	An Approximate GEMM Unit for Energy-Efficient Object Detection. Sensors, 2021, 21, 4195.	2.1	5
4	A Two-Stage Operand Trimming Approximate Logarithmic Multiplier. IEEE Transactions on Circuits and Systems I: Regular Papers, 2021, 68, 2535-2545.	3.5	24
5	On the Design of Logarithmic Multiplier Using Radix-4 Booth Encoding. IEEE Access, 2020, 8, 64578-64590.	2.6	30
6	Data Transmission Efficiency in Bluetooth Low Energy Versions. Sensors, 2019, 19, 3746.	2.1	37
7	Compression of convolutional neural networks: A short survey. , 2018, , .		7
8	Accurate Indoor Sound Level Measurement on a Low-Power and Low-Cost Wireless Sensor Node. Sensors, 2018, 18, 2351.	2.1	19
9	Logarithmic Arithmetic for Low-Power Adaptive Control Systems. Circuits, Systems, and Signal Processing, 2017, 36, 3564-3584.	1.2	1
10	An FPGAâ€based integrated environment for computer architecture. Computer Applications in Engineering Education, 2013, 21, 26-35.	2.2	18
11	A GPU implementation of a structural-similarity-based aerial-image classification. Journal of Supercomputing, 2013, 65, 978-996.	2.4	7
12	Applicability of approximate multipliers in hardware neural networks. Neurocomputing, 2012, 96, 57-65.	3.5	50
13	An approximate method for filtering out data dependencies with a sufficiently large distance betweenÂmemoryÂreferences. Journal of Supercomputing, 2011, 56, 226-244.	2.4	1
14	A simple pipelined squaring circuit for DSP. , 2011, , .		7
15	A simple pipelined logarithmic multiplier. , 2010, , .		9
16	An Iterative Mitchell's Algorithm Based Multiplier. , 2008, , .		4
17	Learning computer architecture concepts with the FPGA-based â€œMoveâ€•microprocessor. Computer Applications in Engineering Education, 2006, 14, 135-141.	2.2	7
18	An Extended ANSI C for Processors with a Multimedia Extension. International Journal of Parallel Programming, 2003, 31, 107-136.	1.1	13