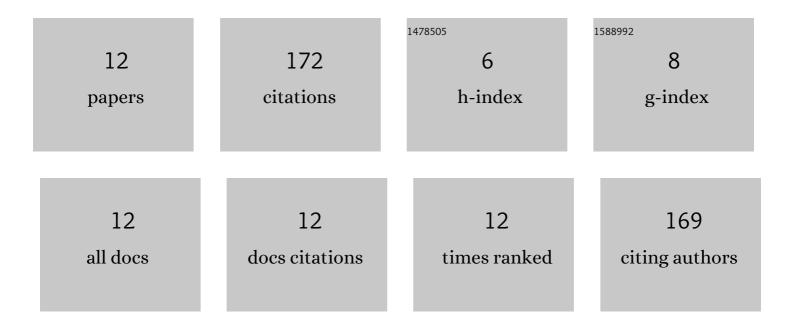
## Jianxin Zhu

List of Publications by Year in descending order

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ΙΙΔΝΥΙΝ ΖΗΠ

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#	Article	IF	CITATIONS
1	A Dual-DC Output Three-Phase Three-Level AC–DC Converter for Low-Frequency Pulsed Power Decoupling Applications. IEEE Transactions on Industrial Electronics, 2022, 69, 52-63.	7.9	7
2	Modified T-Type Three-Level AC–DC Converter Based Multifunctional Compensator for Three-Phase AC Power System With Low-Frequency Pulsed Load. IEEE Transactions on Industrial Electronics, 2022, 69, 11844-11855.	7.9	1
3	A Hybrid Three-Phase AC/DC Power System for Low-Frequency Pulsed Load Applications. IEEE Transactions on Industrial Electronics, 2021, 68, 1871-1882.	7.9	10
4	Hybrid Connected Unified Power Quality Conditioner Integrating Distributed Generation With Reduced Power Capacity and Enhanced Conversion Efficiency. IEEE Transactions on Industrial Electronics, 2021, 68, 12340-12352.	7.9	16
5	A Switch Partial Turned OFF Cycle by Cycle Current Limiting Method for 3L-NPC Half-Bridge Inverters. IEEE Access, 2020, 8, 15632-15639.	4.2	1
6	Quasi-Two-Stage Multifunctional Photovoltaic Inverter With Power Quality Control and Enhanced Conversion Efficiency. IEEE Transactions on Power Electronics, 2020, 35, 7073-7085.	7.9	29
7	A Three-Phase AC/DC Power System with Paralleled Active and Passive Rectifiers for Low-Frequency Pulsed Load Applications. , 2020, , .		3
8	Performance Evaluation of a Multifunctional Compensator for Three-Phase AC Power system with Pulsed Load. , 2019, , .		2
9	Evaluation of Voltage Controllers Based on Active Damping for the CVCF Power Converter under Nonlinear Load Condition. , 2018, , .		0
10	Clustered Voltage Balancing Mechanism and Its Control Strategy for Star-Connected Cascaded H-Bridge STATCOM. IEEE Transactions on Industrial Electronics, 2017, 64, 7623-7633.	7.9	45
11	Simple Zero-Sequence-Voltage-Based Cluster Voltage Balancing Control and the Negative Sequence Current Compensation Regulation Identification for Star-Connected Cascaded H-Bridge STATCOM. IEEE Transactions on Power Electronics, 2017, , 1-1.	7.9	54

12 A fully FPGA-based real-time simulator for the cascaded STATCOM. , 2016, , .