

# Vignesh vlsi

## List of Publications by Year in Descending Order

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This document has been generated based on the publications and citations recorded by exaly.com. For the latest version of this publication list, visit the link given above.

The third column is the impact factor (IF) of the journal, and the fourth column is the number of citations of the article.

17  
papers

53  
citations

3  
h-index

7  
g-index

31  
ext. papers

80  
ext. citations

1  
avg, IF

2.68  
L-index

#	Paper	IF	Citations
17	Daily evapotranspiration prediction using gradient boost regression model for irrigation planning. <i>Journal of Supercomputing</i> , <b>2020</b> , 76, 5732-5744	2.5	14
16	Characterization and modeling of dual material double gate tunnel field effect transistor using superposition approximation method. <i>Concurrency Computation Practice and Experience</i> , <b>2019</b> , 31, e4860	1.4	1
15	An adaptive low power coding scheme for the NoC. <i>International Journal of Advanced Intelligence Paradigms</i> , <b>2019</b> , 13, 324	0.5	
14	Design of high performance filter bank multi-carrier transmitter. <i>Cluster Computing</i> , <b>2019</b> , 22, 12521-12527	2.7	
13	Analog to information convertor using cascaded transform and Gaussian random matrix. <i>Cluster Computing</i> , <b>2019</b> , 22, 11813-11820	2.1	
12	Reliability improved, high performance FIR filter design using new computation sharing multiplier: suitable for signal processing applications. <i>Cluster Computing</i> , <b>2019</b> , 22, 13669-13681	2.1	1
11	Low-power and high-throughput 128-point feedforward FFT processor. <i>Cluster Computing</i> , <b>2019</b> , 22, 13397-13404	2.1	1
10	Analysis and Implementation of High Performance Reconfigurable Finite Impulse Response Filter Using Distributed Arithmetic. <i>Wireless Personal Communications</i> , <b>2018</b> , 102, 3413-3425	1.9	3
9	Design of Arithmetic and Logic Unit (ALU) Using Subthreshold Adiabatic Logic for Low-Power Application. <i>Lecture Notes in Electrical Engineering</i> , <b>2018</b> , 201-209	0.2	1
8	An Area Efficient Design of Warped Filters. <i>Lecture Notes in Electrical Engineering</i> , <b>2018</b> , 189-200	0.2	0
7	High Performance Trench Gate Power MOSFET of Indium Phosphide. <i>Lecture Notes in Electrical Engineering</i> , <b>2018</b> , 175-181	0.2	
6	A Novel Approach to Compress an Image Using Cascaded Transform and Compressive Sensing. <i>Advances in Intelligent Systems and Computing</i> , <b>2016</b> , 743-749	0.4	1
5	A paradigm of distributed arithmetic (DA) approaches for digital FIR filter <b>2016</b> ,		1
4	A novel method to compress voice signal using compressive sensing <b>2015</b> ,		3
3	Performance Analysis of Real Time Operating System with General Purpose Operating System for Mobile Robotic System. <i>Indian Journal of Science and Technology</i> , <b>2015</b> , 8,	1	4
2	DA-Based Efficient Testable FIR Filter Implementation on FPGA Using Reversible Logic. <i>Circuits, Systems, and Signal Processing</i> , <b>2014</b> , 33, 863-884	2.2	3
1	An Embedded systems approach to monitor green house <b>2010</b> ,		16

