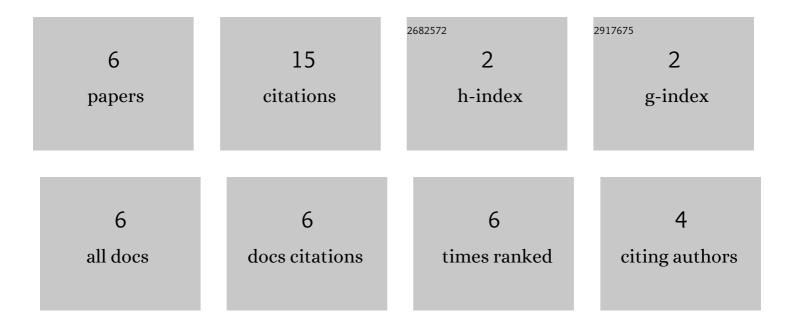
## Hiroyuki Iwata

List of Publications by Year in descending order

Source: https://exaly.com/author-pdf/2622249/publications.pdf Version: 2024-02-01



#	Article	IF	CITATIONS
1	A Power Reduction Method for Scan Testing in Ultra-Low Power Designs. , 2021, , .		0
2	FF-Control Point Insertion (FF-CPI) to Overcome the Degradation of Fault Detection under Multi-Cycle Test for POST. IEICE Transactions on Information and Systems, 2020, E103.D, 2289-2301.	0.7	2
3	Capture-Pattern-Control to Address the Fault Detection Degradation Problem of Multi-cycle Test in Logic BIST. , 2018, , .		3
4	Fault-detection-strengthened method to enable the POST for very-large automotive MCU in compliance with ISO26262. , 2018, , .		3
5	Automotive Functional Safety Assurance by POST with Sequential Observation. IEEE Design and Test, 2018, 35, 39-45.	1.2	7
6	An Effective At-Speed Scan Testing Approach Using Multiple-Timing Clock Waveforms. , 2012, , .		0