

Liter Siek

List of Publications by Year in descending order

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145
docs citations

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times ranked

1294
citing authors

#	ARTICLE	IF	CITATIONS
1	Analysis of energy consumption bounds in CMOS current-steering digital-to-analog converters. Analog Integrated Circuits and Signal Processing, 2022, 111, 339-351.	0.9	2
2	A Low-Power Quadrature LO Generator With Mutual Power-Supply Rejection Technique. IEEE Access, 2021, 9, 137241-137248.	2.6	3
3	A RF-DC Rectifier with Dual Voltage Polarity Self-Biasing for Wireless Sensor Node Application. , 2021, , .		1
4	Low Voltage Low Power Output Programmable OCL-LDO with Embedded Voltage Reference. , 2021, , .		2
5	A digital switching scheme to reduce DAC glitches using code-dependent randomization. , 2021, , .		1
6	Deep Neural Network (DNN) Optimized Design of 2.45 GHz CMOS Rectifier With 73.6% Peak Efficiency for RF Energy Harvesting. IEEE Transactions on Circuits and Systems I: Regular Papers, 2020, 67, 4322-4333.	3.5	15
7	A 12-bit branching time-to-digital converter with power saving features and digital based resolution tuning for PVT variations. Analog Integrated Circuits and Signal Processing, 2020, 105, 57-71.	0.9	1
8	A 10-bit 3.75-GS/s Binary-Weighted DAC with 58.6-pJ Energy Consumption in 65-nm CMOS. , 2020, , .		1
9	Fixed-frequency hysteretic buck converter with novel adaptive window control and transient response improvement. Journal of Engineering, 2019, 2019, 4667-4671.	0.6	0
10	A Low-Noise, Positive-Input, Negative-Output Voltage Generator for Low-to-Moderate Driving Capacity Applications. IEEE Transactions on Circuits and Systems I: Regular Papers, 2019, 66, 3423-3436.	3.5	6
11	A TDC-less all-digital phase locked loop for medical implant applications. Microprocessors and Microsystems, 2019, 69, 168-178.	1.8	5
12	A Single-Stage Dual-Output Tri-Mode AC-DC Regulator for Inductively Powered Application. IEEE Transactions on Circuits and Systems I: Regular Papers, 2019, 66, 3620-3630.	3.5	13
13	A 0.6 V, 1.74 ps Resolution Capacitively Boosted Time-to-Digital Converter in 180 nm CMOS. , 2019, , .		5
14	Multiloop Control for Fast Transient DC-DC Converter. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2019, 27, 219-228.	2.1	13
15	A 10-bit 300 MS/s 5.8 mW SAR ADC With Two-Stage Interpolation for PET Imaging. IEEE Sensors Journal, 2018, 18, 2006-2014.	2.4	6
16	A Single-Stage Direct-Conversion AC-DC Converter for Inductively Powered Application. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2018, 26, 892-902.	2.1	9
17	A 16-mW 1-GS/s With 49.6-dB SNDR TI-SAR ADC for Software-Defined Radio in 65-nm CMOS. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2018, 26, 572-583.	2.1	14
18	A 0.058 mm^2 , 24- μm 0.058 mm^2 $24 \frac{1}{4} \text{ W}$ Temperature Sensor in 40 nm CMOS Process with $\pm 0.5^\circ\text{C}$ Inaccuracy from 175°C to 175°C . Circuits, Systems, and Signal Processing, 2018, 37, 2278-2298.	1.2	1

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19	Novel Edge Comparator with Input Time Hysteresis for Improved Edges Arbitration. , 2018, , .		1
20	A Novel Zero-Voltage-Detector for Buck Converter in Discontinuous Conduction Mode(DCM). , 2018, , .		1
21	Inductive Power Transfer for Electric Vehicles Using Gallium Nitride Power Transistors. , 2018, , .		0
22	A 0.0186 mm ² , 0.65 V Supply, 9.53 ps RMS Jitter All-Digital PLL for Medical Implants. , 2018, , .		1
23	An 87% Peak Efficiency, 37W, Class H Audio Amplifier with GaN Output Stage. , 2018, , .		1
24	A 14-b, 850fs Fully Synthesizable Stochastic-Based Branching Time-to-Digital Converter in 65nm CMOS. , 2018, , .		6
25	Wide-input dynamic range 1MHz clock ultra-low supply flip-flop. Electronics Letters, 2018, 54, 938-939.	0.5	3
26	A Fast Transient Response DC-DC Converter with an Active Compensation Capacitor Module. , 2018, , .		5
27	On-chip reconfigurable switched-capacitor DC-DC converter for indoor PV energy harvesting. Electronics Letters, 2017, 53, 108-110.	0.5	2
28	An 80.4% Peak Power Efficiency Adaptive Supply Class H Power Amplifier for Audio Applications. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2017, 25, 1954-1965.	2.1	5
29	Meta-stability immunity technique for high speed SAR ADCs. Electronics Letters, 2017, 53, 300-302.	0.5	4
30	Single-stage AC-DC voltage regulator for 15 W wireless charging. Electronics Letters, 2017, 53, 337-339.	0.5	1
31	A 2-kW, 95% Efficiency Inductive Power Transfer System Using Gallium Nitride Gate Injection Transistors. IEEE Journal of Emerging and Selected Topics in Power Electronics, 2017, 5, 458-468.	3.7	35
32	A transient-enhanced low dropout regulator with rail to rail dynamic impedance attenuation buffer suitable for commercial design. Microelectronics Journal, 2017, 63, 27-34.	1.1	6
33	A High-Efficiency 6.78-MHz Full Active Rectifier With Adaptive Time Delay Control for Wireless Power Transmission. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2017, 25, 1297-1306.	2.1	15
34	The Design of Clocked-Comparator-Based Time-Interval Measurement Circuit for Pulse ToF Measurement. IEEE Sensors Journal, 2017, 17, 6699-6706.	2.4	4
35	A Hysteretic Switched-Capacitor DC-DC Converter With Optimal Output Ripple and Fast Transient Response. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2017, 25, 2995-3005.	2.1	12
36	2.45GHz wide input range CMOS rectifier for RF energy harvesting. , 2017, , .		9

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37	Low dropout regulator with temperature coefficient curvature correction topology. , 2017, , .		1
38	A 0.9-V input PWM DCM boost converter with low output ripples and fast load transient response based on a novel square-root voltage mode (SRVM) control approach. , 2017, , .		2
39	A close-loop time-mode temperature sensor with inaccuracy of $\hat{\sim}0.6^{\circ}\text{C}/0.5^{\circ}\text{C}$ from $\hat{\sim}40^{\circ}\text{C}$ to 120°C . , 2016, , .		1
40	A 10-bit 1GS/s 4-way TI SAR ADC with tap-interpolated FIR filter based time skew calibration. , 2016, , .		7
41	A 9-bit, 1.08ps resolution two-step time-to-digital converter in 65 nm CMOS for time-mode ADC. , 2016, , .		3
42	Digitally-controlled H-bridge DC-DC converter for micropower PV energy harvesting system. , 2016, , .		5
43	A continuous switching mode step-down switched-capacitor regulator with inrush current control scheme. , 2016, , .		0
44	A switched-capacitor DC-DC converter with embedded fast NMOS-LDOs achieving low noise, low output voltage ripple and fast response. , 2016, , .		3
45	Performance analysis on active rectifier structures for inductively powered application. , 2016, , .		0
46	Review of pulse generators for gated ring oscillator based Time-to-Digital converters. , 2016, , .		3
47	Electronically tunable MOSFET-based resistor used in a variable gain amplifier or filter. , 2016, , .		2
48	A dual redundancy radiation-hardened Flip-Flop based on C-element in 65nm process. , 2016, , .		7
49	A high efficiency synchronous buck converter with adaptive dead-time control. , 2016, , .		4
50	High-Accuracy Time-Mode Duty-Cycle-Modulation-Based Temperature Sensor for Energy-Efficient System Applications. Circuits, Systems, and Signal Processing, 2016, 35, 2317-2330.	1.2	0
51	Asymmetrical Dead-Time Control Driver for Buck Regulator. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2016, 24, 3543-3547.	2.1	2
52	A 1 V 103 dB 3rd-Order Audio Continuous-Time $\Delta\Sigma$ ADC With Enhanced Noise Shaping in 65 nm CMOS. IEEE Journal of Solid-State Circuits, 2016, 51, 2625-2638.	3.5	35
53	A 2.45GHz CMOS rectifier for RF energy harvesting. , 2016, , .		6
54	Fast transient response DC-DC converter with start-up inrush current control. Electronics Letters, 2016, 52, 1883-1885.	0.5	13

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55	Multichannel Time Skew Calibration for Time-Interleaved ADCs Using Clock Signal. Circuits, Systems, and Signal Processing, 2016, 35, 2669-2682.	1.2	6
56	A Flexible-Weighted Nonbinary Searching Technique for High-Speed SAR-ADCs. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2016, 24, 2808-2812.	2.1	18
57	A Filter Bank Mismatch Calibration Technique for Frequency-Interleaved ADCs. Circuits, Systems, and Signal Processing, 2016, 35, 3847-3862.	1.2	5
58	A New Time-Mode On-Chip Oscillator-Based High Linearity and Low Power Temperature Sensor. Journal of Circuits, Systems and Computers, 2015, 24, 1550155.	1.0	1
59	A 28.4 pJ per conversion ISFET-based pH sensing design for low-energy applications. , 2015, , .		0
60	A high-resolution on-chip propagation delay measurement scheme. , 2015, , .		1
61	A high frequency, high efficiency GaN HFET based inductive power transfer system. , 2015, , .		15
62	A new time-mode on-chip oscillator-based low power temperature sensor. , 2015, , .		1
63	Design of WPT coils to minimize AC resistance and capacitor stress applied to SS-topology. , 2015, , .		12
64	A continuous switching mode boost switch-cap regulator with PWM control scheme and fast transient response. , 2015, , .		3
65	A novel control method for magnetic wireless charging system. , 2015, , .		0
66	2-Phase 2-stage capacitor-less gate driver for Gallium Nitride Gate Injection Transistor for reduced gate ringing. , 2015, , .		3
67	Source follower-based high-speed switched capacitor amplifier for pipelined ADCs. Electronics Letters, 2015, 51, 21-23.	0.5	5
68	An output-capacitor-less low-dropout voltage regulator with high power supply rejection ratio and fast load transient response using boosted-input-transconductance structure. , 2015, , .		3
69	A digital time skew calibration technique for time-interleaved ADCs. , 2015, , .		2
70	A highly efficient, 420mW class-H headphone amplifier with single power supply rail. , 2015, , .		2
71	A switched capacitor deadtime controller for DC-DC buck converter. , 2015, , .		1
72	A 9-bit body-biased vernier ring time-to-digital converter in 65 nm CMOS technology. , 2015, , .		5

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73	Unbalanced input pair zero current detector for DC-DC buck converter. Electronics Letters, 2015, 51, 1359-1361.	0.5	6
74	An Area-Efficient Current-Mode Bandgap Reference With Intrinsic Robust Start-Up Behavior. IEEE Transactions on Circuits and Systems II: Express Briefs, 2015, 62, 937-941.	2.2	32
75	A low TC, supply independent and process compensated current reference. , 2015, , .		24
76	A higher order curvature corrected 2 ppm/°C CMOS voltage reference circuit. , 2015, , .		2
77	A high efficiency rectifier for inductively power transfer application. , 2015, , .		2
78	A 400 nW Single-Inductor Dual-Input-â€“Tri-Output DC-DC Buck-Boost Converter With Maximum Power Point Tracking for Indoor Photovoltaic Energy Harvesting. IEEE Journal of Solid-State Circuits, 2015, 50, 2758-2772.	3.5	88
79	P1dB Optimization Methodology for 130 nm SiGe BiCMOS 60 GHz Power Amplifier. Nanoscience and Nanotechnology Letters, 2015, 7, 272-275.	0.4	0
80	A statistic based time skew calibration method for time-interleaved ADCs. , 2014, , .		13
81	Design of frequency-â€“interleaved ADC with mismatch compensation. Electronics Letters, 2014, 50, 659-661.	0.5	9
82	Triple boundary multiphase with predictive interleaving technique for switched capacitor DC-DC converter regulation. , 2014, , .		3
83	A 0.7V low-power fully programmable Gaussian function generator for brain-inspired Gaussian correlation associative memory. Neurocomputing, 2014, 138, 69-77.	3.5	10
84	A 1.33 μm W) μm Successive Approximation ADC With Supply Reduction Technique for Implantable Retinal Prosthesis. IEEE Transactions on Biomedical Circuits and Systems, 2014, 8, 844-856.	2.7	25
85	A Fixed-frequency hysteretic controlled buck DC-DC converter with improved load regulation. , 2014, , .		8
86	Novel active tuning approach for resonant-mode wireless charging system. , 2014, , .		1
87	Adaptive Gate Switching Control for Discontinuous Conduction Mode DC-DC Converter. IEEE Transactions on Power Electronics, 2014, 29, 1311-1320.	5.4	30
88	Autonomous Wearable Sensor Nodes With Flexible Energy Harvesting. IEEE Sensors Journal, 2014, 14, 2299-2306.	2.4	113
89	A 5.8 nW 9.1-ENOB 1-kS/s Local Asynchronous Successive Approximation Register ADC for Implantable Medical Device. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2014, 22, 2220-2224.	2.1	16
90	A 0.42-V Input Boost dc-DC Converter With Pseudo-Digital Pulsewidth Modulation. IEEE Transactions on Circuits and Systems II: Express Briefs, 2014, 61, 634-638.	2.2	7

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91	High accuracy time-mode duty-cycle-modulation-based temperature sensor for energy efficient system applications. , 2014, , .		1
92	A 180 nm Technology New 2.5 Gbps Burst-Mode Optical Receiver Design with Automatic Gain Control and Feed-Forward Created Reset. Nanoscience and Nanotechnology Letters, 2014, 6, 817-824.	0.4	0
93	A 400nW single-inductor dual-input-tri-output DC-DC buck-boost converter with maximum power point tracking for indoor photovoltaic energy harvesting. , 2013, , .		22
94	Analysis and design of high performance frequency-interleaved ADC. , 2013, , .		7
95	A 60GHz power amplifier with 12.1 dBm & P1dB in 0.18um SiGe BiCMOS process. , 2013, , .		0
96	A novel voltage reference with an improved folded cascode current mirror OpAmp dedicated for energy harvesting application. , 2013, , .		5
97	A NOVEL ULTRA-LOW POWER TWO-TERMINAL ZENER VOLTAGE REFERENCE. Journal of Circuits, Systems and Computers, 2012, 21, 1240017.	1.0	0
98	A novel analog-to-residue converter for biomedical DSP application. , 2012, , .		0
99	A novel analog-to-residue conversion scheme based on clock overlapping technique. , 2012, , .		2
100	An ultra-fast 65nm capacitorless LDO regulator dedicated for sensory detection using a direct feedback dual self-reacting loop technique. , 2012, , .		0
101	Low power integrated circuit design with stacking technique. , 2012, , .		2
102	Review on VCO based ADC in modern deep submicron CMOS technology. , 2012, , .		4
103	A fully digital green LDO regulator dedicated for biomedical implant using a power-aware binary switching technique. , 2012, , .		1
104	An ultra-compact green bio-regulator dedicated for brain cortical implant using a dynamic PSR enhancement technique. , 2012, 2012, 1647-50.		0
105	A compact 16-bit dual-slope integrating circuit for direct analog-to-residue conversion. , 2012, , .		1
106	A 1.2V 80MS/S sample and hold for ADC applications. , 2012, , .		3
107	A novel 2-terminal zener voltage reference. , 2011, , .		0
108	Low-power 4-bit flash ADC for digitally controlled DC-DC converter. , 2011, , .		4

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109	An ultra low-power rail-to-rail comparator for ADC designs. , 2011, , .		12
110	A PLL with a VCO of improved PVT tolerance. , 2011, , .		5
111	A novel ripple controlled modulation for high efficiency DC-DC converters. , 2011, , .		2
112	Low power digital type ADC. , 2011, , .		0
113	Analog-to-Digital Converter with energy recovery capability using adiabatic technique. , 2010, , .		4
114	A high speed tracking quantizer for Continuous-Time multi-bit sigma delta modulators. , 2010, , .		0
115	An energy recovery approach for a charge redistribution successive approximation ADC. , 2010, , .		4
116	A very low power 0.7 V subthreshold fully programmable Gaussian function generator. , 2010, , .		6
117	A simplified approach for baseband recovery in SDR architectures. , 2010, , .		1
118	A novel power line communication controller designed for point-of-load dc-dc converters. , 2010, , .		4
119	Segmented Hybrid DPWM and tunable PID controller for digital DC-DC converters. , 2010, , .		4
120	Single inductor quad-input-dual-output buck converter for photovoltaic systems. , 2010, , .		10
121	System-level design of a delta-sigma modulator target for next generation wireless application. , 2009, , .		1
122	A compact current mode neuron circuit with Gaussian taper learning capability. , 2009, , .		11
123	A Low-Noise Multi-GHz CMOS Multiloop Ring Oscillator With Coarse and Fine Frequency Tuning. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2009, 17, 571-577.	2.1	70
124	2.5â€¦Gbit/s burst mode optical receiver with novel feedforward created reset. Electronics Letters, 2009, 45, 70.	0.5	0
125	A 7-GHz multiloop ring oscillator in 0.18-1¼m CMOS technology. Analog Integrated Circuits and Signal Processing, 2008, 56, 179-184.	0.9	23
126	A Top-Down Design Verification Based on Reuse Modular and Parametric Behavioral Modeling for Subranging Pipelined Analog-to-Digital Converter. , 2007, , .		1

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127	A spur-reduction technique in a fully integrated CMOS frequency synthesizer for 5-GHz WLAN SOC. , 2007, , .		1
128	A 1.8 V, 2.5 Gbps Burst Mode Optical Receiver with Feedforward Created Reset for EPON System. , 2007, , .		3
129	A Pipelined Dynamic Reference A/D Converter. , 2007, , .		1
130	A two-step dynamic reference A/D converter. , 2007, , .		1
131	Design of a High Performance Charge Pump Circuit for Low Voltage Phase-locked Loops. , 2007, , .		15
132	Design and frequency/phase-noise analysis of a 10-GHz CMOS ring oscillator with coarse and fine frequency tuning. Analog Integrated Circuits and Signal Processing, 2006, 48, 85-94.	0.9	6
133	Designing CMOS folded-cascode operational amplifier with flicker noise minimisation. Microelectronics Journal, 2001, 32, 69-73.	1.1	28
134	Minimum input sensitivity of high-order multi-stage sigma-delta modulator with first-order front-end. IEEE Transactions on Circuits and Systems Part 2: Express Briefs, 2000, 47, 792-796.	2.3	3
135	Low-cost speech recognition system for small vocabulary and independent speaker. , 2000, , .		0
136	Adaptive-biased buffer with low input capacitance. Electronics Letters, 2000, 36, 775.	0.5	2
137	A low-power compact switched output adiabatic logic (CSOAL) family. International Journal of Electronics, 1999, 86, 323-328.	0.9	0
138	A programmable clock oscillator for integrated sensor applications. , 1998, , .		0
139	Four-quadrant CMOS analogue multiplier for artificial neural networks. Electronics Letters, 1995, 31, 48-49.	0.5	8
140	High-speed hybrid current-mode sense amplifier for CMOS SRAMs. Electronics Letters, 1992, 28, 871.	0.5	18
141	A high-speed current-mode sense-amplifier for (CMOS SRAM's). , 0, , .		4
142	Overloading in multistage sigma-delta modulators. , 0, , .		0
143	An ASM-based ASIC for automobile accelerometer applications. , 0, , .		2
144	A New 4.3 ppm/°C Voltage Reference Using Standard CMOS Process with 1V Supply Voltage. , 0, , .		6

#	ARTICLE	IF	CITATIONS
145	A 0.18- μ m 10-GHz CMOS Ring Oscillator for Optical Transceivers. , 0, , .		7