

# Anubha Goel

## List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/2586415/publications.pdf>

Version: 2024-02-01

12  
papers

257  
citations

1307594

7  
h-index

1474206

9  
g-index

12  
all docs

12  
docs citations

12  
times ranked

71  
citing authors

#	ARTICLE	IF	CITATIONS
1	<sc>SiC</sc>-based analytical model for gate-stack dual metal nanowire <sc>FET</sc> with enhanced analog performance. International Journal of Numerical Modelling: Electronic Networks, Devices and Fields, 2022, 35, .	1.9	4
2	Enhanced Analog Performance and High-Frequency Applications of Dielectric Engineered High-K Schottky Nanowire FET. Silicon, 2022, 14, 9733-9749.	3.3	7
3	Modeling of shallow extension engineered dual metal surrounding gate (SEE-DM-SG) MOSFET gate-induced drain leakage (GIDL). Indian Journal of Physics, 2021, 95, 299-308.	1.8	25
4	Novel Dual-Metal Junctionless Nanotube Field-Effect Transistors for Improved Analog and Low-Noise Applications. Journal of Electronic Materials, 2021, 50, 108-119.	2.2	28
5	Dielectric Modulated Junctionless Biotube FET (DM-JL-BT-FET) Bio-Sensor. IEEE Sensors Journal, 2021, 21, 16731-16743.	4.7	43
6	High-K Spacer Dual-Metal Gate Stack Underlap Junctionless Gate All Around (HK-DMGS-JGAA) MOSFET for high frequency applications. Microsystem Technologies, 2020, 26, 1697-1705.	2.0	36
7	Physics-based analytic modeling and simulation of gate-induced drain leakage and linearity assessment in dual-metal junctionless accumulation nano-tube FET (DM-JAM-TFET). Applied Physics A: Materials Science and Processing, 2020, 126, 1.	2.3	26
8	Shallow Extension Engineered Dual Material Surrounding Gate (SEE-DM-SG) MOSFET for improved gate leakages, analysis of circuit and noise performance. AEU - International Journal of Electronics and Communications, 2019, 111, 152924.	2.9	28
9	Temperature-Dependent Gate-Induced Drain Leakages Assessment of Dual-Metal Nanowire Field-Effect Transistor Analytical Model. IEEE Transactions on Electron Devices, 2019, 66, 2437-2445.	3.0	41
10	GaN based Dual-Metal Gate Stack Engineered Junctionless-Surrounding-Gate (DMSEJSG) MOSFET for High Power Applications. , 2019, , .		4
11	Linearity and Intermodulation Distortion Assessment of Underlap Engineered Cylindrical Junctionless Surrounding Gate MOSFET for Low Noise CMOS RFIC Design. , 2019, , .		2
12	Dielectric Modulated Triple Metal Gate All Around MOSFET (TMGAA) for DNA Bio-Molecule Detection. , 2018, , .		13