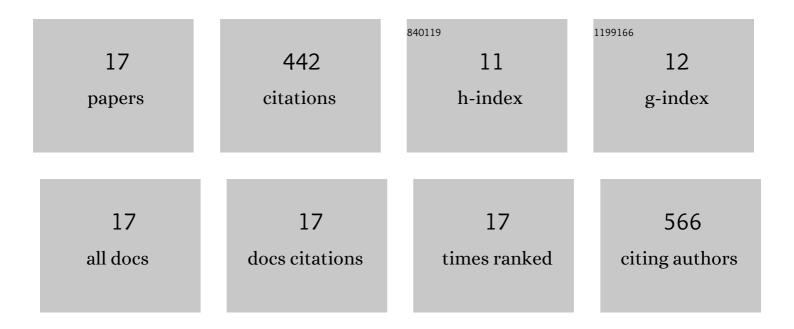
Luca Montesi

List of Publications by Year in descending order

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#	Article	IF	CITATIONS
1	High-Performance Resistance Switching Memory Devices Using Spin-On Silicon Oxide. IEEE Nanotechnology Magazine, 2018, 17, 884-888.	1.1	11
2	Investigation of resistance switching in SiO _{<i>x</i>} RRAM cells using a 3D multi-scale kinetic Monte Carlo simulator. Journal of Physics Condensed Matter, 2018, 30, 084005.	0.7	23
3	Spike-Timing Dependent Plasticity in Unipolar Silicon Oxide RRAM Devices. Frontiers in Neuroscience, 2018, 12, 57.	1.4	24
4	Intrinsic resistance switching in amorphous silicon oxide for high performance SiOx ReRAM devices. Microelectronic Engineering, 2017, 178, 98-103.	1.1	64
5	Intrinsic Resistance Switching in Amorphous Silicon Suboxides: The Role of Columnar Microstructure. Scientific Reports, 2017, 7, 9274.	1.6	41
6	Nanoscale Transformations in Metastable, Amorphous, Siliconâ€Rich Silica. Advanced Materials, 2016, 28, 7486-7493.	11.1	52
7	Silica: Nanoscale Transformations in Metastable, Amorphous, Siliconâ€Rich Silica (Adv. Mater. 34/2016). Advanced Materials, 2016, 28, 7549-7549.	11.1	13
8	Conductive AFM Topography of Intrinsic Conductivity Variations in Silica Based Dielectrics for Memory Applications. ECS Transactions, 2016, 75, 3-9.	0.3	7
9	Advanced physical modeling of SiO <inf>x</inf> resistive random access memories. , 2016, , .		6
10	Nanosecond Analog Programming of Substoichiometric Silicon Oxide Resistive RAM. IEEE Nanotechnology Magazine, 2016, 15, 428-434.	1.1	13
11	Silicon Oxide Reram Device. ECS Meeting Abstracts, 2016, , .	0.0	0
12	Structural changes and conductance thresholds in metal-free intrinsic SiOx resistive random access memory. Journal of Applied Physics, 2015, 117, .	1.1	102
13	Resistance switching in SiO _x ., 2015, , .		0
14	Structural investigation of resistance switching in silicon-rich silica films. , 2015, , .		0
15	Microscopic and spectroscopic analysis of the nature of conductivity changes during resistive switching in siliconâ€rich silicon oxide. Physica Status Solidi C: Current Topics in Solid State Physics, 2015, 12, 211-217.	0.8	21
16	Conductance tomography of conductive filaments in intrinsic silicon-rich silica RRAM. Nanoscale, 2015, 7, 18030-18035.	2.8	62
17	Building Blocks to Use in Innovative Non-volatile FPGA Architecture Based on MTJs. , 2012, , .		3