

M Aalelai Vendhan

List of Publications by Year in descending order

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3
papers

9
citations

2682572

2
h-index

2550090

3
g-index

3
all docs

3
docs citations

3
times ranked

2
citing authors

#	ARTICLE	IF	CITATIONS
1	Analysis on Circuit Metrics of 1-Bit FinFET Adders Realized using Distinct Logic Structures. Indian Journal of Science and Technology, 2019, 12, 1-4.	0.7	5
2	Investigations on performance metrics of FINFET based 8- bit low power adder architectures implemented using various logic styles http://www.indjst.org/index.php/indjst/article/view/1110711489988072 . Indian Journal of Science and Technology, 2018, 11, 1-22.	0.7	2
3	Impact of Temperature on Circuit Metrics of Various Full Adders. Lecture Notes in Electrical Engineering, 2020, , 517-524.	0.4	2