

# Yi-Ju Roh

## List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/2563571/publications.pdf>

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3  
papers

109  
citations

2258059

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| # | ARTICLE  | IF  | CITATIONS |
|---|--|-----|-----------|
| 1 | A 0.6 V 12 b 10 MS/s Low-Noise Asynchronous SAR-Assisted Time-Interleaved SAR (SATI-SAR) ADC. IEEE Journal of Solid-State Circuits, 2016, 51, 1826-1839.                             | 5.4 | 65        |
| 2 | A Reusable Code-Based SAR ADC Design With CDAC Compiler and Synthesizable Analog Building Blocks. IEEE Transactions on Circuits and Systems II: Express Briefs, 2018, 65, 1904-1908. | 3.0 | 29        |
| 3 | A 40-nm CMOS 12b 120-MS/s Nonbinary SAR-Assisted SAR ADC With Double Clock-Rate Coarse Decision. IEEE Transactions on Circuits and Systems II: Express Briefs, 2020, 67, 2833-2837.  | 3.0 | 15        |