

# Tanvir Atahary

## List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/2523253/publications.pdf>

Version: 2024-02-01

18  
papers

110  
citations

2682572

2  
h-index

2550090

3  
g-index

18  
all docs

18  
docs citations

18  
times ranked

37  
citing authors

#	ARTICLE	IF	CITATIONS
1	Parallelized path-based search for constraint satisfaction in autonomous cognitive agents. Journal of Supercomputing, 2021, 77, 1667-1692.	3.6	2
2	Leveraging the Manycore Architecture of the Loihi Spiking Processor to Perform Quasi-Complete Constraint Satisfaction. , 2020, , .		1
3	Solving Constraint Satisfaction Problems Using the Loihi Spiking Neuromorphic Processor. , 2020, , .		15
4	Cognitive Domain Ontologies. , 2020, , .		1
5	High Speed Cognitive Domain Ontologies for Asset Allocation Using Loihi Spiking Neurons. , 2019, , .		9
6	High Speed Approximate Cognitive Domain Ontologies for Constrained Asset Allocation based on Spiking Neurons. , 2019, , .		2
7	Hardware Accelerated Semantic Declarative Memory Systems through CUDA and MapReduce. IEEE Transactions on Parallel and Distributed Systems, 2019, 30, 601-614.	5.6	5
8	Task Allocation Performance Comparison for Low Power Devices. , 2018, , .		6
9	High Speed Approximate Cognitive Domain Ontologies for Asset Allocation based on Isolated Spiking Neurons. , 2018, , .		6
10	Cognitive Domain Ontologies in lookup tables stored in a memristor string matching architecture. , 2017, , .		6
11	A pattern matching approach to map cognitive domain ontologies to the IBM TrueNorth Neurosynaptic System. , 2017, , .		3
12	Parallelizing knowledge mining in a cognitive agent for autonomous decision making. , 2017, , .		7
13	Cognitive domain ontologies in a memristor crossbar architecture. , 2017, , .		6
14	Cognitive domain ontologies on the TrueNorth neurosynaptic system. , 2017, , .		7
15	Parallelized mining of domain knowledge on GPGPU and Xeon Phi clusters. Journal of Supercomputing, 2016, 72, 2132-2156.	3.6	5
16	Knowledge mining for cognitive agents through path based forward checking. , 2015, , .		15
17	High performance declarative memory systems through MapReduce. , 2015, , .		2
18	Hardware Accelerated Cognitively Enhanced Complex Event Processing Architecture. , 2013, , .		12