Joshua C Schabel

List of Publications by Year
in descending order

		3311381	2917675
7	15	1	2
papers	citations	h-index	g-index
7	7	7	7
all docs	docs citations	times ranked	citing authors

#	Article	IF	Citations
1	Hardware Implementation of Hierarchical Temporal Memory Algorithm. ACM Journal on Emerging Technologies in Computing Systems, 2022, 18, 1-23.	2.3	0
2	Design Obfuscation Through 3-D Split Fabrication With Smart Partitioning. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2022, 30, 1230-1243.	3.1	2
3	Exploring the Tradeoffs of Application-Specific Processing. IEEE Journal on Emerging and Selected Topics in Circuits and Systems, 2018, 8, 531-542.	3.6	1
4	Processor-in-memory support for artificial neural networks. , 2016, , .		3
5	Physical design of a 3D-stacked heterogeneous multi-core processor. , 2016, , .		3
6	Computing in 3D. , 2015, , .		5
7	Computing in 3D. , 2015, , .		1