Simon Rokicki

List of Publications by Year
in descending order

4 papers	24 citations	2682572 2 h-index	2550090 3 g-index
4 all docs	4 docs citations	4 times ranked	13 citing authors

#	Article	IF	Citations
1	Hybrid-DBT: Hardware/Software Dynamic Binary Translation Targeting VLIW. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2019, 38, 1872-1885.	2.7	14
2	Toward Speculative Loop Pipelining for High-Level Synthesis. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2020, 39, 4229-4239.	2.7	7
3	SpecHLS: Speculative Accelerator Design Using High-Level Synthesis. IEEE Micro, 2022, 42, 99-107.	1.8	2
4	Supporting runtime reconfigurable VLIWs cores through dynamic binary translation. , 2018, , .		1