

Nho-Van Nguyen

List of Publications by Year in descending order

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#	ARTICLE	IF	CITATIONS
1	An Optimized Discontinuous PWM Method to Minimize Switching Loss for Multilevel Inverters. IEEE Transactions on Industrial Electronics, 2011, 58, 3958-3966.	7.9	103
2	A Reduced Switching Loss PWM Strategy to Eliminate Common-Mode Voltage in Multilevel Inverters. IEEE Transactions on Power Electronics, 2015, 30, 5425-5438.	7.9	83
3	Eliminated common-mode voltage pulsewidth modulation to reduce output current ripple for multilevel inverters. IEEE Transactions on Power Electronics, 2016, 31, 5952-5966.	7.9	74
4	An Efficient Four-State Zero Common-Mode Voltage PWM Scheme With Reduced Current Distortion for a Three-Level Inverter. IEEE Transactions on Industrial Electronics, 2018, 65, 1021-1030.	7.9	37
5	Novel Eliminated Common-Mode Voltage PWM Sequences and an Online Algorithm to Reduce Current Ripple for a Three-Level Inverter. IEEE Transactions on Power Electronics, 2017, 32, 7482-7493.	7.9	29
6	A Reduced Common-Mode-Voltage Pulsewidth Modulation Method With Output Harmonic Distortion Minimization for Three-Level Neutral-Point-Clamped Inverters. IEEE Transactions on Power Electronics, 2020, 35, 6944-6962.	7.9	26
7	Switching Loss Optimization for a Pulsewidth Modulation Strategy With Reduced CMV and Improved Output Harmonic Distortion for a Three-Level NPC Converter. IEEE Transactions on Power Electronics, 2022, 37, 12118-12133.	7.9	5
8	Improved Model Predictive Control for Asymmetric T-Type NPC 3-Level Inverter. Electronics (Switzerland), 2021, 10, 2244.	3.1	4
9	Carrier Based PWM Control of 3-Level NPC Inverter with DC Neutral Point Balancing and Common Mode Voltage Reduction. , 2019, , .		4
10	PWM Strategy to Alleviate Common-Mode Voltage with Minimized Output Harmonic Distortion for Five-Level Cascaded H-Bridge Converters. Energies, 2021, 14, 4476.	3.1	3
11	Novel Single-State PWM Technique for Common-Mode Voltage Elimination in Multilevel Inverters. Journal of Power Electronics, 2012, 12, 548-558.	1.5	3
12	Switching Voltage Modeling and PWM Control in Multilevel Neutral-Point-Clamped Inverter under DC Voltage Imbalance. Journal of Power Electronics, 2015, 15, 504-517.	1.5	2
13	Virtual Space Vector Pulse Width Modulation for Asymmetric T-Type Neutral Point Clamped 3-Level Inverter. Mathematical Problems in Engineering, 2021, 2021, 1-19.	1.1	1