

# Khaled Khalaf

## List of Publications by Year in descending order

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Version: 2024-02-01

22  
papers

269  
citations

1478505

6  
h-index

1474206

9  
g-index

28  
all docs

28  
docs citations

28  
times ranked

318  
citing authors

#	ARTICLE	IF	CITATIONS
1	Digitally Modulated CMOS Polar Transmitters for Highly-Efficient mm-Wave Wireless Communication. IEEE Journal of Solid-State Circuits, 2016, 51, 1579-1592.	5.4	49
2	A low-power radio chipset in 40nm LP CMOS with beamforming for 60GHz high-data-rate wireless communication. , 2013, , .		41
3	A low-power 57-to-66GHz transceiver in 40nm LP CMOS with $\sim$ 17dB EVM at 7Gb/s. , 2012, , .		31
4	Design and Tuning of Coupled-LC mm-Wave Subharmonically Injection-Locked Oscillators. IEEE Transactions on Microwave Theory and Techniques, 2015, 63, 2301-2312.	4.6	26
5	A 60-GHz 8-Way Phased-Array Front-End With T/R Switching and Calibration-Free Beamsteering in 28-nm CMOS. IEEE Journal of Solid-State Circuits, 2018, 53, 2001-2011.	5.4	23
6	Design of D-Band Transformer-Based Gain-Boosting Class-AB Power Amplifiers in Silicon Technologies. IEEE Transactions on Circuits and Systems I: Regular Papers, 2020, 67, 1447-1458.	5.4	19
7	Design and Analysis of a 28 GHz T/R Front-End Module in 22-nm FD-SOI CMOS Technology. IEEE Transactions on Microwave Theory and Techniques, 2021, 69, 2841-2853.	4.6	12
8	Millimeter-Wave Transceivers for Wireless Communication, Radar, and Sensing : (Invited Paper). , 2019, , .		10
9	60-GHz CMOS TX/RX chipset on organic packages with integrated phased-array antennas. , 2016, , .		9
10	Opportunities and Challenges of Digital Signal Processing in Deeply Technology-Scaled Transceivers. Journal of Signal Processing Systems, 2015, 78, 5-19.	2.1	8
11	Signal processing challenges for emerging digital intensive and digitally assisted transceivers with deeply scaled technology (Invited). , 2013, , .		7
12	A 60GHz 8-way phased array front-end with TR switching and calibration-free beamsteering in 28nm CMOS. , 2017, , .		6
13	A 54~64.8 GHz subharmonically injection-locked frequency synthesizer with transmitter EVM between $\sim$ 26.5 dB and $\sim$ 28.8 dB in 28 nm CMOS. , 2017, , .		6
14	A 6x-oversampling 10GS/s 60GHz polar transmitter with 15.3% average PA efficiency in 40nm CMOS. , 2015, , .		4
15	A 28 nm CMOS 7.04 Gsps polar digital front-end processor for 60 GHz transmitter. , 2016, , .		4
16	A 28GHz Two-Way Current Combining Stacked-FET Power Amplifier in 22nm FD-SOI. , 2020, , .		4
17	CMOS low-power transceivers for 60GHz multi Gbit/s communications. , 2013, , .		3
18	Energy-Efficient Digital Front-End Processor for 60 GHz Polar Transmitter. Journal of Signal Processing Systems, 2018, 90, 777-789.	2.1	1

#	ARTICLE	IF	CITATIONS
19	Design of A D-band Transformer-Based Neutralized Class-AB Power Amplifier in Silicon Technologies. , 2019, , .		1
20	Systematic Design of On-Chip Matching Networks for D-band Circuits. , 2019, , .		1
21	Design and Simulation Results. Lecture Notes in Electrical Engineering, 2015, , 41-82.	0.4	0
22	Challenges of Digitally Modulated Transmitter Implementation at Millimeter Waves. , 2019, , 381-399.		0