

Oguzhan Erdem

List of Publications by Year in descending order

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14
papers

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docs citations

14
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57
citing authors

#	ARTICLE	IF	CITATIONS
1	Tree-based string pattern matching on FPGAs. Computers and Electrical Engineering, 2016, 49, 117-133.	4.8	11
2	Multi-pipelined and memory-efficient packet classification engines on FPGAs. Computer Communications, 2015, 67, 75-91.	5.1	10
3	Array Design for Trie-based IP Lookup. IEEE Communications Letters, 2010, 14, 773-775.	4.1	9
4	Hierarchical hybrid search structure for high performance packet classification. , 2012, , .		9
5	Simple CART based real-time traffic classification engine on FPGAs. , 2017, , .		7
6	Pipelined hierarchical architecture for high performance packet classification. Computer Networks, 2016, 103, 143-164.	5.1	6
7	Classification of Parkinsonâ€™s Disease Using Dynamic Time Warping. , 2019, , .		6
8	Compact trie forest: Scalable architecture for IP lookup on FPGAs. , 2012, , .		3
9	Large-scale SRAM-based IP lookup architectures using compact trie search structures. Computers and Electrical Engineering, 2014, 40, 1186-1198.	4.8	3
10	Musical Feature Based Classification of Parkinson's Disease Using Dysphonic Speech. , 2018, , .		3
11	High-performance IP Lookup Engine with Compact Clustered Trie Search. Computer Journal, 2012, 55, 1447-1466.	2.4	0
12	Range tree-linked list hierarchical search structure for packet classification on FPGAs. , 2013, , .		0
13	Clustered Linked List Forest for IPv6 Lookup. , 2013, , .		0
14	Real-Time Traffic Classification using Simple CART Forest on FPGAs. , 2018, , .		0