

Jing Li

List of Publications by Year in descending order

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#	ARTICLE	IF	CITATIONS
1	A Code-Recombination Algorithm-Based ADC With Feature Extraction for WBSN Applications. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2022, 30, 134-142.	3.1	1
2	A 13-Bit ENOB Third-Order Noise-Shaping SAR ADC Employing Hybrid Error Control Structure and LMS-Based Foreground Digital Calibration. IEEE Journal of Solid-State Circuits, 2022, 57, 2181-2195.	5.4	14
3	A 12-Bit Two-Step Single-Slope ADC With a Constant Input-Common-Mode Level Resistor Ramp Generator. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2022, 30, 644-655.	3.1	7
4	A Second-Order Noise-Shaping SAR ADC Using Two Passive Integrators Separated by the Comparator. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2021, 29, 227-231.	3.1	13
5	A $+0.44^{\circ}\text{C}/\mu\text{s} \sim 0.4^{\circ}\text{C}$ Inaccuracy Temperature Sensor With Multi-Threshold MOSFET-Based Sensing Element and CMOS Thyristor-Based VCO. IEEE Transactions on Circuits and Systems I: Regular Papers, 2021, 68, 1102-1113.	5.4	22
6	A13b-ENOB Third-Order Noise-Shaping SAR ADC using a Hybrid Error-Control Structure. , 2021, , .		3
7	A Second-Order Passive Noise-Shaping SAR ADC Using the LMS-Based Mismatch Calibration. , 2021, , .		1
8	A Low Voltage and Low Power 10-bit Non-Binary 2b/Cycle Time and Voltage Based SAR ADC. IEEE Transactions on Circuits and Systems I: Regular Papers, 2020, 67, 1136-1148.	5.4	12
9	A second-order noise-shaping SAR ADC with error-feedback structure and data weighted averaging. Microelectronics Journal, 2020, 105, 104905.	2.0	7
10	A nano-watt power-on reset circuit with Brown-Out detection capability. Microelectronics Journal, 2020, 105, 104898.	2.0	5
11	A 12-Bit Column-Parallel Two-Step Single-Slope ADC With a Foreground Calibration for CMOS Image Sensors. IEEE Access, 2020, 8, 172467-172480.	4.2	10
12	A 12-Bit Dynamic Tracking Algorithm-Based SAR ADC With Real-Time QRS Detection. IEEE Transactions on Circuits and Systems I: Regular Papers, 2020, 67, 2923-2933.	5.4	16
13	A High Area-Efficiency 14-bit SAR ADC With Hybrid Capacitor DAC for Array Sensors. IEEE Transactions on Circuits and Systems I: Regular Papers, 2020, 67, 4396-4408.	5.4	15
14	A High-Linearity Input Buffer with high output common-mode stability for 10bit 3.2GSs ADC. Electronics Letters, 2020, 56, 653-655.	1.0	3
15	A CMOS-Thyristor Based Temperature Sensor with $+0.37^{\circ}\text{C}/\mu\text{s} \sim 0.32^{\circ}\text{C}$ Inaccuracy. Micromachines, 2020, 11, 124.	2.9	4
16	Predictive SAR ADC with two-step loading technology for energy reduction. Microelectronics Journal, 2019, 93, 104622.	2.0	2
17	A Low-Power and Area-Efficient 14-bit SAR ADC with Hybrid CDAC for Array Sensors. , 2019, , .		5
18	A Dynamic Tracking Algorithm Based SAR ADC in Bio-Related Applications. IEEE Access, 2018, 6, 62166-62173.	4.2	10