

Hyun-Gi Seok

List of Publications by Year in descending order

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4
papers

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2682572

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citing authors

#	ARTICLE	IF	CITATIONS
1	A Single-Path Digital-IF Receiver Supporting Inter/Intra 5-CA with a Single Integer LO-PLL in 14nm CMOS FinFET. , 2022, , .		4
2	A 123-dBm Sensitivity Split-Channel BFSK Reconfigurable Data/Wake-Up Receiver for Low-Power Wide-Area Networks. IEEE Journal of Solid-State Circuits, 2021, 56, 2656-2667.	5.4	5
3	A 45- μ W, 162.1-dBc/Hz FoM, 490-MHz Two-Stage Differential Ring VCO Without a Cross-Coupled Latch. IEEE Transactions on Circuits and Systems II: Express Briefs, 2018, 65, 1579-1583.	3.0	13
4	A 2.4GHz, \sim 102dBm-sensitivity, 25kb/s, 0.466mW interference resistant BFSK multi-channel sliding-IF ULP receiver. , 2017, , .		14