

Vasile Gheorghita N Găitan

List of Publications by Year in descending order

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Version: 2024-02-01

34
papers

277
citations

1684188

5
h-index

1281871

11
g-index

34
all docs

34
docs citations

34
times ranked

207
citing authors

#	ARTICLE	IF	CITATIONS
1	An IoT architecture for things from industrial environment. , 2014, , .		62
2	CPU Architecture Based on a Hardware Scheduler and Independent Pipeline Registers. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2015, 23, 1661-1674.	3.1	34
3	Custom designed CPU architecture based on a hardware scheduler and independent pipeline registers — Concept and theory of operation. , 2012, , .		20
4	A flexible acquisition cycle for incompletely defined fieldbus protocols. ISA Transactions, 2014, 53, 776-786.	5.7	19
5	Enabling IoT connectivity for Modbus networks by using IoT edge gateways. , 2018, , .		15
6	Design, Fabrication, and Testing of an IoT Healthcare Cardiac Monitoring Device. Computers, 2020, 9, 15.	3.3	15
7	An internet of things-based distributed intelligent system with self-optimization for controlling traffic-light intersections. , 2012, , .		12
8	Hardware RTOS: Custom Scheduler Implementation Based on Multiple Pipeline Registers and MIPS32 Architecture. Electronics (Switzerland), 2019, 8, 211.	3.1	11
9	Methods to Improve the Performances of the Real-Time Operating Systems for Small Microcontrollers. , 2015, , .		9
10	Transparent interaction of SCADA systems developed over different technologies. , 2014, , .		6
11	Improving interrupt handling in the nMPRA. , 2014, , .		6
12	Hardware event treating in nMPRA. , 2014, , .		6
13	Smart cities become possible thanks to the Internet of Things. , 2015, , .		6
14	Schedulability analysis of nMPRA processor based on multithreaded execution. , 2016, , .		6
15	An RFID and OPC Technology Based Distributed System for Production Control and Monitoring. , 2007, , .		5
16	Implementation of nMPRA CPU architecture based on preemptive hardware scheduler engine and different scheduling algorithms. IET Computers and Digital Techniques, 2017, 11, 221-230.	1.2	5
17	m-GreenCARDIO embedded system designed for out-of-hospital cardiac patients. , 2018, , .		5
18	Middleware Based Model of Heterogeneous Systems for SCADA Distributed Applications. Advances in Electrical and Computer Engineering, 2010, 10, 121-124.	0.9	5

#	ARTICLE	IF	CITATIONS
19	RFID network traffic analysis based on an empirical model. , 2012, , .		4
20	An Overview of the nMPRA and nHSE Microarchitectures for Real-Time Applications. Sensors, 2021, 21, 4500.	3.8	4
21	The Uniform Engineering of Distributed Control Systems Using the OPC Specification. Advances in Electrical and Computer Engineering, 2008, 8, 71-77.	0.9	4
22	Dynamic, unbalanced distribution of tasks on a PS3 cluster system for double precision calculation. Journal of Supercomputing, 2012, 62, 1502-1518.	3.6	3
23	The use of IoT technologies for providing high-quality medical services. , 2017, , .		3
24	Threads Pipelining on the CellBE Systems. Advances in Electrical and Computer Engineering, 2013, 13, 121-126.	0.9	3
25	The development of IoT applications using old hardware equipment and virtual TEDS. , 2016, , .		2
26	Improving the performance of Real-Time Event Processing based on Preemptive Scheduler FPGA Implementation. , 2020, , .		2
27	Products Authentication and Traceability using RFID Technology and OPC UA Servers. Elektronika Ir Elektrotehnika, 2012, 18, .	0.8	2
28	High Complexity Control Gates with Advanced RFID Features for Production Process Monitoring. , 2008, , .		1
29	Programming paradigm of a microcontroller with hardware scheduler engine and independent pipeline registers - a software approach. , 2015, , .		1
30	Evaluation of Balanced Capacitance Matching Unit for HF RFID Systems in Metallic Environments. Elektronika Ir Elektrotehnika, 2012, 18, .	0.8	1
31	A distributed software architecture for remote monitor and control of the smart buildings. , 2015, , .		0
32	Improving the Performance of CPU Architectures by Reducing the Operating System Overhead (Extended Version). Electrical, Control and Communication Engineering, 2016, 10, 13-22.	0.8	0
33	Hardware Scheduler Implementation based on Replicated Resource Architecture for Reconfigurable Systems. , 2019, , .		0
34	CPU Execution Time Analysis based on RISC-V ISA Simulators: A Survey. , 2022, , .		0