Grazvydas Ziemys

List of Publications by Year in descending order

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#	Article	IF	CITATIONS
1	Majority logic gate for 3D magnetic computing. Nanotechnology, 2014, 25, 335202.	2.6	58
2	Domain wall depinning from notches using combined in- and out-of-plane magnetic fields. AIP Advances, 2016, 6, .	1.3	17
3	Controlled data storage for non-volatile memory cells embedded in nano magnetic logic. AIP Advances, 2017, 7, .	1.3	15
4	Speeding up nanomagnetic logic by DMI enhanced Pt/Co/Ir films. AIP Advances, 2018, 8, .	1.3	13
5	Time-dependent domain wall nucleation probability in field-coupled nanomagnets with perpendicular anisotropy. Journal of Applied Physics, 2015, 117, 178503.	2.5	8
6	Towards Logic-In-Memory circuits using 3D-integrated Nanomagnetic logic. , 2016, , .		8
7	Modeling and simulation of nanomagnetic logic with cadence virtuoso using Verilog-A. Solid-State Electronics, 2016, 125, 247-253.	1.4	7
8	PtCoW as a candidate for low power nanomagnetic logic. Journal of Magnetism and Magnetic Materials, 2019, 485, 345-350.	2.3	7
9	Characterization of the magnetization reversal of perpendicular Nanomagnetic Logic clocked in the ns-range. AIP Advances, 2016, 6, .	1.3	6
10	Towards nonvolatile magnetic crossbar arrays: A three-dimensional-integrated field-coupled domain wall gate with perpendicular anisotropy. Journal of Applied Physics, 2015, 117, 17D507.	2.5	5
11	Device-level compact modeling of perpendicular Nanomagnetic Logic for benchmarking purposes. , 2015, , .		3
12	3D nanomagnetic logic: How far beyond CMOS?. , 2017, , .		3
13	On the discrimination between nucleation and propagation in nanomagnetic logic devices. AIP Advances, 2018, 8, .	1.3	3
14	Experiment-based thermal micromagnetic simulations of the magnetization reversal for ns-range clocked nanomagnetic logic. AIP Advances, 2017, 7, 056625.	1.3	2
15	Engineering the Switching Behavior of Nanomagnets for Logic Computation Using 3-D Modeling and Simulation. IEEE Transactions on Magnetics, 2017, 53, 1-4.	2.1	1