

Manodipan Sahoo

List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/2298606/publications.pdf>

Version: 2024-02-01

47
papers

241
citations

1307366

7
h-index

1125617

13
g-index

47
all docs

47
docs citations

47
times ranked

107
citing authors

#	ARTICLE	IF	CITATIONS
1	Empirical Drain Current Model of Graphene Field-Effect Transistor for Application as a Circuit Simulation Tool. IETE Journal of Research, 2022, 68, 645-657.	1.8	1
2	2-D Analytical Modeling and Simulation of Dual Material, Double Gate, Gate Stack Engineered, Junctionless MOSFET based Biosensor with Enhanced Sensitivity. Silicon, 2022, 14, 4473-4484.	1.8	10
3	Analytical Modeling of Short-Channel TMD TFET Considering Effect of Fringing Field and 2-D Junctions Depletion Regions. IEEE Transactions on Electron Devices, 2022, 69, 843-850.	1.6	4
4	Performance and reliability improvement in intercalated MLG NR interconnects using optimized aspect ratio. Scientific Reports, 2022, 12, 1475.	1.6	2
5	Thermal-Aware Modeling and Analysis of Cu-Mixed CNT Nanocomposite Interconnects. IEEE Nanotechnology Magazine, 2022, 21, 163-171.	1.1	2
6	Stability Analysis of Nanoscale Copper-Carbon Hybrid Interconnects. , 2022, , .		3
7	Design, Modeling and Analysis of Cu-Carbon Hybrid Interconnects. IEEE Access, 2021, 9, 113577-113584.	2.6	7
8	Work function optimization for enhancement of sensitivity of dual-material (DM), double-gate (DG), junctionless MOSFET-based biosensor. Applied Physics A: Materials Science and Processing, 2021, 127, 1.	1.1	13
9	Performance Analysis of Self Heated Multilayer Vertical Graphene Nanoribbon Interconnects. , 2021, , .		0
10	Copper-MWCNT Composite: A Solution to Breakdown in Copper Interconnects. , 2021, , .		0
11	A Compact Short-Channel Analytical Drain Current Model of Asymmetric Dual-Gate TMD FET in Subthreshold Region Including Fringing Field Effects. IEEE Access, 2020, 8, 207982-207990.	2.6	6
12	Investigation on the Effect of Gate Dielectric and Other Device Parameters on Digital Performance of Silicene Nanoribbon Tunnel FET. IEEE Transactions on Electron Devices, 2020, 67, 2966-2973.	1.6	7
13	Performance and signal integrity analysis of intercalation doped MLVGNR interconnects. IET Circuits, Devices and Systems, 2020, 14, 192-199.	0.9	3
14	Boltzmann transport equation based semi-classical drain current model for bilayer GFET including scattering effects. IET Circuits, Devices and Systems, 2019, 13, 456-464.	0.9	3
15	Performance and Power Optimization for Intercalation Doped Multilayer Graphene Nanoribbon Interconnects. IETE Journal of Research, 2019, , 1-10.	1.8	0
16	Stability analysis of multilayer vertical graphene nanoribbon interconnects. Materials Research Express, 2019, 6, 085601.	0.8	2
17	A Surface Potential-Based Model for Dual Gate Bilayer Graphene Field Effect Transistor Including the Capacitive Effects. Journal of Circuits, Systems and Computers, 2019, 28, 1950241.	1.0	2
18	Thickness optimization for Intercalation doped Multilayer Graphene Nanoribbon Interconnects. , 2018, , .		1

#	ARTICLE	IF	CITATIONS
19	Investigation of Silicene Nanoribbon Tunnel FET for Low Power Digital VLSI Circuit Application with Variation of Device Parameters. , 2018, , .		1
20	Investigation on the Impact of Various Intercalation Doping on the Signal Integrity in ML-GNR Interconnects. , 2018, , .		0
21	Width optimization of intercalation doped multilayer graphene nanoribbon interconnects. , 2018, , .		0
22	Analysis of Crosstalk-Induced Effects in Multilayer Graphene Nanoribbon Interconnects. Journal of Circuits, Systems and Computers, 2017, 26, 1750102.	1.0	10
23	Analytical study of BTE based multilayer GFET model. , 2016, , .		1
24	Impact of mutual inductance on the crosstalk induced effects in single-walled carbon nanotube bundle interconnects. , 2016, , .		1
25	Modeling and analysis of crosstalk induced overshoot/undershoot effects in multilayer graphene nanoribbon interconnects and its impact on gate oxide reliability. Microelectronics Reliability, 2016, 63, 231-238.	0.9	18
26	A Verilog-A based semiclassical model for dual gated graphene field-effect transistor. , 2016, , .		1
27	Modeling of crosstalk induced overshoot/undershoot effects in Multilayer Graphene Nanoribbon Interconnects. , 2015, , .		0
28	Analytical drain current model for graphene metal-oxide semiconductor field-effect transistor. , 2015, , .		0
29	Modeling of Crosstalk Induced Effects in Copper-Based Nanointerconnects: An ABCD Parameter Matrix-Based Approach. Journal of Circuits, Systems and Computers, 2015, 24, 1540007.	1.0	6
30	Modeling and Analysis of Crosstalk Induced Effects in Multiwalled Carbon Nanotube Bundle Interconnects: An ABCD Parameter-Based Approach. IEEE Nanotechnology Magazine, 2015, 14, 259-274.	1.1	69
31	Impact of Line Resistance Variations on Crosstalk Delay and Noise in Multilayer Graphene Nano Ribbon Interconnects. , 2014, , .		3
32	An ABCD Parameter Based Modeling and Analysis of Crosstalk Induced Effects in Multiwalled Carbon Nanotube Bundle Interconnects. , 2014, , .		7
33	A 45 uW 13 pJ/conv-step 7.4-ENOB 40 kS/s SAR ADC for digital microfluidic biochip applications. , 2014, , .		2
34	A new feedback circuit based charge-pump for wide-range and low-jitter DLL suitable for PET imaging applications. , 2014, , .		2
35	An ABCD parameter based modeling and analysis of crosstalk induced effects in Multilayer Graphene Nano Ribbon interconnects. , 2014, , .		9
36	Modeling of crosstalk induced effects in nanoscale copper interconnects. , 2014, , .		0

#	ARTICLE	IF	CITATIONS
37	Performance modeling and analysis of carbon nanotube bundles for future VLSI circuit applications. Journal of Computational Electronics, 2014, 13, 673-688.	1.3	15
38	On the Suitability of Single-Walled Carbon Nanotube Bundle Interconnects for High-Speed and Power-Efficient Applications. Journal of Low Power Electronics, 2014, 10, 479-494.	0.6	5
39	Performance analysis of multiwalled carbon nanotube bundles. , 2013, , .		9
40	Modeling of crosstalk delay and noise in single-walled carbon nanotube bundle interconnects. , 2013, , .		2
41	A 1.8 V 64.9 μ W 54.1 dB SNDR 1^{st} order sigma-delta modulator design using clocked comparator Based Switched Capacitor technique. , 2013, , .		1
42	An ABCD parameter-based modeling and analysis of crosstalk induced effects in single-walled carbon nanotube bundle interconnects. , 2013, , .		7
43	Impact of Inductance on the Performance of Single Walled Carbon Nanotube Bundle Interconnects. , 2013, , .		1
44	Analytical modeling of crosstalk effects in coupled copper interconnects in deep sub micron technology. , 2012, , .		1
45	Efficient and Compact Electrical Modeling of Multi Walled Carbon Nanotube Interconnects. , 2012, , .		3
46	Comparison of OpAmp Based and Comparator Based Switched Capacitor Filter. Lecture Notes in Computer Science, 2012, , 180-189.	1.0	1
47	Optimal power and noise allocation for analog and digital sections of a low power radio receiver. , 2008, , .		0