

Yang-Kyu Choi

List of Publications by Year in descending order

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421
papers

15,969
citations

20759

60
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24915

109
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432
all docs

432
docs citations

432
times ranked

15211
citing authors

#	ARTICLE	IF	CITATIONS
1	Metallic Ti ₃ C ₂ T _x MXene Gas Sensors with Ultrahigh Signal-to-Noise Ratio. ACS Nano, 2018, 12, 986-993.	7.3	1,153
2	A Polydimethylsiloxane (PDMS) Sponge for the Selective Absorption of Oil from Water. ACS Applied Materials & Interfaces, 2011, 3, 4552-4556.	4.0	606
3	Chemical sensors based on nanostructured materials. Sensors and Actuators B: Chemical, 2007, 122, 659-671.	4.0	587
4	A dielectric-modulated field-effect transistor for biosensing. Nature Nanotechnology, 2007, 2, 430-434.	15.6	448
5	Triboelectric Nanogenerator: Structure, Mechanism, and Applications. ACS Nano, 2021, 15, 258-287.	7.3	343
6	A robust superhydrophobic and superoleophobic surface with inverse-trapezoidal microstructures on a large transparent flexible substrate. Soft Matter, 2010, 6, 1401.	1.2	319
7	Aspartate Aminotransferase (AST/GOT) and Alanine Aminotransferase (ALT/GPT) Detection Techniques. Sensors, 2006, 6, 756-782.	2.1	314
8	Sensitivity of Threshold Voltage to Nanowire Width Variation in Junctionless Transistors. IEEE Electron Device Letters, 2011, 32, 125-127.	2.2	285
9	Sub-50 nm P-channel FinFET. IEEE Transactions on Electron Devices, 2001, 48, 880-886.	1.6	243
10	Extremely scaled silicon nano-CMOS devices. Proceedings of the IEEE, 2003, 9, 1860-1873.	16.4	214
11	Self-cleaning effect of highly water-repellent microshell structures for solar cell applications. Journal of Materials Chemistry, 2011, 21, 633-636.	6.7	189
12	A spacer patterning technology for nanoscale CMOS. IEEE Transactions on Electron Devices, 2002, 49, 436-441.	1.6	178
13	Ultrathin-body SOI MOSFET for deep-sub-tenth micron era. IEEE Electron Device Letters, 2000, 21, 254-255.	2.2	173
14	Vertically stacked thin triboelectric nanogenerator for wind energy harvesting. Nano Energy, 2015, 14, 201-208.	8.2	170
15	Fabrication of Sub-10-nm Silicon Nanowire Arrays by Size Reduction Lithography. Journal of Physical Chemistry B, 2003, 107, 3340-3343.	1.2	169
16	Self-cleaning hybrid energy harvester to generate power from raindrop and sunlight. Nano Energy, 2015, 12, 636-645.	8.2	166
17	Resistive Switching Characteristics of Sol-Gel Zinc Oxide Films for Flexible Memory Applications. IEEE Transactions on Electron Devices, 2009, 56, 696-699.	1.6	164
18	Nature-Inspired Replicated Nano-Micro Structures for Triboelectric Energy Harvesting. Small, 2014, 10, 3887-3894.	5.2	163

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19	Double-Gate Nanowire Field Effect Transistor for a Biosensor. Nano Letters, 2010, 10, 2934-2938.	4.5	162
20	Simple Analytical Bulk Current Model for Long-Channel Double-Gate Junctionless Transistors. IEEE Electron Device Letters, 2011, 32, 704-706.	2.2	160
21	Nanoscale CMOS spacer FinFET for the terabit era. IEEE Electron Device Letters, 2002, 23, 25-27.	2.2	154
22	Sub 50-nm FinFET: PMOS. , 0, , .		150
23	High-performance nanopattern triboelectric generator by block copolymer lithography. Nano Energy, 2015, 12, 331-338.	8.2	146
24	A Full-Range Drain Current Model for Double-Gate Junctionless Transistors. IEEE Transactions on Electron Devices, 2011, 58, 4219-4225.	1.6	138
25	Sub-60-nm quasi-planar FinFETs fabricated using a simplified process. IEEE Electron Device Letters, 2001, 22, 487-489.	2.2	131
26	Ferrofluid-based triboelectric-electromagnetic hybrid generator for sensitive and sustainable vibration energy harvesting. Nano Energy, 2017, 31, 233-238.	8.2	127
27	Impact of contact pressure on output voltage of triboelectric nanogenerator based on deformation of interfacial structures. Nano Energy, 2015, 17, 63-71.	8.2	126
28	Resistive switching of aluminum oxide for flexible memory. Applied Physics Letters, 2008, 92, .	1.5	123
29	Nanogap Field-Effect Transistor Biosensors for Electrical Detection of Avian Influenza. Small, 2009, 5, 2407-2412.	5.2	121
30	First Demonstration of a Logic-Process Compatible Junctionless Ferroelectric FinFET Synapse for Neuromorphic Applications. IEEE Electron Device Letters, 2018, 39, 1445-1448.	2.2	121
31	Analytical Modeling of a Nanogap-Embedded FET for Application as a Biosensor. IEEE Transactions on Electron Devices, 2010, 57, 3477-3484.	1.6	115
32	Integration of field effect transistor-based biosensors with a digital microfluidic device for a lab-on-a-chip application. Lab on A Chip, 2012, 12, 1533.	3.1	108
33	Lock-and-Key-Geometry Effect of Patterned Surfaces: Wettability and Switching of Adhesive Force. Small, 2009, 5, 90-94.	5.2	104
34	Investigation of Silicon Nanowire Gate-All-Around Junctionless Transistors Built on a Bulk Substrate. IEEE Transactions on Electron Devices, 2013, 60, 1355-1360.	1.6	103
35	Surface structural analysis of a friction layer for a triboelectric nanogenerator. Nano Energy, 2017, 42, 34-42.	8.2	89
36	Structure Effects on Resistive Switching of $\text{Al/TiO}_x/\text{Al}$ Devices for RRAM Applications. IEEE Electron Device Letters, 2008, 29, 331-333.	2.2	86

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37	A Compact Model of Quantum Electron Density at the Subthreshold Region for Double-Gate Junctionless Transistors. IEEE Transactions on Electron Devices, 2012, 59, 1008-1012.	1.6	86
38	Hybrid energy harvester with simultaneous triboelectric and electromagnetic generation from an embedded floating oscillator in a single package. Nano Energy, 2016, 23, 50-59.	8.2	86
39	Direct-laser-patterned friction layer for the output enhancement of a triboelectric nanogenerator. Nano Energy, 2017, 35, 379-386.	8.2	86
40	Direct Observation of a Carbon Filament in Water-Resistant Organic Memory. ACS Nano, 2015, 9, 7306-7313.	7.3	85
41	Nonvolatile memory based on sol-gel ZnO thin-film transistors with Ag nanoparticles embedded in the ZnO/gate insulator interface. Applied Physics Letters, 2008, 93, 224106.	1.5	80
42	An Underlap Channel-Embedded Field-Effect Transistor for Biosensor Application in Watery and Dry Environment. IEEE Nanotechnology Magazine, 2012, 11, 390-394.	1.1	80
43	A Comprehensive Study of the Resistive Switching Mechanism in $\text{Al}/\text{TiO}_x/\text{TiO}_2/\text{Al}$ -Structured RRAM. IEEE Transactions on Electron Devices, 2009, 56, 3049-3054.	1.6	79
44	Surface-modified microelectrode array with flake nanostructure for neural recording and stimulation. Nanotechnology, 2010, 21, 085303.	1.3	76
45	Direct Electrochemistry of Uric Acid at Chemically Assembled Carboxylated Single-Walled Carbon Nanotubes Netlike Electrode. Journal of Physical Chemistry B, 2006, 110, 21850-21856.	1.2	74
46	Comprehensive Analysis of Gate-Induced Drain Leakage in Vertically Stacked Nanowire FETs: Inversion-Mode Versus Junctionless Mode. IEEE Electron Device Letters, 2016, 37, 541-544.	2.2	74
47	Triboelectric nanogenerator based on rolling motion of beads for harvesting wind energy as active wind speed sensor. Nano Energy, 2018, 52, 256-263.	8.2	74
48	Network Polydiacetylene Films: Preparation, Patterning, and Sensor Applications. Advanced Functional Materials, 2011, 21, 1032-1039.	7.8	72
49	Self-powered wearable keyboard with fabric based triboelectric nanogenerator. Nano Energy, 2018, 53, 596-603.	8.2	72
50	Sublithographic nanofabrication technology for nanocatalysts and DNA chips. Journal of Vacuum Science & Technology an Official Journal of the American Vacuum Society B, Microelectronics Processing and Phenomena, 2003, 21, 2951.	1.6	70
51	First Demonstration of Junctionless Accumulation-Mode Bulk FinFETs With Robust Junction Isolation. IEEE Electron Device Letters, 2013, 34, 1479-1481.	2.2	70
52	A Triboelectric Sponge Fabricated from a Cube Sugar Template by 3D Soft Lithography for Superhydrophobicity and Elasticity. Advanced Electronic Materials, 2016, 2, 1500331.	2.6	70
53	A New Sensing Metric to Reduce Data Fluctuations in a Nanogap-Embedded Field-Effect Transistor Biosensor. IEEE Transactions on Electron Devices, 2012, 59, 2825-2831.	1.6	69
54	Nonvolatile Memory by All-Around-Gate Junctionless Transistor Composed of Silicon Nanowire on Bulk Substrate. IEEE Electron Device Letters, 2011, 32, 602-604.	2.2	68

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55	A well-ordered flower-like gold nanostructure for integrated sensors via surface-enhanced Raman scattering. <i>Nanotechnology</i> , 2009, 20, 235302.	1.3	67
56	Comprehensive modeling of resistive switching in the Al/TiO _x /TiO ₂ /Al heterostructure based on space-charge-limited conduction. <i>Applied Physics Letters</i> , 2010, 97, .	1.5	67
57	FinFET process refinements for improved mobility and gate work function engineering. , 0, , .		66
58	Label-free DNA detection with a nanogap embedded complementary metal oxide semiconductor. <i>Nanotechnology</i> , 2011, 22, 135502.	1.3	66
59	Ferrocene Functionalized Single-Walled Carbon Nanotube Bundles. Hybrid Interdigitated Construction Film for l-Glutamate Detection. <i>Journal of Physical Chemistry C</i> , 2007, 111, 1200-1206.	1.5	64
60	Palladium nanoparticle decorated silicon nanowire field-effect transistor with side-gates for hydrogen gas detection. <i>Applied Physics Letters</i> , 2014, 104, .	1.5	63
61	Development of a Point-of-Care Testing Platform With a Nanogap-Embedded Separated Double-Gate Field Effect Transistor Array and Its Readout System for Detection of Avian Influenza. <i>IEEE Sensors Journal</i> , 2011, 11, 351-360.	2.4	62
62	Functional Circuitry on Commercial Fabric via Textile-Compatible Nanoscale Film Coating Process for Fibertronics. <i>Nano Letters</i> , 2017, 17, 6443-6452.	4.5	62
63	Self-powered electro-coagulation system driven by a wind energy harvesting triboelectric nanogenerator for decentralized water treatment. <i>Nano Energy</i> , 2016, 28, 288-295.	8.2	61
64	A Vertically Integrated Junctionless Nanowire Transistor. <i>Nano Letters</i> , 2016, 16, 1840-1847.	4.5	61
65	Design Strategy for a Piezoelectric Nanogenerator with a Well-Ordered Nanoshell Array. <i>ACS Nano</i> , 2013, 7, 10773-10779.	7.3	60
66	Vertically Integrated Multiple Nanowire Field Effect Transistor. <i>Nano Letters</i> , 2015, 15, 8056-8061.	4.5	60
67	3-Dimensional broadband energy harvester based on internal hydrodynamic oscillation with a package structure. <i>Nano Energy</i> , 2015, 17, 82-90.	8.2	60
68	Mimicry of Excitatory and Inhibitory Artificial Neuron With Leaky Integrate-and-Fire Function by a Single MOSFET. <i>IEEE Electron Device Letters</i> , 2020, 41, 208-211.	2.2	60
69	Triboelectric nanogenerator with nanostructured metal surface using water-assisted oxidation. <i>Nano Energy</i> , 2016, 21, 258-264.	8.2	59
70	A Nonpiecewise Model for Long-Channel Junctionless Cylindrical Nanowire FETs. <i>IEEE Electron Device Letters</i> , 2012, 33, 155-157.	2.2	58
71	Si ^δ -MoS ₂ Vertical Heterojunction for a Photodetector with High Responsivity and Low Noise Equivalent Power. <i>ACS Applied Materials & Interfaces</i> , 2019, 11, 7626-7634.	4.0	58
72	An underlap field-effect transistor for electrical detection of influenza. <i>Applied Physics Letters</i> , 2010, 96, .	1.5	57

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73	CRP detection from serum for chip-based point-of-care testing system. <i>Biosensors and Bioelectronics</i> , 2013, 41, 322-327.	5.3	57
74	Self-Powered Ion Concentration Sensor with Triboelectricity from Liquid-Solid Contact Electrification. <i>Advanced Electronic Materials</i> , 2016, 2, 1600006.	2.6	57
75	Effects of the oxygen vacancy concentration in InGaZnO-based resistance random access memory. <i>Applied Physics Letters</i> , 2012, 101, .	1.5	55
76	A Highly Responsive Silicon Nanowire/Amplifier MOSFET Hybrid Biosensor. <i>Scientific Reports</i> , 2015, 5, 12286.	1.6	55
77	Functionalized porous Si nanowires for selective and simultaneous electrochemical detection of Cd(II) and Pb(II) ions. <i>Electrochimica Acta</i> , 2016, 211, 998-1005.	2.6	55
78	Spacer FinFET: nanoscale double-gate CMOS technology for the terabit era. <i>Solid-State Electronics</i> , 2002, 46, 1595-1601.	0.8	54
79	Disk-based triboelectric nanogenerator operated by rotational force converted from linear force by a gear system. <i>Nano Energy</i> , 2018, 50, 489-496.	8.2	54
80	Triboelectric Nanogenerator Based on the Internal Motion of Powder with a Package Structure Design. <i>ACS Nano</i> , 2016, 10, 1017-1024.	7.3	53
81	A prototype high sensitivity load cell using single walled carbon nanotube strain gauges. <i>Sensors and Actuators A: Physical</i> , 2012, 180, 120-126.	2.0	51
82	Surface Engineering of Triboelectric Nanogenerator with an Electrodeposited Gold Nanoflower Structure. <i>Scientific Reports</i> , 2015, 5, 13866.	1.6	51
83	Self-heated silicon nanowires for high performance hydrogen gas detection. <i>Nanotechnology</i> , 2015, 26, 095501.	1.3	51
84	Sublithographic vertical gold nanogap for label-free electrical detection of protein-ligand binding. <i>Journal of Vacuum Science & Technology B</i> , 2007, 25, 443.	1.3	50
85	Universal Potential Model in Tied and Separated Double-Gate MOSFETs With Consideration of Symmetric and Asymmetric Structure. <i>IEEE Transactions on Electron Devices</i> , 2008, 55, 1472-1479.	1.6	50
86	Highly durable and flexible memory based on resistance switching. <i>Solid-State Electronics</i> , 2010, 54, 392-396.	0.8	49
87	Piezoelectric nanogenerator with a nanoforest structure. <i>Nano Energy</i> , 2013, 2, 1142-1148.	8.2	49
88	Gold nanoparticle embedded silicon nanowire biosensor for applications of label-free DNA detection. <i>Biosensors and Bioelectronics</i> , 2010, 25, 2182-2185.	5.3	48
89	Electrical Biomolecule Detection Using Nanopatterned Silicon Via Block Copolymer Lithography. <i>Small</i> , 2014, 10, 337-343.	5.2	48
90	Hydrogen annealing effect on DC and low-frequency noise characteristics in CMOS FinFETs. <i>IEEE Electron Device Letters</i> , 2003, 24, 186-188.	2.2	47

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91	Analytical Threshold Voltage Model for Double-Gate MOSFETs With Localized Charges. IEEE Electron Device Letters, 2008, 29, 927-930.	2.2	47
92	Cointegration of single-transistor neurons and synapses by nanoscale CMOS fabrication for highly scalable neuromorphic hardware. Science Advances, 2021, 7, .	4.7	47
93	A pH sensor with a double-gate silicon nanowire field-effect transistor. Applied Physics Letters, 2013, 102, .	1.5	46
94	Surface engineering for enhancement of sensitivity in an underlap-FET biosensor by control of wettability. Biosensors and Bioelectronics, 2013, 41, 867-870.	5.3	46
95	Nanoscale ultrathin body PMOSFETs with raised selective germanium source/drain. IEEE Electron Device Letters, 2001, 22, 447-448.	2.2	44
96	Biristorâ€”Bistable Resistor Based on a Silicon Nanowire. IEEE Electron Device Letters, 2010, 31, 797-799.	2.2	44
97	Foldable and Disposable Memory on Paper. Scientific Reports, 2016, 6, 38389.	1.6	43
98	A study of negative-bias temperature instability of SOI and body-tied FinFETs. IEEE Electron Device Letters, 2005, 26, 326-328.	2.2	42
99	A 9-bit 80 MS/s Successive Approximation Register Analog-to-Digital Converter With a Capacitor Reduction Technique. IEEE Transactions on Circuits and Systems II: Express Briefs, 2010, 57, 502-506.	2.2	42
100	Thermofluorescent Conjugated Polymer Sensors for Nanoâ€”and Microscale Temperature Monitoring. Macromolecular Chemistry and Physics, 2011, 212, 1211-1220.	1.1	40
101	Silicon Nanowire All-Around Gate MOSFETs Built on a Bulk Substrate by All Plasma-Etching Routes. IEEE Electron Device Letters, 2011, 32, 452-454.	2.2	39
102	Analytical Threshold Voltage Model of Junctionless Double-Gate MOSFETs With Localized Charges. IEEE Transactions on Electron Devices, 2013, 60, 2951-2955.	1.6	39
103	Multiplex electrical detection of avian influenza and human immunodeficiency virus with an underlap-embedded silicon nanowire field-effect transistor. Biosensors and Bioelectronics, 2014, 55, 162-167.	5.3	39
104	Artificial Olfactory Neuron for an Inâ€”Sensor Neuromorphic Nose. Advanced Science, 2022, 9, e2106017.	5.6	39
105	FinFET-a quasi-planar double-gate MOSFET. , 0, , .		38
106	Bioinspired Photoresponsive Single Transistor Neuron for a Neuromorphic Visual System. Nano Letters, 2020, 20, 8781-8788.	4.5	38
107	A Oneâ€”Step Route to a Perfectly Ordered Waferâ€”Scale Microbowl Array for Sizeâ€”Dependent Superhydrophobicity. Small, 2008, 4, 211-216.	5.2	37
108	Flammable carbon nanotube transistors on a nitrocellulose paper substrate for transient electronics. Nano Research, 2017, 10, 87-96.	5.8	37

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109	Electrowetting on a Polymer Microlens Array. <i>Langmuir</i> , 2010, 26, 12443-12447.	1.6	36
110	Physically Transient Memory on a Rapidly Dissoluble Paper for Security Application. <i>Scientific Reports</i> , 2016, 6, 38324.	1.6	36
111	Self-sustainable wind speed sensor system with omni-directional wind based triboelectric generator. <i>Nano Energy</i> , 2019, 55, 115-122.	8.2	35
112	Microfabrication and characterization of spray-coated single-wall carbon nanotube film strain gauges. <i>Nanotechnology</i> , 2011, 22, 455301.	1.3	34
113	Nano-electromechanical Switch Based on a Physical Unclonable Function for Highly Robust and Stable Performance in Harsh Environments. <i>ACS Nano</i> , 2017, 11, 12547-12552.	7.3	34
114	A Recoverable Synapse Device Using a Three-Dimensional Silicon Transistor. <i>Advanced Functional Materials</i> , 2018, 28, 1804844.	7.8	34
115	All 3D-Printed Flexible ZnO UV Photodetector on an Ultraflat Substrate. <i>ACS Sensors</i> , 2020, 5, 1028-1032.	4.0	34
116	All-Solid-State Ion Synaptic Transistor for Wafer-Scale Integration with Electrolyte of a Nanoscale Thickness. <i>Advanced Functional Materials</i> , 2021, 31, 2010971.	7.8	34
117	Patterning sub-30-nm MOSFET gate with i-line lithography. <i>IEEE Transactions on Electron Devices</i> , 2001, 48, 1004-1006.	1.6	33
118	A conventional route to scalable morphology-controlled regular structures and their superhydrophobic/hydrophilic properties for biochips application. <i>Lab on A Chip</i> , 2009, 9, 2140.	3.1	33
119	Analytical Modeling and Thermodynamic Analysis of Robust Superhydrophobic Surfaces with Inverse-Trapezoidal Microstructures. <i>Langmuir</i> , 2010, 26, 17389-17397.	1.6	33
120	Performance-enhanced triboelectric nanogenerator using the glass transition of polystyrene. <i>Nano Energy</i> , 2016, 27, 306-312.	8.2	33
121	Multilayer Graphene with a Rippled Structure as a Spacer for Improving Plasmonic Coupling. <i>Advanced Functional Materials</i> , 2016, 26, 5093-5101.	7.8	33
122	Self-Curable Gate-All-Around MOSFETs Using Electrical Annealing to Repair Degradation Induced From Hot-Carrier Injection. <i>IEEE Transactions on Electron Devices</i> , 2016, 63, 910-915.	1.6	33
123	Bioinspired Polydopamine-Based Resistive Switching Memory on Cotton Fabric for Wearable Neuromorphic Device Applications. <i>Advanced Materials Technologies</i> , 2019, 4, 1900151.	3.0	33
124	Investigation of Physically Unclonable Functions Using Flash Memory for Integrated Circuit Authentication. <i>IEEE Nanotechnology Magazine</i> , 2015, 14, 384-389.	1.1	32
125	All-Printed In-Plane Supercapacitors by Sequential Additive Manufacturing Process. <i>ACS Applied Energy Materials</i> , 2020, 3, 4965-4973.	2.5	32
126	Floating Oscillator-Embedded Triboelectric Generator for Versatile Mechanical Energy Harvesting. <i>Scientific Reports</i> , 2015, 5, 16409.	1.6	31

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127	Investigation of Self-Heating Effects in Gate-All-Around MOSFETs With Vertically Stacked Multiple Silicon Nanowire Channels. <i>IEEE Transactions on Electron Devices</i> , 2017, 64, 4393-4399.	1.6	31
128	A flutter-driven triboelectric nanogenerator for harvesting energy of gentle breezes with a rear-fixed fluttering film. <i>Nano Energy</i> , 2022, 98, 107197.	8.2	31
129	Ultra-thin body SOI MOSFET for deep-sub-tenth micron era. , 0, , .		30
130	A biomolecular detection method based on charge pumping in a nanogap embedded field-effect-transistor biosensor. <i>Applied Physics Letters</i> , 2009, 94, .	1.5	30
131	Superhydrophobic Cylindrical Nanoshell Array. <i>Langmuir</i> , 2010, 26, 7661-7664.	1.6	30
132	Bio-Inspired Complementary Photoconductor by Porphyrin-Coated Silicon Nanowires. <i>Advanced Materials</i> , 2011, 23, 3979-3983.	11.1	29
133	A Bandgap-Engineered Silicon-Germanium Biristor for Low-Voltage Operation. <i>IEEE Transactions on Electron Devices</i> , 2014, 61, 2-7.	1.6	29
134	Logic circuits composed of flexible carbon nanotube thin-film transistor and ultra-thin polymer gate dielectric. <i>Scientific Reports</i> , 2016, 6, 26121.	1.6	29
135	Accumulation mode field-effect transistors for improved sensitivity in nanowire-based biosensors. <i>Applied Physics Letters</i> , 2012, 100, .	1.5	28
136	A Superamphiphobic Sponge with Mechanical Durability and a Self-Cleaning Effect. <i>Scientific Reports</i> , 2016, 6, 29993.	1.6	28
137	Joule-Heated and Suspended Silicon Nanowire Based Sensor for Low-Power and Stable Hydrogen Detection. <i>ACS Applied Materials & Interfaces</i> , 2019, 11, 42349-42357.	4.0	28
138	Nanoscale FET-Based Transduction toward Sensitive Extended-Gate Biosensors. <i>ACS Sensors</i> , 2019, 4, 1724-1729.	4.0	28
139	Damage immune field effect transistors with vacuum gate dielectric. <i>Journal of Vacuum Science and Technology B: Nanotechnology and Microelectronics</i> , 2011, 29, 011014.	0.6	27
140	Low-Temperature Fabrication of Robust, Transparent, and Flexible Thin-Film Transistors with a Nanolaminated Insulator. <i>ACS Applied Materials & Interfaces</i> , 2018, 10, 15829-15840.	4.0	27
141	Self-powered wearable touchpad composed of all commercial fabrics utilizing a crossline array of triboelectric generators. <i>Nano Energy</i> , 2019, 65, 103994.	8.2	27
142	Morphology-controlled SWCNT/polymeric microsphere arrays by a wet chemical self-assembly technique and their application for sensors. <i>Nanotechnology</i> , 2006, 17, 2988-2993.	1.3	26
143	Bistable resistor (biristor) - gateless silicon nanowire memory. , 2010, , .		26
144	Multidirection and Multi-amplitude Triboelectric Nanogenerator Composed of Porous Conductive Polymer with Prolonged Time of Current Generation. <i>Advanced Energy Materials</i> , 2018, 8, 1800654.	10.2	26

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145	A Comparative Study on Hot-Carrier Injection in 5-Story Vertically Integrated Inversion-Mode and Junctionless-Mode Gate-All-Around MOSFETs. <i>IEEE Electron Device Letters</i> , 2018, 39, 4-7.	2.2	26
146	Self-Powered Artificial Mechanoreceptor Based on Triboelectrification for a Neuromorphic Tactile System. <i>Advanced Science</i> , 2022, 9, e2105076.	5.6	26
147	Electrochemical behavior of needle-like and forest-like single-walled carbon nanotube electrodes. <i>Journal of Electroanalytical Chemistry</i> , 2006, 594, 27-34.	1.9	25
148	Substrate surface roughness-dependent 3-D complex nanoarchitectures of gold particles from directed electrodeposition. <i>Journal of Materials Chemistry</i> , 2009, 19, 478-483.	6.7	25
149	A nanoforest structure for practical surface-enhanced Raman scattering substrates. <i>Nanotechnology</i> , 2012, 23, 095301.	1.3	25
150	Hybrid Porphyrin-Silicon Nanowire Field-Effect Transistor by Opto-Electrical Excitation. <i>ACS Nano</i> , 2012, 6, 7885-7892.	7.3	25
151	Ferromagnetic nanoparticle-embedded hybrid nanogenerator for harvesting omnidirectional vibration energy. <i>Nanoscale</i> , 2018, 10, 12276-12283.	2.8	25
152	Body Thickness Dependence of Impact Ionization in a Multiple-Gate FinFET. <i>IEEE Electron Device Letters</i> , 2007, 28, 625-627.	2.2	24
153	Investigation of Size Dependence on Sensitivity for Nanowire FET Biosensors. <i>IEEE Nanotechnology Magazine</i> , 2011, 10, 1405-1411.	1.1	24
154	Vacuum gate dielectric gate-all-around nanowire for hot carrier injection and bias temperature instability free transistor. <i>Applied Physics Letters</i> , 2014, 104, .	1.5	24
155	A Unified-RAM (URAM) Cell for Multi-Functioning Capacitorless DRAM and NVM. , 2007, , .		23
156	A Bulk FinFET Unified-RAM (URAM) Cell for Multifunctioning NVM and Capacitorless 1T-DRAM. <i>IEEE Electron Device Letters</i> , 2008, 29, 632-634.	2.2	23
157	Designed Workfunction Engineering of Double-Stacked Metal Nanocrystals for Nonvolatile Memory Application. <i>IEEE Transactions on Electron Devices</i> , 2009, 56, 377-382.	1.6	23
158	Polysilicon Channel TFT With Separated Double-Gate for Unified RAM (URAM)-Unified Function for Nonvolatile SONOS Flash and High-Speed Capacitorless 1T-DRAM. <i>IEEE Transactions on Electron Devices</i> , 2010, 57, 601-607.	1.6	23
159	High-performance thin-film transistors produced from highly separated solution-processed carbon nanotubes. <i>Applied Physics Letters</i> , 2014, 104, .	1.5	23
160	Low-Frequency Noise Characteristics in SONOS Flash Memory With Vertically Stacked Nanowire FETs. <i>IEEE Electron Device Letters</i> , 2017, 38, 40-43.	2.2	23
161	A self-powered character recognition device based on a triboelectric nanogenerator. <i>Nano Energy</i> , 2020, 70, 104534.	8.2	23
162	Reliability study of CMOS FinFETs. , 0, , .		22

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163	Single nanowire on graphene (SNOG) as an efficient, reproducible, and stable SERS-active platform. <i>Nanoscale</i> , 2016, 8, 8878-8886.	2.8	22
164	Local Electro-Thermal Annealing for Repair of Total Ionizing Dose-Induced Damage in Gate-All-Around MOSFETs. <i>IEEE Electron Device Letters</i> , 2016, 37, 843-846.	2.2	22
165	Electro-Thermal Annealing Method for Recovery of Cyclic Bending Stress in Flexible a-IGZO TFTs. <i>IEEE Transactions on Electron Devices</i> , 2017, 64, 3189-3192.	1.6	22
166	Physically Unclonable Function by an All-Printed Carbon Nanotube Network. <i>ACS Applied Electronic Materials</i> , 2019, 1, 1162-1168.	2.0	22
167	A Single Transistor Neuron With Independently Accessed Double-Gate for Excitatory-Inhibitory Function and Tunable Firing Threshold Voltage. <i>IEEE Electron Device Letters</i> , 2020, 41, 1157-1160.	2.2	22
168	Enhancement of Program Speed in Dopant-Segregated Schottky-Barrier (DSSB) FinFET SONOS for NAND-Type Flash Memory. <i>IEEE Electron Device Letters</i> , 2009, 30, 78-81.	2.2	21
169	Latch-up based bidirectional npn selector for bipolar resistance-change memory. <i>Applied Physics Letters</i> , 2013, 103, .	1.5	21
170	A Novel Technique for Curing Hot-Carrier-Induced Damage by Utilizing the Forward Current of the PN-Junction in a MOSFET. <i>IEEE Electron Device Letters</i> , 2017, 38, 1012-1014.	2.2	21
171	On-Demand Printing of Wearable Thermotherapy Pad. <i>Advanced Healthcare Materials</i> , 2020, 9, e1901575.	3.9	21
172	Curing of Aged Gate Dielectric by the Self-Heating Effect in MOSFETs. <i>IEEE Transactions on Electron Devices</i> , 2020, 67, 777-788.	1.6	21
173	Gate-Induced Drain-Leakage (GIDL) Programming Method for Soft-Programming-Free Operation in Unified RAM (URAM). <i>IEEE Electron Device Letters</i> , 2009, 30, 189-191.	2.2	20
174	Nanowire Mechanical Switch with a Built-In Diode. <i>Small</i> , 2010, 6, 1197-1200.	5.2	20
175	Demonstration of a Curable Nanowire FinFET Using Punchthrough Current to Repair Hot-Carrier Damage. <i>IEEE Electron Device Letters</i> , 2018, 39, 180-183.	2.2	20
176	Low-frequency noise characteristics in p-channel FinFETs. <i>IEEE Electron Device Letters</i> , 2002, 23, 722-724.	2.2	19
177	Oscillating behaviour of hazardous gas on tin oxide gas sensor: Fourier and wavelet transform analysis. <i>Sensors and Actuators B: Chemical</i> , 2006, 115, 357-364.	4.0	19
178	Controlled Molecularly Mediated Assembly of Gold Nanooctahedra for a Glucose Biosensor. <i>Journal of Physical Chemistry C</i> , 2008, 112, 3605-3611.	1.5	19
179	Vertically Integrated Unidirectional Biristor. <i>IEEE Electron Device Letters</i> , 2011, 32, 1483-1485.	2.2	19
180	Nanowire FET Biosensors on a Bulk Silicon Substrate. <i>IEEE Transactions on Electron Devices</i> , 2012, 59, 2243-2249.	1.6	19

#	ARTICLE	IF	CITATIONS
181	Droplet transportation using a pre-charging method for digital microfluidics. <i>Microfluidics and Nanofluidics</i> , 2012, 12, 821-827.	1.0	19
182	Sustainable electronics for nano-spacecraft in deep space missions. , 2016, , .		19
183	Investigation of Low-Frequency Noise in Nonvolatile Memory Composed of a Gate- All-Around Junctionless Nanowire FET. <i>IEEE Transactions on Electron Devices</i> , 2016, 63, 2210-2213.	1.6	19
184	Charge and dielectric effects of biomolecules on electrical characteristics of nanowire FET biosensors. <i>Applied Physics Letters</i> , 2017, 111, .	1.5	19
185	Self-Aligned Nanoforest in Silicon Nanowire for Sensitive Conductance Modulation. <i>Nano Letters</i> , 2012, 12, 5603-5608.	4.5	18
186	A Low Switching Noise and High-Efficiency Buck Converter Using a Continuous-Time Reconfigurable Delta-Sigma Modulator. <i>IEEE Transactions on Power Electronics</i> , 2018, 33, 10501-10511.	5.4	18
187	30 nm ultra-thin-body SOI MOSFET with selectively deposited Ge raised S/D. , 0, , .		17
188	Partially Depleted SONOS FinFET for Unified RAM (URAM)â€™Unified Function for High-Speed 1T DRAM and Nonvolatile Memory. <i>IEEE Electron Device Letters</i> , 2008, 29, 781-783.	2.2	17
189	Highly uniform carbon nanotube nanomesh network transistors. <i>Nano Research</i> , 2015, 8, 1320-1326.	5.8	17
190	Large-sized sandpaper coated with solution-processed aluminum for a triboelectric nanogenerator with reliable durability. <i>RSC Advances</i> , 2017, 7, 137-144.	1.7	17
191	High-Performance Field-Effect Transistor and Logic Gates Based on GaSâ€™MoS ₂ van der Waals Heterostructure. <i>ACS Applied Materials & Interfaces</i> , 2020, 12, 5106-5112.	4.0	17
192	Random number generator with a chaotic wind-driven triboelectric energy harvester. <i>Nano Energy</i> , 2020, 78, 105275.	8.2	17
193	A Bioinspired Artificial Gustatory Neuron for a Neuromorphic Based Electronic Tongue. <i>Nano Letters</i> , 2022, 22, 5244-5251.	4.5	17
194	Fabrication of Patterned Polydiacetylene Composite Films Using a Replicaâ€™Molding (REM) Technique. <i>Macromolecular Rapid Communications</i> , 2010, 31, 270-274.	2.0	16
195	Transformable Functional Nanoscale Building Blocks with Wafer-Scale Silicon Nanowires. <i>Nano Letters</i> , 2011, 11, 854-859.	4.5	16
196	Carrier Lifetime Engineering for Floating-Body Cell Memory. <i>IEEE Transactions on Electron Devices</i> , 2012, 59, 367-373.	1.6	16
197	A Novel FinFET With High-Speed and Prolonged Retention for Dynamic Memory. <i>IEEE Electron Device Letters</i> , 2014, 35, 1236-1238.	2.2	16
198	Controlled anisotropic wetting of scalloped silicon nanogroove. <i>RSC Advances</i> , 2016, 6, 41914-41918.	1.7	16

#	ARTICLE	IF	CITATIONS
199	3D Carbon Electrode Based Triboelectric Nanogenerator. <i>Advanced Materials Technologies</i> , 2016, 1, 1600160.	3.0	16
200	Three-Dimensional Fin-Structured Semiconducting Carbon Nanotube Network Transistor. <i>ACS Nano</i> , 2016, 10, 10894-10900.	7.3	16
201	Tunneling Effects in a Charge-Plasma Dopingless Transistor. <i>IEEE Nanotechnology Magazine</i> , 2017, 16, 315-320.	1.1	16
202	Noninverting Buck-Boost DC-DC Converter Using a Duobinary-Encoded Single-Bit Delta-Sigma Modulator. <i>IEEE Transactions on Power Electronics</i> , 2020, 35, 484-495.	5.4	16
203	Controlled Synthesis of Gold Nanocomplex Arrays by a Combined Top-Down and Bottom-Up Approach and Their Electrochemical Behavior. <i>Journal of Physical Chemistry C</i> , 2008, 112, 12747-12753.	1.5	15
204	Parasitic BJT Read Method for High-Performance Capacitorless 1T-DRAM Mode in Unified RAM. <i>IEEE Electron Device Letters</i> , 2009, 30, 1108-1110.	2.2	15
205	A color display system based on thermochromic conjugated polydiacetylene supramolecules. <i>Macromolecular Research</i> , 2010, 18, 404-407.	1.0	15
206	Fullerene-Derivative-Embedded Nanogap Field-Effect Transistor and Its Nonvolatile Memory Application. <i>Small</i> , 2010, 6, 1617-1621.	5.2	15
207	Analysis and Evaluation of a BJT-Based 1T-DRAM. <i>IEEE Electron Device Letters</i> , 2010, 31, 393-395.	2.2	15
208	Fin-Width Dependence of BJT-Based 1T-DRAM Implemented on FinFET. <i>IEEE Electron Device Letters</i> , 2010, 31, 909-911.	2.2	15
209	Electrophoretic deposition of amphiphilic diacetylene supramolecules: polymerization, selective immobilization, pattern transfer and sensor applications. <i>Journal of Materials Chemistry</i> , 2011, 21, 18605.	6.7	15
210	Highly durable floating body cell memory: Vertical biristor. , 2012, , .		15
211	Accurate extraction of mobility in carbon nanotube network transistors using C-V and I-V measurements. <i>Applied Physics Letters</i> , 2014, 105, 212103.	1.5	15
212	A Generalized Threshold Voltage Model of Tied and Untied Double-Gate Junctionless FETs for a Symmetric and Asymmetric Structure. <i>IEEE Transactions on Electron Devices</i> , 2015, 62, 2710-2716.	1.6	15
213	Vertically Integrated Nanowire-Based Unified Memory. <i>Nano Letters</i> , 2016, 16, 5909-5916.	4.5	15
214	Quantitative Analysis of Deuterium Annealing Effect on Poly-Si TFTs by Low Frequency Noise and DC Characterization. <i>IEEE Transactions on Electron Devices</i> , 2018, 65, 1640-1644.	1.6	15
215	Quantitative Analysis of High-Pressure Deuterium Annealing Effects on Vertically Stacked Gate-All-Around SONOS Memory. <i>IEEE Transactions on Electron Devices</i> , 2020, 67, 3903-3907.	1.6	15
216	Dynamic determination of domestic liquefied petroleum gas down to several ppm levels using a Sr-doped SnO ₂ thick film gas sensor. <i>Mikrochimica Acta</i> , 2006, 156, 245-251.	2.5	14

#	ARTICLE	IF	CITATIONS
217	Transparent Zinc Oxide Gate Metal-Oxide-Semiconductor Field-Effect Transistor for High-Responsivity Photodetector. IEEE Electron Device Letters, 2009, 30, 493-495.	2.2	14
218	Fully Depleted Polysilicon TFTs for Capacitorless 1T-DRAM. IEEE Electron Device Letters, 2009, 30, 742-744.	2.2	14
219	High-Performance Polycrystalline Silicon TFT on the Structure of a Dopant-Segregated Schottky-Barrier Source/Drain. IEEE Electron Device Letters, 2010, 31, 228-230.	2.2	14
220	Influence of Total Ionizing Dose on Sub-100nm Gate-All-Around MOSFETs. IEEE Transactions on Nuclear Science, 2014, 61, 1420-1425.	1.2	14
221	Electrothermal Annealing (ETA) Method to Enhance the Electrical Performance of Amorphous-Oxide-Semiconductor (AOS) Thin-Film Transistors (TFTs). ACS Applied Materials & Interfaces, 2016, 8, 23820-23826.	4.0	14
222	A frequency reconfigurable dipole antenna with solid-state plasma in silicon. Scientific Reports, 2018, 8, 14996.	1.6	14
223	Curing of Hot-Carrier Induced Damage by Gate-Induced Drain Leakage Current in Gate-All-Around FETs. IEEE Electron Device Letters, 2019, 40, 1909-1912.	2.2	14
224	A Poly-Crystalline Silicon Nanowire Transistor with Independently Controlled Double-Gate for Physically Unclonable Function by Multi-States and Self-Destruction. Advanced Electronic Materials, 2021, 7, 2000989.	2.6	14
225	Parasitic S/D resistance effects on hot-carrier reliability in body-tied FinFETs. IEEE Electron Device Letters, 2006, 27, 514-516.	2.2	13
226	FinFACT-Fin Flip-Flop Actuated Channel Transistor. IEEE Electron Device Letters, 2010, 31, 764-766.	2.2	13
227	A mechanical and electrical transistor structure (METS) with a sub-2 nm nanogap for effective voltage scaling. Nanoscale, 2014, 6, 7799.	2.8	13
228	Quasi-planar NMOS FinFETs with sub-100 nm gate lengths. , 0, , .		12
229	Low-frequency noise characteristics of ultrathin body p-MOSFETs with molybdenum gate. IEEE Electron Device Letters, 2003, 24, 31-33.	2.2	12
230	Vertically standing carbon nanotubes as charge storage nodes for an ultimately scaled nonvolatile memory application. Applied Physics Letters, 2007, 91, 063110.	1.5	12
231	Metal nanocrystals synthesized with a micellar template based on a diblock copolymer for three-dimensional nonvolatile memory. Applied Physics Letters, 2008, 93, 052106.	1.5	12
232	Gate-to-Source/Drain Nonoverlap Device for Soft-Program Immune Unified RAM (URAM). IEEE Electron Device Letters, 2009, 30, 544-546.	2.2	12
233	Nanogap Electrode Fabrication for a Nanoscale Device by Volume-Expanding Electrochemical Synthesis. Small, 2011, 7, 2210-2216.	5.2	12
234	Terahertz time-domain spectroscopy of anisotropic complex conductivity tensors in silicon nanowire films. Applied Physics Letters, 2012, 100, 211102.	1.5	12

#	ARTICLE	IF	CITATIONS
235	Photoactive Memory by a Si-Nanowire Field-Effect Transistor. <i>ACS Nano</i> , 2012, 6, 1449-1454.	7.3	12
236	Micropatterning Polydiacetylene Supramolecular Vesicles on Glass Substrates using a Pre-patterned Hydrophobic Thin Film. <i>Macromolecular Chemistry and Physics</i> , 2012, 213, 610-616.	1.1	12
237	A Core Compact Model for Multiple-Gate Junctionless FETs. <i>IEEE Transactions on Electron Devices</i> , 2015, 62, 2285-2291.	1.6	12
238	Controllable electrical and physical breakdown of poly-crystalline silicon nanowires by thermally assisted electromigration. <i>Scientific Reports</i> , 2016, 6, 19314.	1.6	12
239	Suppression of Self-Heating Effects in 3-D V-NAND Flash Memory Using a Plugged Pillar-Shaped Heat Sink. <i>IEEE Electron Device Letters</i> , 2019, 40, 212-215.	2.2	12
240	Mechanically robust triboelectric nanogenerator with a shear thickening fluid for impact monitoring. <i>Journal of Materials Chemistry A</i> , 2022, 10, 10383-10390.	5.2	12
241	Ultra-thin body PMOSFETs with selectively deposited Ge source/drain. , 0, , .		11
242	Nanogap Capacitors for Label Free DNA Analysis. <i>Materials Research Society Symposia Proceedings</i> , 2002, 729, 4101.	0.1	11
243	Alternative route to reconstitute an electrical contact of enzyme on a single-walled carbon nanotube-ferrocene hybrid. <i>Journal of Electroanalytical Chemistry</i> , 2008, 621, 38-42.	1.9	11
244	High Injection Efficiency and Low-Voltage Programming in a Dopant-Segregated Schottky Barrier (DSSB) FinFET SONOS for nor-type Flash Memory. <i>IEEE Electron Device Letters</i> , 2009, 30, 265-268.	2.2	11
245	A charge pumping technique to identify biomolecular charge polarity using a nanogap embedded biotransistor. <i>Applied Physics Letters</i> , 2010, 97, .	1.5	11
246	Physical Observation of a Thermo-Morphic Transition in a Silicon Nanowire. <i>ACS Nano</i> , 2012, 6, 2378-2384.	7.3	11
247	A biristor based on a floating-body silicon nanowire for biosensor applications. <i>Applied Physics Letters</i> , 2013, 102, .	1.5	11
248	Investigation of optimal hydrogen sensing performance in semiconducting carbon nanotube network transistors with palladium electrodes. <i>Applied Physics Letters</i> , 2015, 107, .	1.5	11
249	Ink-jet printed semiconducting carbon nanotube ambipolar transistors and inverters with chemical doping technique using polyethyleneimine. <i>Applied Physics Letters</i> , 2016, 109, .	1.5	11
250	Self-powered data erasing of nanoscale flash memory by triboelectricity. <i>Nano Energy</i> , 2018, 52, 63-70.	8.2	11
251	Photoinduced Memory with Hybrid Integration of an Organic Fullerene Derivative and an Inorganic Nanogap-Embedded Field-Effect Transistor for Low-Voltage Operation. <i>Advanced Materials</i> , 2011, 23, 3326-3331.	11.1	10
252	A 9-bit 100-MS/s flash-SAR ADC without track-and-hold circuits. , 2012, , .		10

#	ARTICLE	IF	CITATIONS
253	A Dual-Gate Field-Effect Transistor for Label-Free Electrical Detection of Avian Influenza. <i>BioNanoScience</i> , 2012, 2, 35-41.	1.5	10
254	Evolution of Unified-RAM: 1T-DRAM and BE-SONOS Built on a Highly Scaled Vertical Channel. <i>IEEE Transactions on Electron Devices</i> , 2014, 61, 60-65.	1.6	10
255	Self-Destructible Fin Flip-Flop Actuated Channel Transistor. <i>IEEE Electron Device Letters</i> , 2016, 37, 130-133.	2.2	10
256	Reconfigurable Yagi-Uda antenna based on a silicon reflector with a solid-state plasma. <i>Scientific Reports</i> , 2017, 7, 17232.	1.6	10
257	A study of the charge distribution and output characteristics of an ultra-thin tribo-dielectric layer. <i>Nano Energy</i> , 2019, 62, 458-464.	8.2	10
258	Multilevel States of Nano-Electromechanical Switch for a PUF-Based Security Device. <i>Small</i> , 2019, 15, e1803825.	5.2	10
259	A Single Transistor-Based Threshold Switch for a Bio-Inspired Reconfigurable Threshold Logic. <i>Advanced Electronic Materials</i> , 2021, 7, 2100117.	2.6	10
260	Vertical InGaAs Biristor for Sub-1 V Operation. <i>IEEE Electron Device Letters</i> , 2021, 42, 681-683.	2.2	10
261	Wafer-scale controlled Au/Pt bimetallic flowerlike structure array. <i>Gold Bulletin</i> , 2008, 41, 58-65.	3.2	9
262	Charge pumping technique to analyze the effect of intrinsically retained charges and extrinsically trapped charges in biomolecules by use of a nanogap embedded biotransistor. <i>Applied Physics Letters</i> , 2010, 96, .	1.5	9
263	Improvement of Sensitivity and Limit of Detection in a Nanogap Biosensor by Controlling Surface Wettability. <i>BioNanoScience</i> , 2013, 3, 192-197.	1.5	9
264	Optimization of Bias Schemes for Long-Term Endurable 1T-DRAM Through the Use of the Biristor Mode Operation. <i>IEEE Electron Device Letters</i> , 2014, 35, 220-222.	2.2	9
265	A multi-directional wind based triboelectric generator with investigation of frequency effects. <i>Extreme Mechanics Letters</i> , 2018, 19, 46-53.	2.0	9
266	A Comprehensive Study of a Single-Transistor Latch in Vertical Pillar-Type FETs With Asymmetric Source and Drain. <i>IEEE Transactions on Electron Devices</i> , 2018, 65, 5208-5212.	1.6	9
267	A Temperature Sensor With a Thermistor. <i>IEEE Electron Device Letters</i> , 2021, 42, 1654-1657.	2.2	9
268	Reliability Issues in Multi-Gate FinFETs. , 2006, , .		8
269	Nonvolatile Memory Characteristics of NMOSFET With Ag Nanocrystals Synthesized via a Thermal Decomposition Process for Uniform Device Distribution. <i>IEEE Nanotechnology Magazine</i> , 2008, 7, 145-150.	1.1	8
270	High Aspect Ratio Silicon Nanowire for Stiction Immune Gate-All-Around MOSFETs. <i>IEEE Electron Device Letters</i> , 2009, 30, 864-866.	2.2	8

#	ARTICLE	IF	CITATIONS
271	Dopant-Segregated Schottky Source/Drain FinFET With a NiSi FUSI Gate and Reduced Leakage Current. IEEE Transactions on Electron Devices, 2010, 57, 2902-2906.	1.6	8
272	Joule Heating to Enhance the Performance of a Gate-All-Around Silicon Nanowire Transistor. IEEE Transactions on Electron Devices, 2016, 63, 2288-2292.	1.6	8
273	Ultra-Fast Erase Method of SONOS Flash Memory by Instantaneous Thermal Excitation. IEEE Electron Device Letters, 2016, 37, 190-192.	2.2	8
274	Highly Biased Linear Condition Method for Separately Extracting Source and Drain Resistance in MOSFETs. IEEE Transactions on Electron Devices, 2018, 65, 419-423.	1.6	8
275	A triboelectric nanogenerator implemented with an acoustic foam for a self-driven silent tire. Nano Energy, 2022, 96, 107090.	8.2	8
276	Reduction of direct-tunneling gate leakage current in double-gate and ultra-thin body MOSFETs. , 0, , .		7
277	Mass fabrication of resistive random access crossbar arrays by step and flash imprint lithography. Nanotechnology, 2009, 20, 445305.	1.3	7
278	An Extraction Method of the Energy Distribution of Interface Traps by an Optically Assisted Charge Pumping Technique. IEEE Transactions on Electron Devices, 2011, 58, 3667-3673.	1.6	7
279	Addressable Nanowire Field-Effect-Transistor Biosensors With Local Backgates. IEEE Transactions on Electron Devices, 2012, 59, 2507-2511.	1.6	7
280	Origin of transient V_{th} shift after erase and its impact on 2D/3D structure charge trap flash memory cell operations. , 2012, , .		7
281	Transfer of functional memory devices to any substrate. Physica Status Solidi - Rapid Research Letters, 2013, 7, 326-331.	1.2	7
282	First Demonstration of Ultra-Thin SiGe-Channel Junctionless Accumulation-Mode (JAM) Bulk FinFETs on Si Substrate with PN Junction-Isolation Scheme. IEEE Journal of the Electron Devices Society, 2014, 2, 123-127.	1.2	7
283	$\hat{\rho}/16$ spaced single RF chain MIMO antenna using low-power CMOS switches. , 2015, , .		7
284	An integrated time register and arithmetic circuit with combined operation for time-domain signal processing. , 2015, , .		7
285	A Separate Extraction Method for Asymmetric Source and Drain Resistances Using Frequency-Dispersive C-V Characteristics in Exfoliated MoS ₂ FET. IEEE Electron Device Letters, 2016, 37, 231-233.	2.2	7
286	A 21%-Jitter-Improved Self-Aligned Dividerless Injection-Locked PLL With a VCO Control Voltage Ripple-Compensated Phase Detector. IEEE Transactions on Circuits and Systems II: Express Briefs, 2016, 63, 733-737.	2.2	7
287	Characterization of intrinsic subgap density-of-states in exfoliated MoS ₂ FETs using a multi-frequency capacitance-conductance technique. AIP Advances, 2017, 7, .	0.6	7
288	Feasibility Study of Extended-Gate-Type Silicon Nanowire Field-Effect Transistors for Neural Recording. Sensors, 2017, 17, 705.	2.1	7

#	ARTICLE	IF	CITATIONS
289	Enhanced transconductance in a double-gate graphene field-effect transistor. <i>Solid-State Electronics</i> , 2018, 141, 65-68.	0.8	7
290	Localized Electrothermal Annealing with Nanowatt Power for a Silicon Nanowire Field-Effect Transistor. <i>ACS Applied Materials & Interfaces</i> , 2018, 10, 4838-4843.	4.0	7
291	Electrothermal Annealing to Enhance the Electrical Performance of an Exfoliated MoS ₂ Field-Effect Transistor. <i>IEEE Electron Device Letters</i> , 2018, , 1-1.	2.2	7
292	Carbon Nanotube Based $\hat{\Gamma}^3$ Ray Detector. <i>ACS Sensors</i> , 2019, 4, 1097-1102.	4.0	7
293	Effect of OFF-State Stress on Gate-Induced Drain Leakage by Interface Traps in Buried-Gate FETs. <i>IEEE Transactions on Electron Devices</i> , 2019, 66, 5126-5132.	1.6	7
294	A Study of High-Temperature Effects on an Asymmetrically Doped Vertical Pillar-Type Field-Effect Transistor. <i>IEEE Nanotechnology Magazine</i> , 2020, 19, 52-55.	1.1	7
295	Triboelectric nanogenerator for a repairable transistor with self-powered electro-thermal annealing. <i>Nano Energy</i> , 2020, 76, 105000.	8.2	7
296	3D Stackable Broadband Photoresponsive InGaAs Biristor Neuron for a Neuromorphic Visual System with Near 1 V Operation. , 2021, , .		7
297	A Junctionless Single Transistor Neuron With Vertically Stacked Multiple Nanowires for Highly Scalable Neuromorphic Hardware. <i>IEEE Transactions on Electron Devices</i> , 2022, 69, 3142-3146.	1.6	7
298	Spacer FinFET: nano-scale CMOS technology for the terabit era. , 0, , .		6
299	Body effects in tri-gate bulk FinFETs for DTMOS. , 2006, , .		6
300	Quasi 3-D Velocity Saturation Model for Multiple-Gate MOSFETs. <i>IEEE Transactions on Electron Devices</i> , 2007, 54, 1165-1170.	1.6	6
301	An Optically Assisted Program Method for Capacitorless 1T-DRAM. <i>IEEE Transactions on Electron Devices</i> , 2010, 57, 1714-1718.	1.6	6
302	Comprehensive study of a detection mechanism and optimization strategies to improve sensitivity in a nanogap-embedded biotransistor. <i>Journal of Applied Physics</i> , 2010, 107, 114705.	1.1	6
303	A 9.15mW 0.22mm ² 10b 204MS/s pipelined SAR ADC in 65nm CMOS. , 2010, , .		6
304	Fin Width $(W_{m\text{fin}})$ Dependence of Programming Characteristics on a Dopant-Segregated Schottky-Barrier (DSSB) FinFET SONOS Device for a NOR-Type Flash Memory Device. <i>IEEE Electron Device Letters</i> , 2010, 31, 71-73.	2.2	6
305	Adhesion Force Change by Electrowetting on a Polymer Microlens Array. <i>Journal of Adhesion Science and Technology</i> , 2012, 26, 2079-2086.	1.4	6
306	An electrostatic micromechanical biosensor for electrical detection of label-free DNA. <i>Applied Physics Letters</i> , 2012, 100, 163701.	1.5	6

#	ARTICLE	IF	CITATIONS
307	Impact of crystalline damage on a vertically integrated junctionless nanowire transistor. Applied Physics Letters, 2016, 109, .	1.5	6
308	Comprehensive Study on the Relation Between Low-Frequency Noise and Asymmetric Parasitic Resistances in a Vertical Pillar-Type FET. IEEE Electron Device Letters, 2017, 38, 1008-1011.	2.2	6
309	LF Noise Analysis for Trap Recovery in Gate Oxides Using Built-In Joule Heater. IEEE Transactions on Electron Devices, 2017, 64, 5081-5086.	1.6	6
310	A Low-Power Continuous-Time Delta-Sigma Modulator Using a Resonant Single Op-Amp Third-Order Loop Filter. IEEE Transactions on Circuits and Systems II: Express Briefs, 2018, 65, 854-858.	2.2	6
311	Electro-Thermal Erasing at 10^4 -Fold Faster Speeds in Charge-Trap Flash Memory. IEEE Electron Device Letters, 2019, 40, 196-199.	2.2	6
312	Extraction of Interface Trap Density Through Synchronized Optical Charge Pumping in Gate-All-Around MOSFETs. IEEE Electron Device Letters, 2020, 41, 1629-1632.	2.2	6
313	Low-Frequency Noise Characteristics Under the OFF-State Stress. IEEE Transactions on Electron Devices, 2020, 67, 4366-4371.	1.6	6
314	Investigation of Leaky Characteristic in a Single-Transistor-Based Leaky Integrate-and-Fire Neuron. IEEE Transactions on Electron Devices, 2021, 68, 5912-5915.	1.6	6
315	A Vertical Silicon Nanowire Based Single Transistor Neuron with Excitatory, Inhibitory, and Myelination Functions for Highly Scalable Neuromorphic Hardware. Small, 2021, 17, e2103775.	5.2	6
316	Refinement of Unified Random Access Memory. IEEE Transactions on Electron Devices, 2009, 56, 601-608.	1.6	5
317	Low-Cost and Highly Heat Controllable Capacitorless PiFET (Partially Insulated FET) 1T DRAM for Embedded Memory. IEEE Nanotechnology Magazine, 2009, 8, 100-105.	1.1	5
318	Interface-Trap Analysis by an Optically Assisted Charge-Pumping Technique in a Floating-Body Device. IEEE Electron Device Letters, 2011, 32, 84-86.	2.2	5
319	Multi-layer nanogap array for high-performance SERS substrate. Nanotechnology, 2011, 22, 235303.	1.3	5
320	20-MHz bandwidth continuous-time delta-sigma modulator for EPWM transmitter. , 2012, , .		5
321	Loop stability compensation technique for continuous-time common-mode feedback circuits. , 2015, , .		5
322	Threshold Voltage Tuning Technique in Gate-All-Around MOSFETs by Utilizing Gate Electrode With Potential Distribution. IEEE Electron Device Letters, 2016, 37, 1391-1394.	2.2	5
323	Schottky Tunneling Effects in a Tunnel FET. IEEE Transactions on Electron Devices, 2017, 64, 5223-5229.	1.6	5
324	Impact of Post-Metal Annealing With Deuterium or Nitrogen for Curing a Gate Dielectric Using Joule Heat Driven by Punch-Through Current. IEEE Electron Device Letters, 2021, 42, 276-279.	2.2	5

#	ARTICLE	IF	CITATIONS
325	One Bristor-Two Transistor (1B2T) Neuron With Reduced Output Voltage and Pulsewidth for Energy-Efficient Neuromorphic Hardware. IEEE Transactions on Electron Devices, 2021, 68, 430-433.	1.6	5
326	Curing of 1-Transistor-DRAM by Joule Heat From Punch-Through Current. IEEE Electron Device Letters, 2022, 43, 370-373.	2.2	5
327	Threshold voltage shift by quantum confinement in ultra-thin body device. , 0, , .		4
328	Nanostructured Electrodes for Improved Neural Recording. Materials Research Society Symposia Proceedings, 2002, 729, 4111.	0.1	4
329	Batch Fabrication of Nanopillars for Autonomous Nanofluidic SERS Arrays. Materials Research Society Symposia Proceedings, 2002, 729, 491.	0.1	4
330	Investigation of the source-side injection characteristic of a dopant-segregated Schottky barrier metal-oxide-semiconductor field-effect-transistor. Applied Physics Letters, 2009, 95, 063508.	1.5	4
331	Resistive-Memory Embedded Unified RAM (R-URAM). IEEE Transactions on Electron Devices, 2009, 56, 2670-2674.	1.6	4
332	P-Channel Nonvolatile Flash Memory With a Dopant-Segregated Schottky-Barrier Source/Drain. IEEE Transactions on Electron Devices, 2010, 57, 1737-1742.	1.6	4
333	Optically Assisted Charge Pumping on Floating-Body FETs. IEEE Electron Device Letters, 2010, 31, 1365-1367.	2.2	4
334	Liquid gate dielectric field effect transistor for a radiation nose. Sensors and Actuators A: Physical, 2012, 182, 1-5.	2.0	4
335	Experimental study on quantum mechanical effect for insensitivity of threshold voltage against temperature variation in strained SOI MOSFETs. , 2015, , .		4
336	Nanogap Embedded Transistor for Investigation of Charge Properties in DNA. IEEE Nanotechnology Magazine, 2016, 15, 188-192.	1.1	4
337	A SONOS device with a separated charge trapping layer for improvement of charge injection. AIP Advances, 2017, 7, .	0.6	4
338	A frequency reconfigurable slot dipole antenna using surface PIN diodes. , 2017, , .		4
339	On-Chip Curing by Microwave for Long Term Usage of Electronic Devices in Harsh Environments. Scientific Reports, 2018, 8, 14953.	1.6	4
340	A 0.65-V, 11.2-Gb/s Power Noise Tolerant Source-Synchronous Injection-Locked Receiver With Direct DTLB DFE. IEEE Transactions on Circuits and Systems II: Express Briefs, 2018, 65, 1564-1568.	2.2	4
341	A Comparative Study of the Curing Effects of Local and Global Thermal Annealing on a FinFET. IEEE Journal of the Electron Devices Society, 2019, 7, 954-958.	1.2	4
342	A Strategy for Optimizing Low Operating Voltage in a Silicon Bristor. IEEE Nanotechnology Magazine, 2020, 19, 5-10.	1.1	4

#	ARTICLE	IF	CITATIONS
343	Low-Power True Random Number Generator Based on Randomly Distributed Carbon Nanotube Networks. IEEE Access, 2021, 9, 91341-91346.	2.6	4
344	Improved Self-Curing Effect in a MOSFET With Gate Biasing. IEEE Electron Device Letters, 2021, 42, 1731-1734.	2.2	4
345	Regression Model-Based AMS Circuit Optimization Technique Utilizing Parameterized Operating Condition. Electronics (Switzerland), 2022, 11, 408.	1.8	4
346	CMOS Ternary Logic With a Biristor Threshold Switch for Low Static Power Consumption. IEEE Electron Device Letters, 2022, 43, 1005-1008.	2.2	4
347	Mimicking biological synaptic plasticity with a leaky charge-trap FinFET. Journal of Materials Chemistry C, 2022, 10, 9961-9967.	2.7	4
348	Synaptic Segmented Transistor with Improved Linearity by Schottky Junctions and Accelerated Speed by Double-Layered Nitride. ACS Applied Materials & Interfaces, 2022, 14, 32261-32269.	4.0	4
349	Sub-lithographic patterning technology for nanowire model catalysts and DNA label-free hybridization detection. , 2003, , .		3
350	Improvement of the Sensing Window on a Capacitorless 1T-DRAM of a FinFET-Based Unified RAM. IEEE Transactions on Electron Devices, 2009, 56, 3228-3231.	1.6	3
351	Geometric effects of nanocrystals in nonvolatile memory using block copolymer nanotemplate. Solid-State Electronics, 2009, 53, 640-643.	0.8	3
352	Analysis of Trapped Charges in Dopant-Segregated Schottky Barrier-Embedded FinFET SONOS Devices. IEEE Electron Device Letters, 2009, 30, 1084-1086.	2.2	3
353	A 2.85mW 0.12mm ² ; 1.0V 11-bit 20-MS/s algorithmic ADC in 65nm CMOS. , 2009, , .		3
354	A new approach to cell size scaling with a multi-dual cell and a buffer/background programming of unified RAM. Microelectronic Engineering, 2010, 87, 135-138.	1.1	3
355	A Bendable-Channel FinFET for Logic Application. IEEE Electron Device Letters, 2010, 31, 624-626.	2.2	3
356	A fully-microfabricated SWCNT film strain sensor. Journal of the Korean Physical Society, 2012, 61, 1656-1659.	0.3	3
357	Investigation of gate length and fringing field effects for program and erase efficiency in gate-all-around SONOS memory cells. Solid-State Electronics, 2013, 79, 7-10.	0.8	3
358	Flexible high-performance nonvolatile memory by transferring GAA silicon nanowire SONOS onto a plastic substrate. , 2014, , .		3
359	Optimization of the intrinsic length of a PIN diode for a reconfigurable antenna. , 2016, , .		3
360	Ultra-low power hydrogen sensor by suspended and palladium coated silicon nanowire. , 2017, , .		3

#	ARTICLE	IF	CITATIONS
361	Power reduction for recovery of a FinFET by electrothermal annealing. Solid-State Electronics, 2019, 151, 6-10.	0.8	3
362	Multi-functional logic circuits composed of ultra-thin electrolyte-gated transistors with wafer-scale integration. Journal of Materials Chemistry C, 2021, 9, 7222-7227.	2.7	3
363	Hybrid Gate Dielectric of MoS ₂ Transistors for Enhanced Photo-Electronic Stability. Advanced Materials Interfaces, 2021, 8, 2100599.	1.9	3
364	Dielectric Detection Using Biochemical Assays. Biological and Medical Physics Series, 2013, , 97-123.	0.3	3
365	Rapid prototyping of microwave metasurfaces by ink-jet printing on polyester (PET) transparencies. Flexible and Printed Electronics, 2020, 5, 045003.	1.5	3
366	Power Reduction in Punch-Through Current-Based Electro-Thermal Annealing in Gate-All-Around FETs. Micromachines, 2022, 13, 124.	1.4	3
367	Mnemonic-opto-synaptic transistor for in-sensor vision system. Scientific Reports, 2022, 12, 1818.	1.6	3
368	Doping-Free Nanoscale Complementary Carbon-Nanotube Field-Effect Transistors with DNA-Templated Molecular Lithography. Small, 2008, 4, 1959-1963.	5.2	2
369	Integrated current readout circuit and DMFET array for label-free detection of cancer marker. , 2008, , .		2
370	A Highly Flexible Superhydrophobic Microlens Array with Small Contact Angle Hysteresis for Droplet-Based Microfluidics. , 2009, , .		2
371	Detection of a Nanoscale Hot Spot by Hot Carriers in a Poly-Si TFT Using Polydiacetylene-Based Thermoresponsive Fluorometry. IEEE Transactions on Electron Devices, 2011, 58, 1570-1574.	1.6	2
372	A New Charge-Pumping Technique for a Double-Gated SOI MOSFET Using Pulsed Drain Current Transients. IEEE Transactions on Electron Devices, 2012, 59, 241-246.	1.6	2
373	Advanced Class-S transmitter with tri-level delta-sigma modulator. , 2013, , .		2
374	Design and measurement of 500-MS/s ΣΔ modulator with half-delayed return-to-zero feedback DAC. , 2014, , .		2
375	Temperature measurement of Joule heated silicon micro/nanowires using selectively decorated quantum dots. Nanotechnology, 2016, 27, 505705.	1.3	2
376	Investigation of Border Trap Characteristics in the AlON/GeO ₂ /Ge Gate Stacks. IEEE Transactions on Electron Devices, 2017, 64, 3998-4001.	1.6	2
377	Sanitization of Data in Nanoscale Flash Memory by Thermal Erasing and Reuse of Storage. Physica Status Solidi (A) Applications and Materials Science, 2018, 215, 1800194.	0.8	2
378	A 12 Gb/s 1.59 mW/Gb/s Input-Data-Jitter-Tolerant Injection-Type CDR With Super-Harmonic Injection-Locking in 65-nm CMOS. IEEE Transactions on Circuits and Systems II: Express Briefs, 2019, 66, 1972-1976.	2.2	2

#	ARTICLE	IF	CITATIONS
379	Ternary logic decoder using independently controlled double-gate Si-NW MOSFETs. Scientific Reports, 2021, 11, 13018.	1.6	2
380	Gateless and Capacitorless Germanium Biristor with a Vertical Pillar Structure. Micromachines, 2021, 12, 899.	1.4	2
381	A Low-Power Class-C Voltage-Controlled Oscillator With Robust Start-Up and Compact High-Q Capacitor Array. IEEE Transactions on Circuits and Systems II: Express Briefs, 2022, 69, 819-823.	2.2	2
382	Reliability Improvement of Gate-All-Around SONOS Memory by Joule Heat From Gate-Induced Drain Leakage Current. IEEE Transactions on Electron Devices, 2022, 69, 115-119.	1.6	2
383	Concealable Oscillation-Based Physical Unclonable Function With a Single-Transistor Latch. IEEE Electron Device Letters, 2022, 43, 1359-1362.	2.2	2
384	Tunable and Reconfigurable Logic Gates With Electrolyte-Gated Transistor Array Co-Integrated With Neuromorphic Synapses. IEEE Transactions on Electron Devices, 2022, 69, 4231-4235.	1.6	2
385	A comprehensive modeling of dynamic negative-bias temperature instability in PMOS body-tied FinFETs. IEEE Electron Device Letters, 2006, 27, 281-283.	2.2	1
386	Selective oxidation fin channel MOSFET for source/drain series resistance reduction. , 2006, , .		1
387	A Gate-Dielectric-Last Process via Photosolidification of Liquid Resin. IEEE Electron Device Letters, 2012, 33, 746-748.	2.2	1
388	Generation method of a driving signal for a dual-mode supply modulator in a 3-level envelope delta-sigma modulator transmitter. , 2014, , .		1
389	Compensation technique for time alignment of envelope and phase paths in an envelope delta-sigma modulator. IEICE Electronics Express, 2015, 12, 20150372-20150372.	0.3	1
390	First demonstration of a wrap-gated CNT-FET with vertically-suspended channels. , 2016, , .		1
391	Triboelectric energy harvester with an ultra-thin tribo-dielectric layer by initiated CVD and investigation of underlying physics in the triboelectricity. , 2016, , .		1
392	Triboelectrification driven fin-fact (flip-flop actuated channel transistor) for security application. , 2017, , .		1
393	Editors' Choiceâ€”Vertically Integrated Nanowire-Based Zero-Capacitor Dynamic Random Access Memory. ECS Journal of Solid State Science and Technology, 2017, 6, Q1-Q5.	0.9	1
394	Improved Technique for Extraction of Effective Mobility by Considering Gate Bias-Dependent Inversion Charges in a Floating-Body Si/SiGe pMOSFET. Journal of Nanoscience and Nanotechnology, 2017, 17, 3247-3250.	0.9	1
395	Analysis of damage curing in a MOSFET with joule heat generated by forward junction current at the source and drain. Microelectronics Reliability, 2020, 104, 113548.	0.9	1
396	Reconfigurable Beamforming Silicon Plasma Antenna with Vertical PIN Diode Array. Advanced Electronic Materials, 2020, 6, 2000257.	2.6	1

#	ARTICLE	IF	CITATIONS
397	Off-state leakage in MOSFET considering source/drain extension regions. Semiconductor Science and Technology, 2021, 36, 085018.	1.0	1
398	Lateral profiling of gate dielectric damage by off-state stress and positive-bias temperature instability. Microelectronics Reliability, 2021, 127, 114383.	0.9	1
399	A Multiple-€State Ion Synaptic Transistor Applicable to Abnormal Car Detection with Transfer Learning. Advanced Intelligent Systems, 0, , 2100231.	3.3	1
400	Configurable 3D nanoscale high aspect ratio pillars for surface-enhanced Raman spectroscopy. , 0, , .		0
401	High Performance Power MOSFETs with Strained-Si Channel. , 0, , .		0
402	Nonvolatile memory characteristics of NMOSFET with silver nanocrystals synthesized by thermal decomposition process. , 2006, , .		0
403	A thermally actuated organic display device using thermo-chromatic polymer composite film with self-aligned patterns. Proceedings of the IEEE International Conference on Micro Electro Mechanical Systems (MEMS), 2008, , .	0.0	0
404	Characterization of current injection mechanism in Schottky-barrier metal-oxide-semiconductor field-effect transistors. Applied Physics Letters, 2009, 95, .	1.5	0
405	Densely-Packed Microbowl Array with Balanced Dielectrophoretic Forces for Single-Cell Microarray. Materials Research Society Symposia Proceedings, 2009, 1222, 1.	0.1	0
406	Macromol. Rapid Commun. 3/2010. Macromolecular Rapid Communications, 2010, 31, .	2.0	0
407	Electrostatic switching biosensor - a novel label-free DNA detection using an electrode charging technique. , 2011, , .		0
408	Microfabrication of spray-coated SWCNT film strain gauges. , 2011, , .		0
409	Back Cover: Transfer of functional memory devices to any substrate (Phys. Status Solidi RRL 5/2013). Physica Status Solidi - Rapid Research Letters, 2013, 7, .	1.2	0
410	A CMOS advanced class-S transmitter architecture based on tri-level envelope encoding. , 2014, , .		0
411	Output enhancement of triboelectric energy harvester by micro-porous triboelectric layer. , 2015, , .		0
412	An optimum strategy for the low voltage operation of the mechanical switch. , 2015, , .		0
413	A novel triboelectric nanogenerator with high performance and long duration time of sinusoidal current generation. , 2017, , .		0
414	Advanced characterization technique for the extraction of intrinsic effective mobility in ultra-thin-body strained SOI MOSFETs. , 2017, , .		0

#	ARTICLE	IF	CITATIONS
415	Reply to Comments by Ortiz-Conde <i>et al.</i> . IEEE Transactions on Electron Devices, 2018, 65, 4022-4024.	1.6	0
416	Data Sanitization of SRAM by Thermal Distortion. IEEE Transactions on Electron Devices, 2021, 68, 3706-3710.	1.6	0
417	Fabrication of Nanowires and Their Applications. , 2014, , 89-128.		0
418	Self-Heating Effects in 3-D Vertical-NAND (V-NAND) Flash Memory. IEEE Transactions on Electron Devices, 2020, 67, 5505-5510.	1.6	0
419	Ultra-fast data sanitization of SRAM by back-biasing to resist a cold boot attack. Scientific Reports, 2022, 12, 35.	1.6	0
420	A Steep-Slope Phenomenon by Gate Charge Pumping in a MOSFET. IEEE Electron Device Letters, 2022, 43, 521-524.	2.2	0
421	Extremely scaled 3-dimensional multiple-gate technologies for terabit era. Journal of Nanoscience and Nanotechnology, 2007, 7, 4126-30.	0.9	0