

Soumyasanta Laha

List of Publications by Year in descending order

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26
papers

285
citations

1937685

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all docs

26
docs citations

26
times ranked

256
citing authors

#	ARTICLE	IF	CITATIONS
1	Compact Instrumentation for Accurate Detection and Measurement of Glucose Concentration Using Photoacoustic Spectroscopy. IEEE Access, 2022, 10, 31885-31895.	4.2	3
2	Exploiting Wireless Technology for Energy-Efficient Accelerators With Multiple Dataflows and Precision. IEEE Transactions on Circuits and Systems I: Regular Papers, 2022, 69, 2742-2755.	5.4	2
3	WiNN: Wireless Interconnect based Neural Network Accelerator. , 2021, , .		2
4	Sustainability in Network-on-Chips by Exploring Heterogeneity in Emerging Technologies. IEEE Transactions on Sustainable Computing, 2019, 4, 293-307.	3.1	6
5	Sub-THz Tunable Push-Push Oscillators with FinFETs for Wireless NoCs. , 2018, , .		0
6	A Compact Continuous non-Invasive Glucose Monitoring System with Phase-Sensitive Front End. , 2018, , .		3
7	Scalable Power-Efficient Kilo-Core Photonic-Wireless NoC Architectures. , 2018, , .		5
8	mm-Wave tunable colpitts oscillators based on FinFETs. , 2017, , .		1
9	Wireless data and power transfer of an optogenetic implantable visual cortex stimulator. , 2015, 2015, 8006-9.		7
10	Kilo-core Wireless Network-on-Chips (NoCs) Architectures. , 2015, , .		7
11	A New Frontier in Ultralow Power Wireless Links: Network-on-Chip and Chip-to-Chip Interconnects. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2015, 34, 186-198.	2.7	62
12	A-WiNoC: Adaptive Wireless Network-on-Chip Architecture for Chip Multiprocessors. IEEE Transactions on Parallel and Distributed Systems, 2015, 26, 3289-3302.	5.6	66
13	LC oscillators in nanoscale DG-MOSFETs. , 2014, , .		0
14	Energy-efficient adaptive wireless NoCs architecture. , 2013, , .		18
15	Bias optimization of 2.4 GHz double gate MOSFET RF mixer. Analog Integrated Circuits and Signal Processing, 2013, 77, 529-537.	1.4	4
16	On ultra-short wireless interconnects for NoCs and SoCs: Bridging the ‘THz Gap’. , 2013, , .		8
17	Dead zone free area efficient Charge Pump Phase Frequency Detector in nanoscale DG-MOSFET. , 2013, , .		1
18	A 60 GHz tunable LNA in 32 nm double gate MOSFET for a wireless NoC architecture. , 2013, , .		0

#	ARTICLE	IF	CITATIONS
19	Radio Frequency IC Design with Nanoscale DG-MOSFETs. , 2013, , .		0
20	Optimum biasing and design of high performance double gate MOSFET RF mixers. , 2012, , .		1
21	Energy efficient modulation for a wireless network-on-chip architecture. , 2012, , .		5
22	Evaluation and performance analysis of energy efficient wireless NoC architecture. , 2012, , .		2
23	60 GHz OOK Transmitter in 32 nm DG FinFET technology. , 2012, , .		5
24	Wireless networks-on-chips: architecture, wireless channel, and devices. IEEE Wireless Communications, 2012, 19, 58-65.	9.0	72
25	Double gate MOSFET based efficient wide band tunable power amplifiers. , 2012, , .		3
26	Ultra-compact low-power ICO/VCO circuits with double gate MOSFETs. , 2011, , .		2