Kang-Yoon Lee

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185
papers

1,190
citations

18
papers

1,668
ext. papers

1,668
avg, IF

18
papers

25
g-index

4.74
L-index

#	Paper	IF	Citations
185	A Design of a Wireless Power Receiving Unit With a High-Efficiency 6.78-MHz Active Rectifier Using Shared DLLs for Magnetic-Resonant A4 WP Applications. <i>IEEE Transactions on Power Electronics</i> , 2016 , 31, 4484-4498	7.2	51
184	Circularly Polarized Spidron Fractal Dielectric Resonator Antenna. <i>IEEE Antennas and Wireless Propagation Letters</i> , 2015 , 14, 1806-1809	3.8	49
183	Octave Bandwidth Doherty Power Amplifier Using Multiple Resonance Circuit for the Peaking Amplifier. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , 2019 , 66, 583-593	3.9	43
182	Circularly Polarized Semi-Eccentric Annular Dielectric Resonator Antenna for X-Band Applications. <i>IEEE Antennas and Wireless Propagation Letters</i> , 2015 , 14, 1810-1813	3.8	34
181	Design of a High Efficiency DCDC Buck Converter With Two-Step Digital PWM and Low Power Self-Tracking Zero Current Detector for IoT Applications. <i>IEEE Transactions on Power Electronics</i> , 2018 , 33, 1428-1439	7.2	33
180	A Design of a 92.4% Efficiency Triple Mode Control DCDC Buck Converter With Low Power Retention Mode and Adaptive Zero Current Detector for IoT/Wearable Applications. <i>IEEE Transactions on Power Electronics</i> , 2017 , 32, 6946-6960	7.2	33
179	A Sidelobe-Reduced, Four-Beam Array Antenna Fed by a Modified \$4times4\$ Butler Matrix for 5G Applications. <i>IEEE Transactions on Antennas and Propagation</i> , 2019 , 67, 4528-4536	4.9	32
178	. IEEE Transactions on Circuits and Systems II: Express Briefs, 2015 , 62, 533-537	3.5	32
177	CMOS Power Amplifier Integrated Circuit With Dual-Mode Supply Modulator for Mobile Terminals. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , 2016 , 63, 157-167	3.9	32
176	Doherty Power Amplifier Based on the Fundamental Current Ratio for Asymmetric cells. <i>IEEE Transactions on Microwave Theory and Techniques</i> , 2017 , 65, 4190-4197	4.1	29
175	Symmetric Three-Way Doherty Power Amplifier for High Efficiency and Linearity. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , 2017 , 64, 862-866	3.5	28
174	An \$L1\$ -Band Dual-Mode RF Receiver for GPS and Galileo in 0.18-\$mu {hbox{m}}\$ CMOS. <i>IEEE Transactions on Microwave Theory and Techniques</i> , 2009 , 57, 919-927	4.1	25
173	Comparison frequency doubling and charge pump matching techniques for dual-band /spl Delta//spl Sigma/ fractional-N frequency synthesizer. <i>IEEE Journal of Solid-State Circuits</i> , 2005 , 40, 2228	3- 2 2 2 36	22
172	Self-Calibrated Two-Point DeltaBigma Modulation Technique for RF Transmitters. <i>IEEE Transactions on Microwave Theory and Techniques</i> , 2010 , 58, 1748-1757	4.1	21
171	Highly Efficient Fully Integrated GaN-HEMT Doherty Power Amplifier Based on Compact Load Network. <i>IEEE Transactions on Microwave Theory and Techniques</i> , 2017 , 65, 5203-5211	4.1	20
170	Optimized Current of the Peaking Amplifier for Two-Stage Doherty Power Amplifier. <i>IEEE Transactions on Microwave Theory and Techniques</i> , 2017 , 65, 209-217	4.1	19
169	. IEEE Transactions on Power Electronics, 2019 , 34, 6803-6817	7.2	19

(2020-2020)

168	An Efficient Reconfigurable RF-DC Converter With Wide Input Power Range for RF Energy Harvesting. <i>IEEE Access</i> , 2020 , 8, 79310-79318	3.5	18
167	A Triple-Mode Wireless Power-Receiving Unit With 85.5% System Efficiency for A4WP, WPC, and PMA Applications. <i>IEEE Transactions on Power Electronics</i> , 2018 , 33, 3141-3156	7.2	17
166	A Wideband Circularly Polarized Pixelated Dielectric Resonator Antenna. Sensors, 2016, 16,	3.8	17
165	A Wide-Locking-Range Dual Injection-Locked Frequency Divider With an Automatic Frequency Calibration Loop in 65-nm CMOS. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , 2015 , 62, 327-331	3.5	15
164	Energy-efficient switching scheme for SAR ADC using zero-energy dual capacitor switching. <i>Analog Integrated Circuits and Signal Processing</i> , 2018 , 94, 317-322	1.2	14
163	6¶8 GHz GaAs pHEMT Broadband Power Amplifier Based on Dual-Frequency Selective Impedance Matching Technique. <i>IEEE Access</i> , 2019 , 7, 66275-66280	3.5	13
162	A CMOS RF Energy Harvester With 47% Peak Efficiency Using Internal Threshold Voltage Compensation. <i>IEEE Microwave and Wireless Components Letters</i> , 2019 , 29, 415-417	2.6	13
161	A 3.9 mW Bluetooth Low-Energy Transmitter Using All-Digital PLL-Based Direct FSK Modulation in 55 nm CMOS. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , 2018 , 65, 3037-3048	3.9	12
160	Dual Circularly-Polarized Spidron Fractal Slot Antenna. <i>Electromagnetics</i> , 2017 , 37, 40-48	0.8	11
159	Fermi-Level Pinning Free High-Performance 2D CMOS Inverter Fabricated with Van Der Waals Bottom Contacts. <i>Advanced Electronic Materials</i> , 2021 , 7, 2001212	6.4	11
158	Compact Load Network for GaN-HEMT Doherty Power Amplifier IC Using Left-Handed and Right-Handed Transmission Lines. <i>IEEE Microwave and Wireless Components Letters</i> , 2017 , 27, 293-295	2.6	10
157	A Highly Linear, Small-Area Analog Front End With Gain and Offset Compensation for Automotive Capacitive Pressure Sensors in 0.35- \$mu \$ m CMOS. <i>IEEE Sensors Journal</i> , 2015 , 15, 1967-1976	4	10
156	A Design of 8 fJ/Conversion-Step 10-bit 8MS/s Low Power Asynchronous SAR ADC for IEEE 802.15.1 IoT Sensor Based Applications. <i>IEEE Access</i> , 2020 , 8, 85869-85879	3.5	10
155	A Low-Power Multichannel Time-to-Digital Converter Using All-Digital Nested Delay-Locked Loops With 50-ps Resolution and High Throughput for LiDAR Sensors. <i>IEEE Transactions on Instrumentation and Measurement</i> , 2020 , 69, 9262-9271	5.2	10
154	Transmitter-Oriented Dual-Mode SWIPT With Deep-Learning-Based Adaptive Mode Switching for IoT Sensor Networks. <i>IEEE Internet of Things Journal</i> , 2020 , 7, 8979-8992	10.7	9
153	Single-Fed Circularly Polarized Dielectric Resonator Antenna With an Enhanced Axial Ratio Bandwidth and Enhanced Gain. <i>IEEE Access</i> , 2020 , 8, 41045-41052	3.5	9
152	. IEEE Antennas and Wireless Propagation Letters, 2020 , 19, 443-447	3.8	9
151	6.78 MHz Wireless Power Transmitter Based on a Reconfigurable Class E Power Amplifier for Multiple Device Charging. <i>IEEE Transactions on Power Electronics</i> , 2020 , 35, 5907-5917	7.2	9

150	84 dB DC-gain two-stage class-AB OTA. IET Circuits, Devices and Systems, 2019, 13, 614-621	1.1	8
149	Dual Mode SWIPT: Waveform Design and Transceiver Architecture with Adaptive Mode Switching Policy 2018 ,		8
148	Design of a 900 MHz Dual-Mode SWIPT for Low-Power IoT Devices. Sensors, 2019, 19,	3.8	8
147	An Ultra-Low-Power Super Regeneration Oscillator-Based Transceiver With 177-/spl mu/W Leakage-Compensated PLL and Automatic Quench Waveform Generator. <i>IEEE Transactions on Microwave Theory and Techniques</i> , 2013 , 61, 3381-3390	4.1	8
146	A Design of Low-Power 10-bit 1-MS/s Asynchronous SAR ADC for DSRC Application. <i>Electronics</i> (Switzerland), 2020 , 9, 1100	2.6	8
145	Retroreflective Transceiver Array Using a Novel Calibration Method Based on Optimum Phase Searching. <i>IEEE Transactions on Industrial Electronics</i> , 2021 , 68, 2510-2520	8.9	8
144	A 6-bit 4 MS/s, VCM-based sub-radix-2 SAR ADC with inverter type comparator. <i>Microelectronics Journal</i> , 2017 , 62, 120-125	1.8	7
143	Wide input range, high-efficiency magnetic resonant wireless power receiver. <i>International Journal of Electronics</i> , 2015 , 102, 326-344	1.2	7
142	Dual-Band Circularly Polarized Annular Slot Antenna With a Lumped Inductor for GPS Application. <i>IEEE Transactions on Antennas and Propagation</i> , 2020 , 68, 8197-8202	4.9	7
141	Design of a Low Power 10-b 8-MS/s Asynchronous SAR ADC with On-Chip Reference Voltage Generator. <i>Electronics (Switzerland)</i> , 2020 , 9, 872	2.6	7
140	A Design of Wide-Range and Low Phase Noise Linear Transconductance VCO with 193.76 dBc/Hz FoMT for mm-Wave 5G Transceivers. <i>Electronics (Switzerland)</i> , 2020 , 9, 935	2.6	7
139	X-band two-stage Doherty power amplifier based on pre-matched GaN-HEMTs. <i>IET Microwaves, Antennas and Propagation</i> , 2018 , 12, 179-184	1.6	7
138	5.8 GHz High-Efficiency RF-DC Converter Based on Common-Ground Multiple-Stack Structure. <i>Sensors</i> , 2019 , 19,	3.8	7
137	Vertical-Strip-Fed Broadband Circularly Polarized Dielectric Resonator Antenna. Sensors, 2017, 17,	3.8	7
136	Wideband Circularly Polarized Spidron Fractal Slot Antenna with an Embedded Patch. <i>International Journal of Antennas and Propagation</i> , 2017 , 2017, 1-7	1.2	7
135	LUT-Based Focal Beamforming System Using 2-D Adaptive Sequential Searching Algorithm for Microwave Power Transfer. <i>IEEE Access</i> , 2020 , 8, 196024-196033	3.5	7
134	98-dB Gain Class-AB OTA With 100 pF Load Capacitor in 180-nm Digital CMOS Process. <i>IEEE Access</i> , 2019 , 7, 17772-17779	3.5	7
133	Broadband InGaP/GaAs HBT Power Amplifier Integrated Circuit Using Cascode Structure and Optimized Shunt Inductor. <i>IEEE Transactions on Microwave Theory and Techniques</i> , 2019 , 67, 5090-5100	4.1	7

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132	High-efficiency rectifier (5.2 GHz) using a Class-F Dickson charge pump. <i>Microwave and Optical Technology Letters</i> , 2017 , 59, 3018-3023	1.2	6	
131	All-Digital Bandwidth Mismatch Calibration of TI-ADCs Based on Optimally Induced Minimization. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2020, 28, 1175-1184	2.6	6	
130	260- \$mu\$ W DCO With Constant Current Over PVT Variations Using FLL and Adjustable LDO. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , 2018 , 65, 739-743	3.5	6	
129	A Wide Input Range Buck-Boost DCDC Converter Using Hysteresis Triple-Mode Control Technique with Peak Efficiency of 94.8% for RF Energy Harvesting Applications. <i>Energies</i> , 2018 , 11, 1618	3.1	6	
128	Broadband Circularly Polarized Slot Antenna Loaded by a Multiple-Circular-Sector Patch. <i>Sensors</i> , 2018 , 18,	3.8	6	
127	A Wideband Circularly Polarized Antenna with a Multiple-Circular-Sector Dielectric Resonator. <i>Sensors</i> , 2016 , 16,	3.8	6	
126	A 15-W Quadruple-Mode Reconfigurable Bidirectional Wireless Power Transceiver With 95% System Efficiency for Wireless Charging Applications. <i>IEEE Transactions on Power Electronics</i> , 2021 , 36, 3814-3827	7.2	6	
125	Dual-Mode CMOS Power Amplifier Based on Load-Impedance Modulation. <i>IEEE Microwave and Wireless Components Letters</i> , 2018 , 28, 1041-1043	2.6	6	
124	A High-Efficient Wireless Power Receiver for Hybrid Energy-Harvesting Sources. <i>IEEE Transactions on Power Electronics</i> , 2021 , 36, 11148-11162	7.2	6	
123	Modeling Random Clock Jitter Effect of High-Speed Current-Steering NRZ and RZ DAC. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , 2018 , 65, 2832-2841	3.9	5	
122	A Highly Linear, AEC-Q100 Compliant Signal Conditioning IC for Automotive Piezo-Resistive Pressure Sensors. <i>IEEE Transactions on Industrial Electronics</i> , 2018 , 65, 7363-7373	8.9	5	
121	Improvement of RF Wireless Power Transmission Using a Circularly Polarized Retrodirective Antenna Array with EBG Structures. <i>Applied Sciences (Switzerland)</i> , 2018 , 8, 324	2.6	5	
120	Design of a Low-Power, Small-Area AEC-Q100-Compliant SENT Transmitter in Signal Conditioning IC for Automotive Pressure and Temperature Complex Sensors in 180 Nm CMOS Technology. <i>Sensors</i> , 2018 , 18,	3.8	5	
119	A 2.45 GHz High Efficiency CMOS RF Energy Harvester with Adaptive Path Control. <i>Electronics</i> (Switzerland), 2020 , 9, 1107	2.6	5	
118	Doherty Power Amplifier Based on Asymmetric Cells With Complex Combining Load. <i>IEEE Transactions on Microwave Theory and Techniques</i> , 2021 , 69, 2336-2344	4.1	5	
117	High-Efficiency Multilevel Multimode Dynamic Supply Switching Modulator for LTE Power Amplifier. <i>IEEE Transactions on Power Electronics</i> , 2021 , 36, 6967-6977	7.2	5	
116	Analysis of Received Power in RF Wireless Power Transfer System With Array Antennas. <i>IEEE Access</i> , 2021 , 1-1	3.5	5	
115	A 6-bit 4[MS/s 26fJ/conversion-step segmented SAR ADC with reduced switching energy for BLE. <i>International Journal of Circuit Theory and Applications</i> , 2018 , 46, 375-383	2	5	

114	A High-Efficiency Fast Transient COT Control DCDC Buck Converter With Current Reused Current Sensor. <i>IEEE Transactions on Power Electronics</i> , 2021 , 36, 9521-9535	7.2	5
113	High-Gain Waveguide-Fed Circularly Polarized Spidron Fractal Aperture Antenna. <i>Applied Sciences</i> (Switzerland), 2019 , 9, 691	2.6	4
112	Dual-mode supply modulator for CMOS envelope tracking power amplifier integrated circuit. <i>Microwave and Optical Technology Letters</i> , 2015 , 57, 1338-1343	1.2	4
111	A 0.33¶ GHz Open-Loop Duty Cycle Corrector With Digital Falling Edge Modulator. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , 2018 , 65, 1949-1953	3.5	4
110	A Design of Fast-Settling, Low-Power 4.19-MHz Real-Time Clock Generator With Temperature Compensation and 15-dB Noise Reduction. <i>IEEE Transactions on Very Large Scale Integration (VLSI) Systems</i> , 2018 , 26, 1151-1158	2.6	4
109	A low phase noise 30-GHz frequency synthesizer with linear transconductance VCO and dual-injection-locked frequency divider. <i>Analog Integrated Circuits and Signal Processing</i> , 2016 , 86, 365-3	3 7 6²	4
108	A design of wide input range, high efficiency rectifier for mobile wireless charging receiver 2014 ,		4
107	VHF/UHF broadband four-way power combiner/divider using 0 [®] hybrid and impedance transformer based on transmission lines. <i>IET Microwaves, Antennas and Propagation</i> , 2017 , 11, 1748-1753	1.6	4
106	Coupling-Shielded Inductor for High Isolation Between PA and LC-Based DCO. <i>IEEE Electron Device Letters</i> , 2017 , 38, 24-27	4.4	4
105	2.6 GHz GaN-HEMT Doherty power amplifier integrated circuit with 55.5% efficiency based on a compact load network 2017 ,		4
104	A High Performance Adaptive Digital LDO Regulator With Dithering and Dynamic Frequency Scaling for IoT Applications. <i>IEEE Access</i> , 2020 , 8, 132200-132211	3.5	4
103	Circularly Polarized Dielectric Resonator Antenna With Two Annular Vias. <i>IEEE Access</i> , 2021 , 9, 41123-4	13,258	4
102	GaN-HEMT asymmetric three-way Doherty power amplifier using GPD. <i>IET Microwaves, Antennas and Propagation</i> , 2018 , 12, 2115-2121	1.6	4
101	InGaP/GaAs HBT Broadband Power Amplifier IC with 54.3% Fractional Bandwidth Based on Cascode Structure 2019 ,		3
100	A High-Efficiency and Wide-Input Range RF Energy Harvester Using Multiple Rectenna and Adaptive Matching. <i>Energies</i> , 2020 , 13, 1023	3.1	3
99	Design of a capacitor-less LDO with high PSRR for RF energy harvesting applications 2017,		3
98	Design of Peak Efficiency of 85.3% WPC/PMA Wireless Power Receiver Using Synchronous Active Rectifier and Multi Feedback Low-Dropout Regulator. <i>Energies</i> , 2018 , 11, 479	3.1	3
97	Hybrid weighted bit flipping low density parity check decoding 2014 , 28, 82-92		3

96	A 4.18.2 GHz LC VCO using a vertical solenoid inductor in 0.13 th digital CMOS 2013,		3
95	Low power FSK transmitter using all-digital PLL for IEEE 802.15.4g application. <i>Analog Integrated Circuits and Signal Processing</i> , 2013 , 74, 599-612	1.2	3
94	A 10-bit 1 MS/s segmented Dual-Sampling SAR ADC with reduced switching energy. <i>Microelectronics Journal</i> , 2017 , 70, 89-96	1.8	3
93	Efficiency enhanced CMOS digitally controlled dynamic bias switching power amplifier for LTE. <i>Microwave and Optical Technology Letters</i> , 2015 , 57, 2315-2321	1.2	3
92	A low power DLL based clock and data recovery circuit with wide range anti-harmonic lock. <i>Analog Integrated Circuits and Signal Processing</i> , 2013 , 74, 355-364	1.2	3
91	A design of transceiver for 13.56 MHz RFID reader using the peak detector with automatic reference voltage generator and voltage limiter 2010 ,		3
90	An Ultra-Low Power, Adaptive All-Digital Frequency-Locked Loop With Gain Estimation and Constant Current DCO. <i>IEEE Access</i> , 2020 , 8, 97215-97230	3.5	3
89	A 12 bit 750 kS/s 0.13 mW Dual-sampling SAR ADC. <i>Journal of Semiconductor Technology and Science</i> , 2016 , 16, 760-770	1.5	3
88	Design of 0.68-mW LC-based Digitally Controlled Oscillator (DCO) for Bluetooth Low Energy (BLE) Transceiver. <i>Journal of Semiconductor Technology and Science</i> , 2017 , 17, 611-620	1.5	3
87	A Highly Reliable, 5.8 GHz DSRC Wake-Up Receiver with an Intelligent Digital Controller for an ETC System. <i>Sensors</i> , 2020 , 20,	3.8	3
86	High-Efficiency Stacked Power Amplifier IC With 23% Fractional Bandwidth for Average Power Tracking Application. <i>IEEE Access</i> , 2019 , 7, 176658-176667	3.5	3
85	Robust Design of 3D-Printed 6fl8 GHz Double-Ridged TEM Horn Antenna. <i>Applied Sciences</i> (Switzerland), 2018 , 8, 1582	2.6	3
84	A broadband circularly polarized magneto-electric dipole array antenna for 5G millimeter-wave applications. <i>Applied Physics Letters</i> , 2021 , 119, 023503	3.4	3
83	Compact and High Gain 4 A Circularly Polarized Microstrip Patch Antenna Array for Next Generation Small Satellite. <i>Applied Sciences (Switzerland)</i> , 2021 , 11, 8869	2.6	3
82	Design of High Performance Hybrid Type Digital-Feedback Low Drop-Out Regulator Using SSCG Technique. <i>IEEE Access</i> , 2021 , 9, 28167-28176	3.5	3
81	An Inductive 2-D Position Detection IC With 99.8% Accuracy for Automotive EMR Gear Control System. <i>IEEE Transactions on Very Large Scale Integration (VLSI) Systems</i> , 2017 , 25, 1731-1741	2.6	2
80	Bandwidth-Enhanced Circularly Polarized Crescent-Shaped Slot Antenna via Circular-Patch Loading. <i>Applied Sciences (Switzerland)</i> , 2019 , 9, 1117	2.6	2
79	Low-power 10-bit SAR ADC using class-AB type amplifier for IoT applications 2017 ,		2

78	A design of wide input range triple-mode active rectifier with peak efficiency of 94.2 % and maximum output power of 8 W for wireless power receiver in 0.18 \(\bar{\pi} \)M BCD. <i>Analog Integrated Circuits and Signal Processing</i> , 2016 , 86, 255-265	1.2	2
77	A Design of Small Area, 0.95 mW, 612?1152 MHz Open Loop Injection-Locked Frequency Multiplier for IoT Sensor Applications. <i>Sensors</i> , 2018 , 18,	3.8	2
76	Scaled-down reference switching scheme for low-power SAR ADCs. <i>Analog Integrated Circuits and Signal Processing</i> , 2018 , 97, 143-148	1.2	2
75	A High Noise Immunity, 28 🛘 6-Channel Finger Touch Sensing IC Using OFDM and Frequency Translation Technique. <i>Sensors</i> , 2018 , 18,	3.8	2
74	A 3-D Meandered Probe-Fed Dual-Band Circularly Polarized Dielectric Resonator Antenna. <i>Sensors</i> , 2018 , 18,	3.8	2
73	Adaptive Mode Switching Algorithm for Dual Mode SWIPT with Duty Cycle Operation 2018,		2
72	A design of a 5.6 GHz frequency synthesizer with switched bias LIT VCO and low noise on-chip LDO regulator for 5G applications. <i>International Journal of Circuit Theory and Applications</i> , 2019 , 47, 1856-18	68	2
71	A high efficiency transmitter and receiver for magnetic resonant wireless battery charging system in 0.35 h BCD. <i>Analog Integrated Circuits and Signal Processing</i> , 2014 , 79, 57-72	1.2	2
70	Automatic power controlled, load compensated magnetic resonant wireless power transmitter. <i>International Journal of Electronics Letters</i> , 2014 , 2, 121-133	0.6	2
69	An IEEE 802.15.4g SUN FSK RF CMOS transceiver for Smart Grid and CEs 2013 ,		2
68	Circularly polarized CHANEL-logo antenna for GNSS applications. <i>Journal of Electromagnetic Waves and Applications</i> , 2017 , 31, 1434-1443	1.3	2
67	A Wide Input Range, High-Efficiency Multi-Mode Active Rectifier for Magnetic Resonant Wireless Power Transfer System. <i>IEICE Transactions on Electronics</i> , 2013 , E96.C, 102-107	0.4	2
66	A low power, wide range VCO with automatic amplitude calibration loop 2009 ,		2
65	A 1-GHz Tuning Range DCO with a 3.9 kHz Discrete Tuning Step for UWB Frequency Synthesizer. <i>IEICE Transactions on Electronics</i> , 2010 , E93-C, 770-776	0.4	2
64	Doherty Power Amplifier With Extended High-Efficiency Range Based on the Utilization of Multiple Output Power Back-Off Parameters. <i>IEEE Transactions on Microwave Theory and Techniques</i> , 2022 , 1-1	4.1	2
63	Dual-Mode Supply Modulator IC With an Adaptive Quiescent Current Controller for Its Linear Amplifier in LTE Mobile Power Amplifier. <i>IEEE Access</i> , 2021 , 1-1	3.5	2
62	A High Efficiency Active Rectifier for 6.78MHz Wireless Power Transfer Receiver with Bootstrapping Technique and All Digital Delay-Locked Loop. <i>IEIE Transactions on Smart Processing and Computing</i> , 2014 , 3, 410-415	1.2	2
61	Reconfigurable Hybrid Resonant Topology for Constant Current/Voltage Wireless Power Transfer of Electric Vehicles. <i>Electronics (Switzerland)</i> , 2020 , 9, 1323	2.6	2

60	Cavity-Backed Patch Filtenna for Harmonic Suppression. <i>IEEE Access</i> , 2020 , 8, 221580-221589	3.5	2
59	Optimized Broadband Load Network for Doherty Power Amplifier Based on Bandwidth Balancing. <i>IEEE Microwave and Wireless Components Letters</i> , 2021 , 31, 280-283	2.6	2
58	Dual-Band RF Wireless Power Transfer System with a Shared-Aperture Dual-Band Tx Array Antenna. <i>Energies</i> , 2021 , 14, 3803	3.1	2
57	A design of power managements IC with peak efficiency of 92.8 % step-up converter and peak efficiency of 93.8 % step-down converter for power transmitting unit of A4WP applications in 0.18 th BCD. <i>Analog Integrated Circuits and Signal Processing</i> , 2016 , 88, 115-125	1.2	2
56	A 15-W Triple-Mode Wireless Power Transmitting Unit With High System Efficiency Using Integrated Power Amplifier and DCDC Converter. <i>IEEE Transactions on Industrial Electronics</i> , 2021 , 68, 9574-9585	8.9	2
55	Mid-Range Wireless Power Transfer System for Various Types of Multiple Receivers Using Power Customized Resonator. <i>IEEE Access</i> , 2021 , 9, 45230-45241	3.5	2
54	5.8 GHz 4-Channel Beamforming Tx IC for Microwave Power Transfer. <i>IEEE Access</i> , 2021 , 9, 72316-7232	53.5	2
53	A Sigma-Delta ADC for Signal Conditioning IC of Automotive Piezo-Resistive Pressure Sensors with over 80 dB SNR. <i>Sensors</i> , 2018 , 18,	3.8	2
52	A 10- and 12-Bit Multi-Channel Hybrid Type Successive Approximation Register Analog-to-Digital Converter for Wireless Power Transfer System. <i>Energies</i> , 2018 , 11, 2673	3.1	2
51	40 dB-Isolation, 1.85 dB-Insertion Loss Full CMOS SPDT Switch with Body-Floating Technique and Ultra-Small Active Matching Network Using On-Chip Solenoid Inductor for BLE Applications. <i>Electronics (Switzerland)</i> , 2018 , 7, 297	2.6	2
50	A 2.4 GHz Power Receiver Embedded With a Low-Power Transmitter and PCE of 53.8%, for Wireless Charging of IoT/Wearable Devices. <i>IEEE Transactions on Microwave Theory and Techniques</i> , 2021 , 69, 43	1 5 -432	25 ²
49	A frame-based EM-simulation for design of LC oscillator with MoM capacitor banks. <i>International Journal of RF and Microwave Computer-Aided Engineering</i> , 2017 , 27, e21112	1.5	1
48	High speed DC-DC synchronous boost converter using type-III compensation for low power applications 2015 ,		1
47	Frequency Selective Degeneration for 6¶8 GHz GaAs pHEMT Broadband Power Amplifier Integrated Circuit. <i>Electronics (Switzerland)</i> , 2020 , 9, 1588	2.6	1
46	Scaled GaN-HEMT Large-Signal Model Based on EM Simulation. <i>Electronics (Switzerland)</i> , 2020 , 9, 632	2.6	1
45	A Design of 6.8 mW All Digital Delay Locked Loop With Digitally Controlled Dither Cancellation for TDC in Ranging Sensor. <i>IEEE Access</i> , 2020 , 8, 57722-57732	3.5	1
44	A design of ultra-low noise LDO using noise reduction network techniques 2017,		1
43	Buck DC-DC converter with PFM/PWM dual mode self-tracking zero current detector 2017 ,		1

42	Design of 36 dB IRR baseband analog for Bluetooth low energy 5.0 application in 55 nm CMOS 2017 ,		1
41	Low Power High Speed Dynamic Comparator 2018,		1
40	LabVIEW based modeling of SWIPT system using BPSK modulation 2018,		1
39	A 10-b 10MS/s SAR ADC with power and accuracy control of the comparator 2015 ,		1
38	A 84% tuning range, 4.3-GHz VCO with two-step negative-Gm tuning and amplitude calibration loop. <i>Analog Integrated Circuits and Signal Processing</i> , 2011 , 66, 441-447	1.2	1
37	A novel dead-time generation method of clock generator for resonant power transfer system 2010 ,		1
36	Low power multi-channel capacitive touch sensing unit using capacitor to time conversion method 2012 ,		1
35	A wide-range VCO with an automatic frequency, amplitude and gain calibration loop. <i>Analog Integrated Circuits and Signal Processing</i> , 2010 , 62, 91-98	1.2	1
34	A L1-band dual-mode RF receiver for GPS and Galileo in 0.18th CMOS 2008 ,		1
33	Automatic mode matching control loop design and its application to the mode matched MEMS gyroscope 2007 ,		1
32	A 5.8 GHz RF Receiver Front-End with 77.6 dB Dynamic Range AGC for a DSRC Transceiver. <i>IEEE Access</i> , 2022 , 1-1	3.5	1
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