

Jiajia Chen

List of Publications by Year in descending order

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#	ARTICLE	IF	CITATIONS
1	A New Input Grouping and Sharing Method to Design Low Complexity FFT Implementation. IEEE Transactions on Circuits and Systems II: Express Briefs, 2024, 71, 832-836.	3.0	0
2	A New Information Hiding Scheme Using Discrete Wavelet Transform at Physical Layer. , 2022, , .		0
3	A New Algorithm to Derive High Performance and Low Hardware Cost DCT for HEVC. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2022, 41, 5367-5379.	2.7	3
4	Micro-Doppler Signature-Based Detection, Classification, and Localization of Small UAV With Long Short-Term Memory Neural Network. IEEE Transactions on Geoscience and Remote Sensing, 2021, 59, 6285-6300.	6.3	26
5	A Demosaicing Algorithm Based on Local Directional Gradients for Polarization Image. , 2021, , .		1
6	A New Multi-Focus Image Fusion Algorithm and Its Efficient Implementation. IEEE Transactions on Circuits and Systems for Video Technology, 2020, 30, 1374-1384.	8.3	24
7	An Optimized Quantization Constraints Set for Image Restoration and its GPU Implementation. IEEE Transactions on Image Processing, 2020, 29, 6043-6053.	9.8	3
8	A New Polarization Image Demosaicing Algorithm by Exploiting Inter-Channel Correlations With Guided Filtering. IEEE Transactions on Image Processing, 2020, 29, 7076-7089.	9.8	26
9	A Novel Area-Power Efficient Design for Approximated Small-Point FFT Architecture. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2020, 39, 4816-4827.	2.7	14
10	Hardware Efficient Integer Discrete Cosine Transform for Efficient Image/Video Compression. IEEE Access, 2019, 7, 152635-152645.	4.2	10
11	Area- and Power-Efficient Nearly-Linear Phase Response IIR Filter by Iterative Convex Optimization. IEEE Access, 2019, 7, 22952-22965.	4.2	5
12	New Hardware and Power Efficient Sporadic Logarithmic Shifters for DSP Applications. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2018, 37, 896-900.	2.7	6
13	Indoor Sound Source Localization With Probabilistic Neural Network. IEEE Transactions on Industrial Electronics, 2018, 65, 6403-6413.	7.9	84
14	A New Accurate and Fast Homography Computation Algorithm for Sports and Traffic Video Analysis. IEEE Transactions on Circuits and Systems for Video Technology, 2018, 28, 2993-3006.	8.3	10
15	Tap Delay-and-Accumulate Cost Aware Coefficient Synthesis Algorithm for the Design of Area-Power Efficient FIR Filters. IEEE Transactions on Circuits and Systems I: Regular Papers, 2018, 65, 712-722.	5.4	13
16	A New Cost-Aware Sensitivity-Driven Algorithm for the Design of FIR Filters. IEEE Transactions on Circuits and Systems I: Regular Papers, 2017, 64, 1588-1598.	5.4	15
17	Low complexity and quasi-linear phase IIR filters design based on iterative convex optimization. , 2016, , .		2
18	A New Paradigm of Common Subexpression Elimination by Unification of Addition and Subtraction. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2016, 35, 1605-1617.	2.7	21

#	ARTICLE	IF	CITATIONS
19	Hypergraph Based Minimum Arborescence Algorithm for the Optimization and Reoptimization of Multiple Constant Multiplications. IEEE Transactions on Circuits and Systems I: Regular Papers, 2016, 63, 233-244.	5.4	16
20	New design for low complexity and low power partial programmable shifters. , 2015, , .		0
21	Novel Design Algorithm for Low Complexity Programmable FIR Filters Based on Extended Double Base Number System. IEEE Transactions on Circuits and Systems I: Regular Papers, 2015, 62, 224-233.	5.4	36
22	High-Level Synthesis Algorithm for the Design of Reconfigurable Constant Multiplier. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2009, 28, 1844-1856.	2.7	39